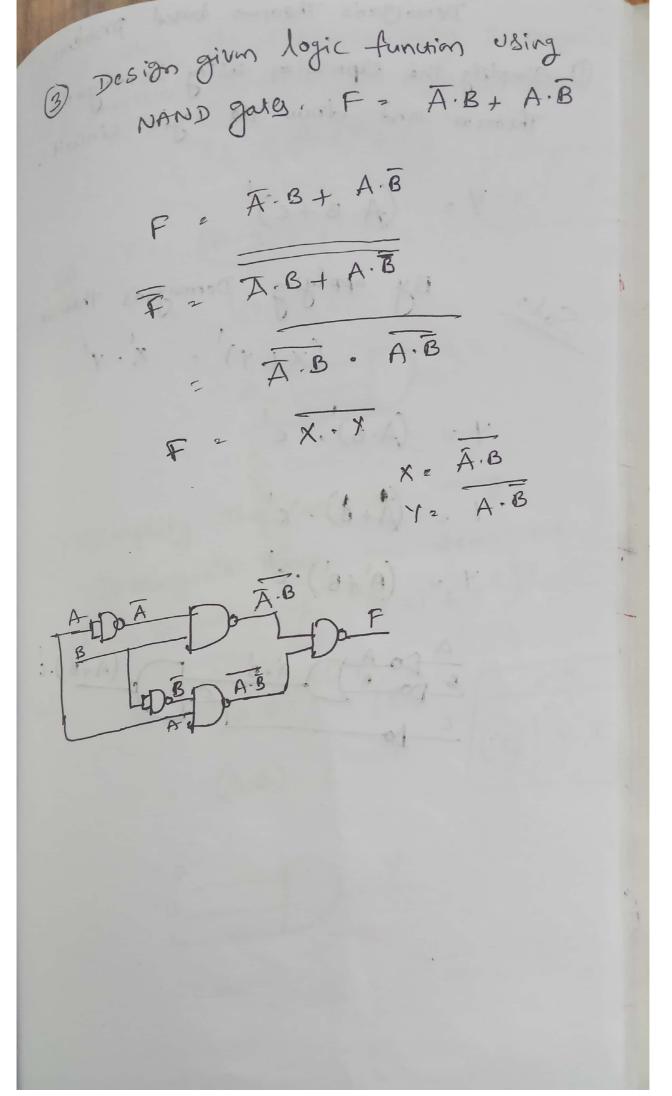
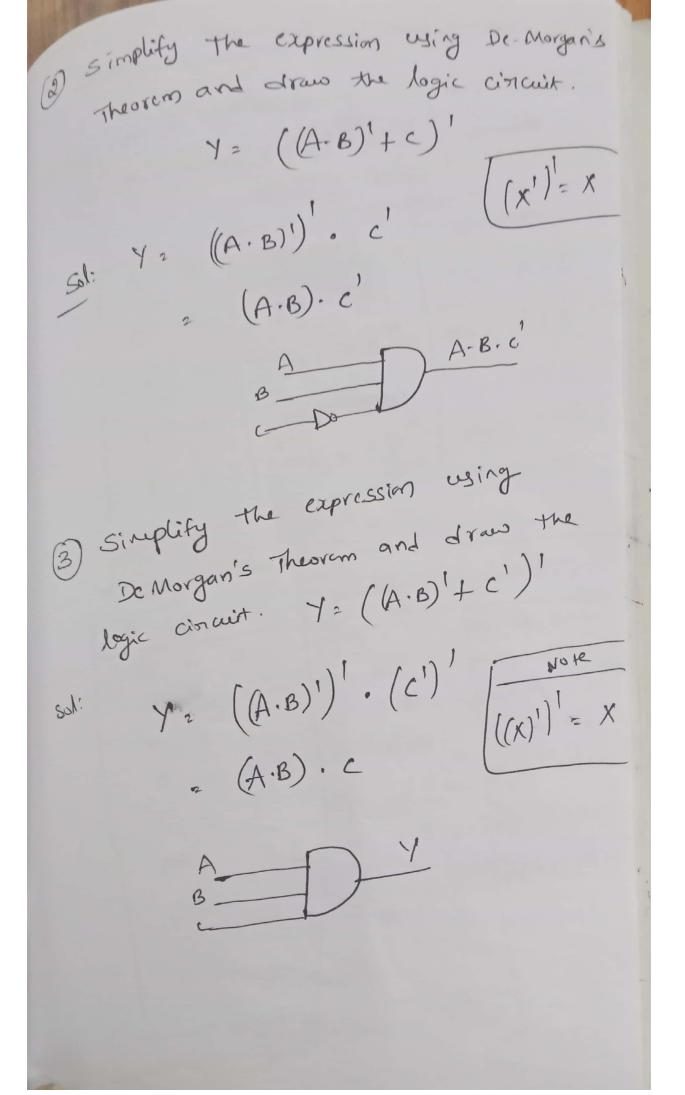
Session 758 Terminal Questions 1 Design given logic function using NAND gaty. F(A, B, c) = A + B.C Apply double complement on both sides F = A+B.C apply semorgan's Theorem. X+7 2 X. 9 2 A. B.C X, A F 2 X.4 Y : B.C 2 Design given logic function using NOR gates Y= (A+B). (C+D') = (A+B). (C+D) apply Demorgan's 2 A+B + C+D 2 X +1 X = A+B, 1/2 (+5

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Demogan's Theorem based problems 1) Simplify the expression using De Morgan's Theorem and draw the logic cincuit. Y = (A.B+C) By applying Demorgan's Theorem (x+y)' = x'. y' Y 2 (A.B). C Y = (A+B) · c' Y 2 (A'+B').C B DO B' A'+B' (A+B).c



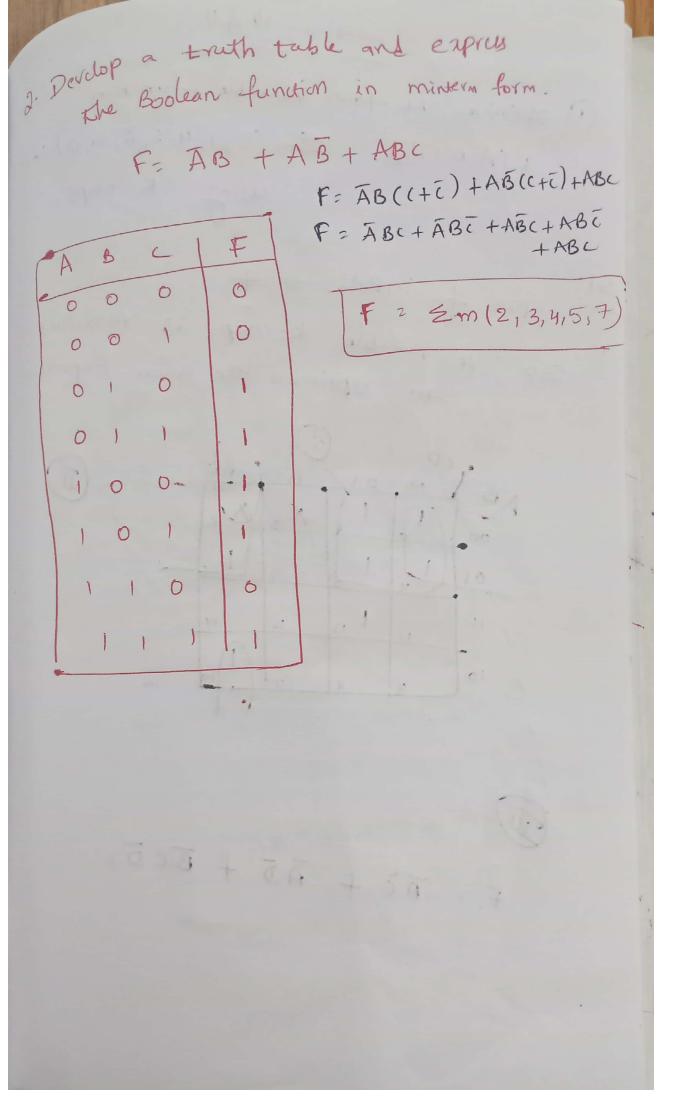
A B C Mintern Mautern A B C Mintern Mauern Mo: A+B Mo: A+B+C	Answer Session - 9 Terminal Questions Seperate tables listing Min and Max terms
A B C Mo: A+B Mo: A+B M	(100 2- voriables (A,B) and 3-variables
A B C Mo: A.B.C Mo: A+B+C Mo:	A B $m_0 = \overline{A} \cdot \overline{B}$ $m_1 = \overline{A} \cdot \overline{B}$ $m_1 = \overline{A} \cdot \overline{B}$ $m_2 = \overline{A} \cdot \overline{B}$ $m_2 = \overline{A} \cdot \overline{B}$ $m_2 = \overline{A} \cdot \overline{B}$
A B C Mo: A.B.C Mo: A+B+C Mo:	
	A B C Mo: A.B.C Mo: A+B+C Mo:

4. Minimize the given product of sum (Pos) using Boolean laws and sterch the logic concest Y= (A+B) (A+B) (A+B) Sol: Y = (A.A + A.B + A.B + B.B) (A+B) = (A + A-B+ A-B+0) (A+B) = (A+A[B+B]) (A+B) = (A+A) (A+B) Logic circuit $= A \cdot (\overline{A} + B)$ $= A \cdot \overline{A} + AB$ $= A \cdot \overline{A} + AB$ Y 2 AB 5. Minimize the given sum of products (SOP) lesing Boolean laws and Sketch the logic concuit. FlABC + ABC + ABC Soli F = ABC + AB[(+c) A AGEN) = ABC + AB Cti=1 $\begin{cases} = A \left(\overline{B}C + B \right) \\ = A \left(\overline{R} \cdot B \right) \end{cases}$ B+B=1 $A \left[\left(\overline{B}_{\dagger}B\right) \cdot \left(C+B\right) \right]$

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6. Minimire The given Sum of products (Sop) using Boolean laws and sketch the Logic Concent. F(A,B,C), A.C+A.C+B.C F= AtC+c] + B.C F. A+B.C

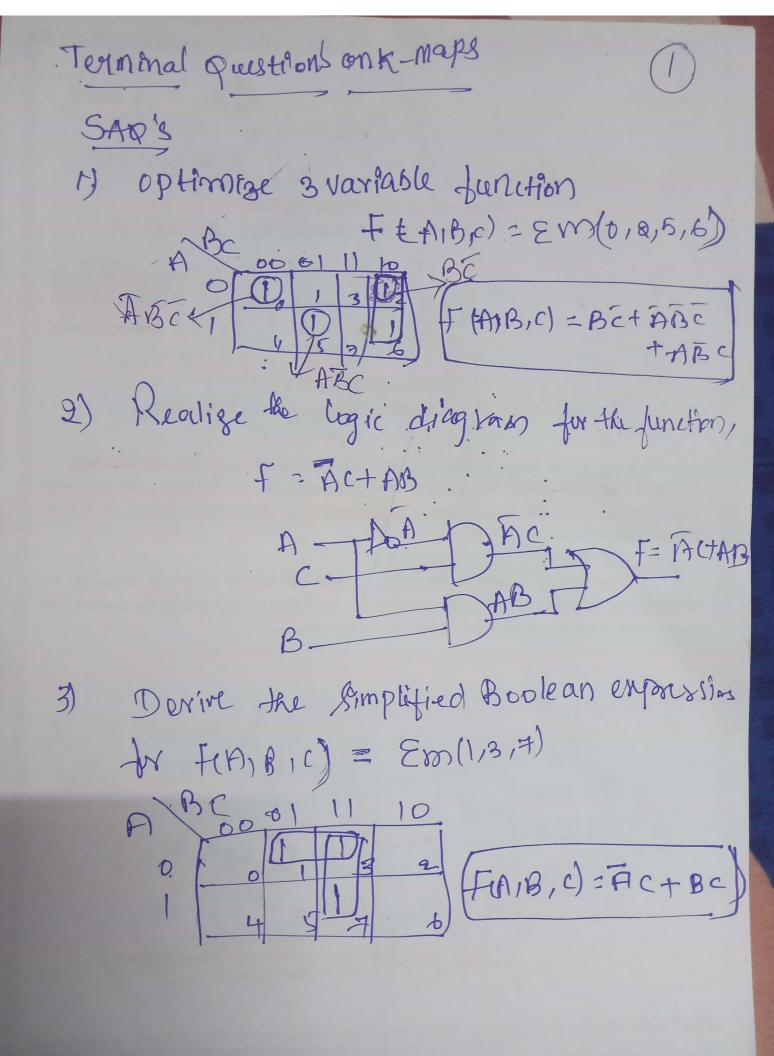
Short Answer questions 1. Develop a touth table that represents the Boolean eccuation. F= ABC+ABC+ABC+ABC = \Zm(1,4,6,7). A B C

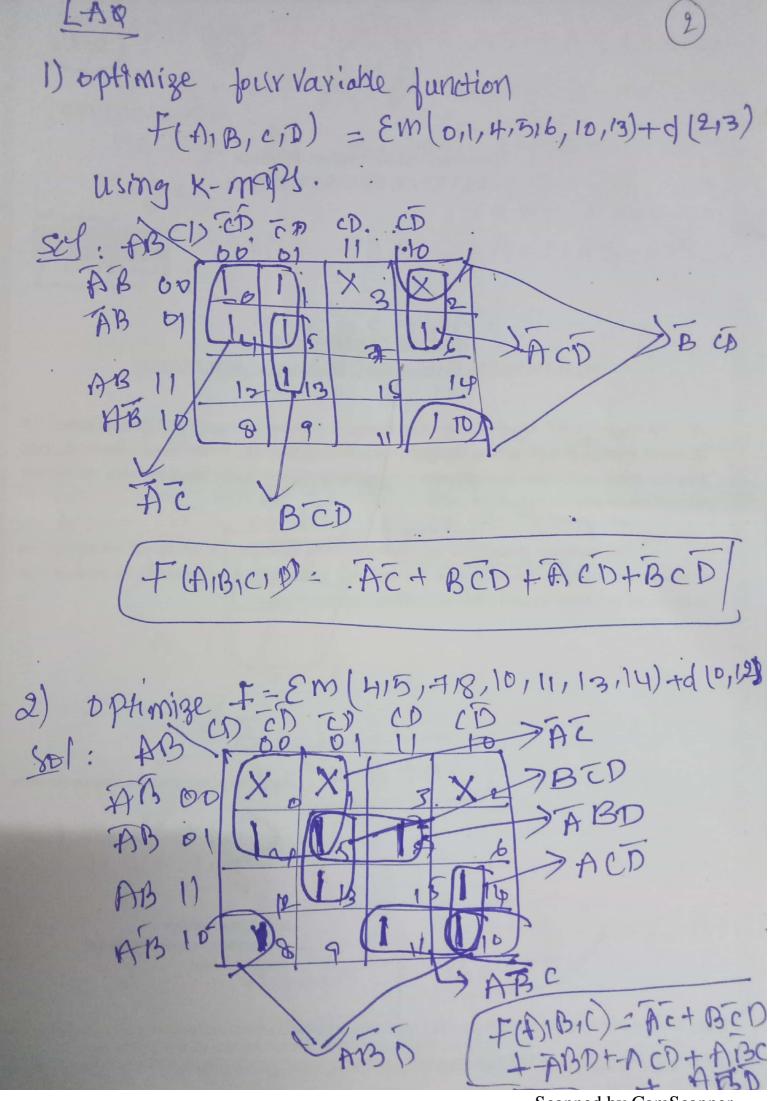


Example: 72-58 (Using 1's complement) 72 in binary (8-bit) -) 01001000 58 in binary (8-5it) -> 00111010 i's complement of subtrainend (58) 11000101 5+493. Add minuent + 1's complex of 01001000 End anount corry 11000101 -> Add Carry to the negula. 00001101 00001110 Sty 4: Result - 0000 1110 Decimal Equalent -> 14

Frample: Performs the 2's complement Subtraction on given decimal values. 89-27 89 in binary -> (01011001)2 26 in binary - (00011010)2 SHP2: Take 2'3, complet of subtrained (26). 2's complement = 1's complement +1 13 comp of 26 -> 11100101 Add 1 11100110 · step3: Add minuent with 2's complet of Subtrakent. minuery = 87 - 016 1.100. 11100110 Disard Disard Interpret The susult. Stou:

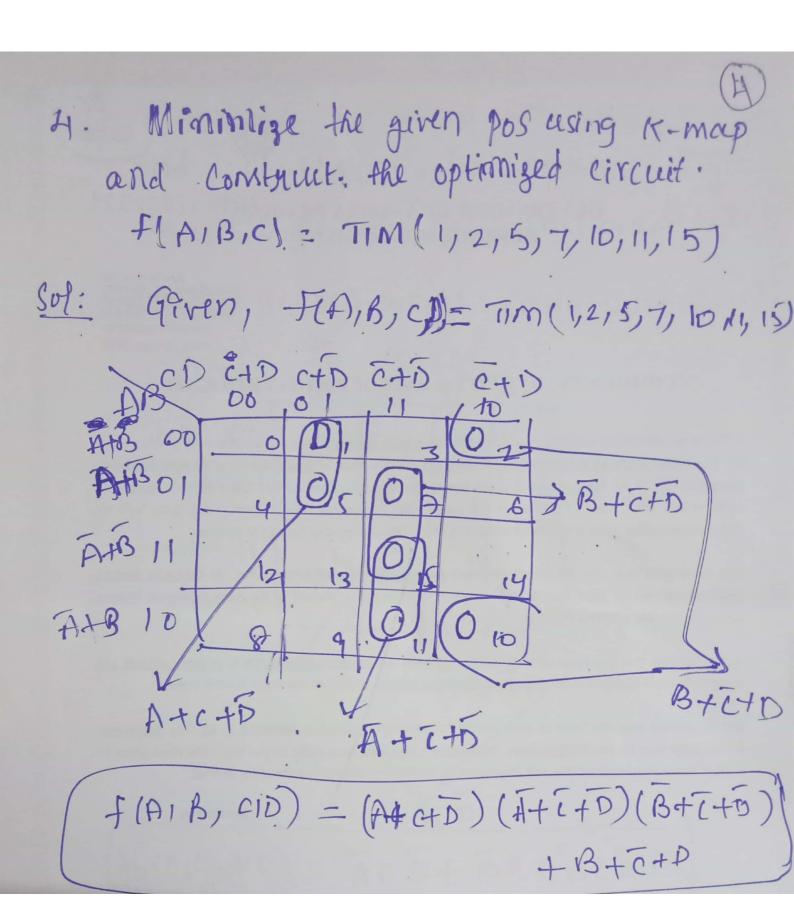
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3. optimize F(AB,C) & AB'C+A'B'C+A'BC + A'B'c'+ AB'c' using K-maps and realize the resultant expression using logic gates. Sel:-Given, FLAIBIC) = ABCHABCHABC + ABC + ABC 100 F(A)B(C) = Em(0,1,3,4,5) BCBCBCBCBC F(A,Bic) = B+AC. Logic Cirwit F= B+AC



Minimize the given sop using K-Map and construct the optimized logic cignuit : F(A1B1C1D) = 2m (0,2,5,6,8,10) So: Given, f(A,B,c,1)= Em(0,2,5,6,8-10) CD ED CD AB. 00 AB o AB ABEL FLAIB, CID) = ACD+BD+ABCD AED BD FLABLID ABCD

