

MSP430 Timer in Capture Mode

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1 Aim

To program the MSP430 Timer_A module in **capture mode** for measuring the period of an external signal.

2 Theory

Timer_A in capture mode allows the microcontroller to record the timer value whenever an edge is detected on a capture pin. By storing multiple capture values, the time period of an input waveform can be computed.

3 Circuit

4 Source Code

```
1 #include <msp430.h>
2 #define NUMBER_TIMER_CAPTURES 20
3
4 volatile unsigned int timerAcaptureValues[NUMBER_TIMER_CAPTURES];
5 unsigned int timerAcapturePointer = 0;
6
7 int main(void)
8 {
9     WDTCTL = WDTPW | WDTHOLD;          // Stop watchdog timer
10
11     // Configure GPIO
12     P1DIR |= BIT0;
13     P1OUT |= BIT0;
14     P1SEL1 |= BIT2;                    // TA0.CCI2A input
15     P1REN |= BIT2;
16     P1OUT &= ~BIT2;
17
18     PM5CTL0 &= ~LOCKLPM5;              // Unlock GPIO
19
20     // Timer0_A3 Setup
21     TA0CTL2 |= CM_1 | CCIS_0 | CCIE | CAP | SCS;
```

```

1/*    programming the TIMER in CAPTURE MODE
2 *
3 *    step-1: An external signal from the function generator is to
        be captured by the mc.
4 *
5 *            choose CCIS bit---find which pin is multiplexed for
        capture input signal from datasheet
6 *            see the controls need to be written in the SEL and OUT
        bits from datasheet
7 *
8 *    step-2: setup timer clk,clr bit enable interrupt, MC from
        TA0CTL register
9 *    step-3: setup capture mode by writing appropriate bits in the
        TA0CCTL register( CAP,CCIS,SCS,capture on rising/falling edge, CIE)
10 *
11 *    step-4: In the interrupt vector, under case TA0IV_TACCx, write
        the interrupt subroutine.
12 *    in this program ,Capture a number of periods of the external
        signal and store them in an array.
13// When the set number of periods is captured the program is
        trapped and the LED on
14// P1.0 is toggled. At this point halt the program execution read
        out the values using
15// the debugger.
16// ACLK = REFOCLK = 32kHz, MCLK = SMCLK = default DCODIV = 1MHz.
17 *
18 */
19#include <msp430.h>
20#define NUMBER_TIMER_CAPTURES      20
21
22volatile unsigned int timerAcaptureValues[NUMBER_TIMER_CAPTURES];
23unsigned int timerAcapturePointer = 0;
24
25
26/**
27 * main.c
28 */
29int main(void)
30{

```

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Figure 1: Circuit for Timer Capture Mode

```

22 TA0CTL |= TASSEL_2 | MC_2 | TACLR;
23
24 __bis_SR_register(LPM0_bits | GIE);
25 __no_operation();
26 }

```

```

27
28 // Timer0_A3 ISR
29 #pragma vector = TIMER0_A1_VECTOR
30 __interrupt void TIMER0_A1_ISR(void)
31 {
32     switch(__even_in_range(TA0IV, TA0IV_TAIFG))
33     {
34         case TA0IV_TACCR2:
35             timerAcaptureValues[timerAcapturePointer++] = TA0CCR2
36             ;
37             if (timerAcapturePointer >= 20)
38             {
39                 while (1)
40                 {
41                     P1OUT ^= BIT0;
42                     __delay_cycles(100000);
43                 }
44                 break;
45             default:
46                 break;
47     }
48 }

```

Listing 1: Timer Capture Mode Code

5 Observation

When the program runs, the MSP430 captures the timer counts on each rising edge of the input signal. After 20 captures, the LED at P1.0 toggles continuously, indicating completion.

6 Conclusion

The experiment successfully demonstrated the use of the Timer_A peripheral in capture mode for measuring signal periods.