# ANALOG IC DESIGN ELECTIVE - II

## **OBJECTIVES**

The student will be introduced to

- The student will be able to understand the behavior of MOS Devices and Small-Signal & Large-Signal Modeling of MOS Transistor and Analog Sub-Circuits.
- In this course, students can study CMOS Amplifiers like Differential Amplifiers, Cascode Amplifiers, Output Amplifiers, and Operational Amplifiers.
- Another main object of this course is to motivate the graduate students to design and to develop the Analog CMOS Circuits for different Analog operations.
- The concepts of Open-Loop Comparators and Different Types of Oscillators like Ring Oscillator, LC Oscillator etc.

#### UNIT -I:

**MOS Devices and Modeling:** The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

#### **UNIT-II:**

**Analog CMOS Sub-Circuits:** MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

# **UNIT-III:**

**CMOS Amplifiers:** Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

#### **UNIT-IV:**

**CMOS Operational Amplifiers:** Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

#### **UNIT-V:**

**Comparators:** Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

## **UNIT-VI:**

**Oscillators & Phase-Locked Loops:** General Considerations, Ring Oscillators, LC Oscillators, Voltage Controlled Oscillators.

Simple PLL, Charge Pump PLLs, Non-Ideal Effects in PLLs, Delay Locked Loops, Applications.

## **Text Books**:

- 1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition.
- 2. CMOS Analog Circuit Design Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.

## **References:**

- 1. Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.
- 2. Analog Integrated Circuit Design- David A.Johns, Ken Martin, Wiley Student Edn, 2013.

## **OUTCOMES**

After going through this course the student will be able to

- Understand the concepts of MOS Devices and Modeling.
- Design and analyze any Analog Circuits in real time applications.
- Extend the Analog Circuit Design to Different Applications in Real Time.
- Understand of Open-Loop Comparators and Different Types of Oscillators.