Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
W16	DXN_0	NA	0	NA	CONFIG	(THERMAL_D_N)	XADC	+VADJ/1 (matching bank 14 - w/ Dynamic CFGBVS circuit)
T17	VCCADC_0	NA	0	NA	CONFIG	(+1.8V analog)		
T16	GNDADC_0	NA	0	NA	CONFIG	(AGND)		
W17	DXP_0	NA	0	NA	CONFIG	(THERMAL_D_P)		
U16	VREFN_0	NA	0	NA	CONFIG	(AGND)		
V17	VREFP_0	NA	0	NA	CONFIG	(VREFP)		
U17	VP_0	NA	0	NA	CONFIG	(AGND)		
V16	VN_0	NA	0	NA	CONFIG	(AGND)		
C15	VCCBATT_0	NA	0	NA	CONFIG	(GND)		
D15	CCLK_0	NA	0	NA	CONFIG	(CCLKX)		
F15	TCK_0	NA	0	NA	CONFIG	(FTCK)	JTAG	
H15	TMS_0	NA	0	NA	CONFIG	(FTMS)		
F14	TDO_0	NA	0	NA	CONFIG	(FTDO)		
G15	TDI_0	NA	0	NA	CONFIG	(FTDI)		
AG14	INIT_B_0	NA	0	NA	CONFIG	(nINITB)	CFG	
AM15	PROGRAM_B_0	NA	0	NA	CONFIG	(nPROGRAMX)		
AL15	CFGBVS_0	NA	0	NA	CONFIG	(CFGBVS - dynamic)		
AH14	DONE_0	NA	0	NA	CONFIG	(DONE)		
AJ14	M2_0	NA	0	NA	CONFIG	'0'	Master Serial Mode	
AJ15	M0_0	NA	0	NA	CONFIG	'1'		
AH15	M1_0	NA	0	NA	CONFIG	'0'		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AG18	IO_0_11	NA	11	NA	HR	SCL_LLLX	I2C Bus to iMX6 CPU	+VIO_B/1
AK23	IO_L1P_T0_11	0	11	NA	HR	FMC_HB13+/1		
AL23	IO_L1N_T0_11	0	11	NA	HR	FMC_HB13-/1		
AM22	IO_L2P_T0_11	0	11	NA	HR	FMC_HB08+/1		
AN22	IO_L2N_T0_11	0	11	NA	HR	FMC_HB08-/1		
AP21	IO_L3P_T0_DQS_11	0	11	NA	HR	FMC_HB09+/1		
AP22	IO_L3N_T0_DQS_11	0	11	NA	HR	FMC_HB09-/1		
AN20	IO_L4P_T0_11	0	11	NA	HR	FMC_HB07+/1		
AP20	IO_L4N_T0_11	0	11	NA	HR	FMC_HB07-/1		
AN19	IO_L5P_T0_11	0	11	NA	HR	FMC_HB04+/1		
AP19	IO_L5N_T0_11	0	11	NA	HR	FMC_HB04-/1		
AL21	IO_L6P_T0_11	0	11	NA	HR	(test point)		
AM21	IO_L6N_T0_VREF_11	0	11	NA	HR	*(VREF_B_M2C/1)*		
AN17	IO_L7P_T1_11	1	11	NA	HR	FMC_HB05+/1		
AN18	IO_L7N_T1_11	1	11	NA	HR	FMC_HB05-/1		
AP16	IO_L8P_T1_11	1	11	NA	HR	FMC_HB01+/1		
AP17	IO_L8N_T1_11	1	11	NA	HR	FMC_HB01-/1		
AM17	IO_L9P_T1_DQS_11	1	11	NA	HR	FMC_HB19+/1		
AM18	IO_L9N_T1_DQS_11	1	11	NA	HR	FMC_HB19-/1	FMC-1 HB	
AL16	IO_L10P_T1_11	1	11	NA	HR	FMC_HB02+/1		
AM16	IO_L10N_T1_11	1	11	NA	HR	FMC_HB02-/1	[NOTE: A=0 (standard) boards are routed as shown.	
AL20	IO_L11P_T1_SRCC_11	1	11	NA	HR	**FMC_HB00+CC/1**		
AM20	IO_L11N_T1_SRCC_11	1	11	NA	HR	**FMC_HB00-CC/1**	A=1 (CMS) boards route the FMC_HB06 pair	
AL18	IO_L12P_T1_MRCC_11	1	11	NA	HR	**FMC_HB17+CC/1**	from backplane FCLKA instead of the FMC and	
AL19	IO_L12N_T1_MRCC_11	1	11	NA	HR	**FMC_HB17-CC/1**		
AK18	IO_L13P_T2_MRCC_11	2	11	NA	HR	**FMC_HB06+CC/1**	FMC_HB21 pair from backplane Port 3 RX insetad of the FMC. These	
AK19	IO_L13N_T2_MRCC_11	2	11	NA	HR	**FMC_HB06-CC/1**		
AH19	IO_L14P_T2_SRCC_11	2	11	NA	HR	FMC_HB14+/1		
AJ19	IO_L14N_T2_SRCC_11	2	11	NA	HR	FMC_HB14-/1		
AK16	IO_L15P_T2_DQS_11	2	11	NA	HR	FMC_HB03+/1	two pairs have external 100ohm terminations only	
AK17	IO_L15N_T2_DQS_11	2	11	NA	HR	FMC_HB03-/1		
AJ16	IO_L16P_T2_11	2	11	NA	HR	FMC_HB15+/1	when A=1 (CMS).]	
AJ17	IO_L16N_T2_11	2	11	NA	HR	FMC_HB15-/1		
AH17	IO_L17P_T2_11	2	11	NA	HR	FMC_HB10+/1		
AH18	IO_L17N_T2_11	2	11	NA	HR	FMC_HB10-/1		
AG16	IO_L18P_T2_11	2	11	NA	HR	FMC_HB11+/1		
AG17	IO_L18N_T2_11	2	11	NA	HR	FMC_HB11-/1		
AJ22	IO_L19P_T3_11	3	11	NA	HR	(test point)		
AK22	IO_L19N_T3_VREF_11	3	11	NA	HR	*(VREF_B_M2C/1)*		
AJ21	IO_L20P_T3_11	3	11	NA	HR	FMC_HB20+/1		
AK21	IO_L20N_T3_11	3	11	NA	HR	FMC_HB20-/1		
AH20	IO_L21P_T3_DQS_11	3	11	NA	HR	FMC_HB16+/1		
AJ20	IO_L21N_T3_DQS_11	3	11	NA	HR	FMC_HB16-/1		
AG20	IO_L22P_T3_11	3	11	NA	HR	FMC_HB18+/1		
AG21	IO_L22N_T3_11	3	11	NA	HR	FMC_HB18-/1		
AG22	IO_L23P_T3_11	3	11	NA	HR	FMC_HB12+/1		
AH22	IO_L23N_T3_11	3	11	NA	HR	FMC_HB12-/1		
AG23	IO_L24P_T3_11	3	11	NA	HR	FMC_HB21+/1		
AH23	IO_L24N_T3_11	3	11	NA	HR	FMC_HB21-/1		
AG15	IO_25_11	NA	11	NA	HR	SDA_LLLX	I2C Bus to iMX6 CPU	

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AD25	IO_0_12	NA	12	NA	HR	nTHERM	Thermal alert input	+VADJ/1
AM23	IO_L1P_T0_12	0	12	NA	HR	FMC_HA11+/1	FMC-1 HA	
AN23	IO_L1N_T0_12	0	12	NA	HR	FMC_HA11-/1		
AN24	IO_L2P_T0_12	0	12	NA	HR	FMC_HA07+/1		
AP24	IO_L2N_T0_12	0	12	NA	HR	FMC_HA07-/1		
AN25	IO_L3P_T0_DQS_12	0	12	NA	HR	FMC_HA05+/1		
AP25	IO_L3N_T0_DQS_12	0	12	NA	HR	FMC_HA05-/1		
AP26	IO_L4P_T0_12	0	12	NA	HR	FMC_HA04+/1		
AP27	IO_L4N_T0_12	0	12	NA	HR	FMC_HA04-/1		
AN27	IO_L5P_T0_12	0	12	NA	HR	FMC_HA02+/1		
AN28	IO_L5N_T0_12	0	12	NA	HR	FMC_HA02-/1		
AM27	IO_L6P_T0_12	0	12	NA	HR	SDA_L2	I2C bus to FMC-1	
AM28	IO_L6N_T0_VREF_12	0	12	NA	HR	*(VREF_A_M2C/1)*	FMC-1 HA	
AM25	IO_L7P_T1_12	1	12	NA	HR	FMC_HA09+/1		
AM26	IO_L7N_T1_12	1	12	NA	HR	FMC_HA09-/1		
AJ29	IO_L8P_T1_12	1	12	NA	HR	FMC_HA13+/1		
AK29	IO_L8N_T1_12	1	12	NA	HR	FMC_HA13-/1		
AK24	IO_L9P_T1_DQS_12	1	12	NA	HR	FMC_HA03+/1		
AL24	IO_L9N_T1_DQS_12	1	12	NA	HR	FMC_HA03-/1		
AK28	IO_L10P_T1_12	1	12	NA	HR	FMC_HA06+/1		
AL28	IO_L10N_T1_12	1	12	NA	HR	FMC_HA06-/1		
AL25	IO_L11P_T1_SRCC_12	1	12	NA	HR	**FMC_HA00+CC/1**		
AL26	IO_L11N_T1_SRCC_12	1	12	NA	HR	**FMC_HA00-CC/1**		
AK26	IO_L12P_T1_MRCC_12	1	12	NA	HR	**FMC_HA17+CC/1**		
AK27	IO_L12N_T1_MRCC_12	1	12	NA	HR	**FMC_HA17-CC/1**		
AJ26	IO_L13P_T2_MRCC_12	2	12	NA	HR	**FMC_HA18+CC/1**		
AJ27	IO_L13N_T2_MRCC_12	2	12	NA	HR	**FMC_HA18-CC/1**		
AH27	IO_L14P_T2_SRCC_12	2	12	NA	HR	**FMC_HA01+CC/1**		
AH28	IO_L14N_T2_SRCC_12	2	12	NA	HR	**FMC_HA01-CC/1**		
AH25	IO_L15P_T2_DQS_12	2	12	NA	HR	FMC_HA12+/1		
AJ25	IO_L15N_T2_DQS_12	2	12	NA	HR	FMC_HA12-/1		
AH24	IO_L16P_T2_12	2	12	NA	HR	FMC_HA08+/1		
AJ24	IO_L16N_T2_12	2	12	NA	HR	FMC_HA08-/1		
AG26	IO_L17P_T2_12	2	12	NA	HR	FMC_HA16+/1		
AG27	IO_L17N_T2_12	2	12	NA	HR	FMC_HA16-/1		
AG28	IO_L18P_T2_12	2	12	NA	HR	FMC_HA14+/1		
AH29	IO_L18N_T2_12	2	12	NA	HR	FMC_HA14-/1		
AD29	IO_L19P_T3_12	3	12	NA	HR	SCL_L2	I2C bus to FMC-1	
AE29	IO_L19N_T3_VREF_12	3	12	NA	HR	*(VREF_A_M2C/1)*	FMC-1 HA	
AF28	IO_L20P_T3_12	3	12	NA	HR	FMC_HA15+/1		
AF29	IO_L20N_T3_12	3	12	NA	HR	FMC_HA15-/1		
AE27	IO_L21P_T3_DQS_12	3	12	NA	HR	FMC_HA20+/1		
AE28	IO_L21N_T3_DQS_12	3	12	NA	HR	FMC_HA20-/1		
AE26	IO_L22P_T3_12	3	12	NA	HR	FMC_HA10+/1		
AF26	IO_L22N_T3_12	3	12	NA	HR	FMC_HA10-/1		
AE24	IO_L23P_T3_12	3	12	NA	HR	FMC_HA23+/1		
AF24	IO_L23N_T3_12	3	12	NA	HR	FMC_HA23-/1		
AF25	IO_L24P_T3_12	3	12	NA	HR	FMC_HA22+/1		
AG25	IO_L24N_T3_12	3	12	NA	HR	FMC_HA22-/1		
AD24	IO_25_12	NA	12	NA	HR	(test point)		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AC30	IO_0_13	NA	13	NA	HR	*ULED0	User LED	+VADJ/1
AC32	IO_L1P_T0_13	0	13	NA	HR	FMC_HA21+/1	FMC-1 HA	
AC33	IO_L1N_T0_13	0	13	NA	HR	FMC_HA21-/1		
AC34	IO_L2P_T0_13	0	13	NA	HR	FMC_HA19+/1		
AD34	IO_L2N_T0_13	0	13	NA	HR	FMC_HA19-/1		
AE34	IO_L3P_T0_DQS_13	0	13	NA	HR	SCL_L1	CBS/Quad PLL/MMC/FMC-0 I2C	
AF34	IO_L3N_T0_DQS_13	0	13	NA	HR	SDA_L1		
AE33	IO_L4P_T0_13	0	13	NA	HR	FMC_LA22+/1	FMC-1 LA	
AF33	IO_L4N_T0_13	0	13	NA	HR	FMC_LA22-/1		
AE31	IO_L5P_T0_13	0	13	NA	HR	FMC_LA21+/1		
AE32	IO_L5N_T0_13	0	13	NA	HR	FMC_LA21-/1		
AD31	IO_L6P_T0_13	0	13	NA	HR	*ULED1	User LED	
AD32	IO_L6N_T0_VREF_13	0	13	NA	HR	*(VREF_A_M2C/1)*	FMC-1 LA	
AH34	IO_L7P_T1_13	1	13	NA	HR	FMC_LA15+/1		
AJ34	IO_L7N_T1_13	1	13	NA	HR	FMC_LA15-/1		
AF30	IO_L8P_T1_13	1	13	NA	HR	FMC_LA19+/1		
AG30	IO_L8N_T1_13	1	13	NA	HR	FMC_LA19-/1		
AG33	IO_L9P_T1_DQS_13	1	13	NA	HR	FMC_LA23+/1		
AH33	IO_L9N_T1_DQS_13	1	13	NA	HR	FMC_LA23-/1		
AH30	IO_L10P_T1_13	1	13	NA	HR	FMC_LA11+/1		
AJ30	IO_L10N_T1_13	1	13	NA	HR	FMC_LA11-/1		
AG32	IO_L11P_T1_SRCC_13	1	13	NA	HR	**FPCLKE+**	M-LVDS CBS Clock In[/Out]	
AH32	IO_L11N_T1_SRCC_13	1	13	NA	HR	**FPCLKE-**	(usable when +VADJ/1=2.5V)	
AF31	IO_L12P_T1_MRCC_13	1	13	NA	HR	**FMC_LA17+CC/1**	FMC-1 LA	
AG31	IO_L12N_T1_MRCC_13	1	13	NA	HR	**FMC_LA17-CC/1**		
AJ31	IO_L13P_T2_MRCC_13	2	13	NA	HR	**FMC_LA18+CC/1**		
AK31	IO_L13N_T2_MRCC_13	2	13	NA	HR	**FMC_LA18-CC/1**		
AJ32	IO_L14P_T2_SRCC_13	2	13	NA	HR	**FPCLKF+**	M-LVDS CBS Clock In[/Out]	
AK32	IO_L14N_T2_SRCC_13	2	13	NA	HR	**FPCLKF-**	(usable when +VADJ/1=2.5V)	
AK33	IO_L15P_T2_DQS_13	2	13	NA	HR	FMC_LA16+/1	FMC-1 LA	
AL33	IO_L15N_T2_DQS_13	2	13	NA	HR	FMC_LA16-/1		
AK34	IO_L16P_T2_13	2	13	NA	HR	FMC_LA26+/1		
AL34	IO_L16N_T2_13	2	13	NA	HR	FMC_LA26-/1		
AL30	IO_L17P_T2_13	2	13	NA	HR	FMC_LA27+/1		
AL31	IO_L17N_T2_13	2	13	NA	HR	FMC_LA27-/1		
AL29	IO_L18P_T2_13	2	13	NA	HR	FMC_LA02+/1		
AM30	IO_L18N_T2_13	2	13	NA	HR	FMC_LA02-/1		
AM31	IO_L19P_T3_13	3	13	NA	HR	*ULED2	User LED	
AM32	IO_L19N_T3_VREF_13	3	13	NA	HR	*(VREF_A_M2C/1)*	FMC-1 LA	
AM33	IO_L20P_T3_13	3	13	NA	HR	FMC_LA12+/1		
AN34	IO_L20N_T3_13	3	13	NA	HR	FMC_LA12-/1		
AN32	IO_L21P_T3_DQS_13	3	13	NA	HR	FMC_LA07+/1		
AP33	IO_L21N_T3_DQS_13	3	13	NA	HR	FMC_LA07-/1		
AP31	IO_L22P_T3_13	3	13	NA	HR	FMC_LA08+/1		
AP32	IO_L22N_T3_13	3	13	NA	HR	FMC_LA08-/1		
AP29	IO_L23P_T3_13	3	13	NA	HR	FMC_LA03+/1		
AP30	IO_L23N_T3_13	3	13	NA	HR	FMC_LA03-/1		
AN29	IO_L24P_T3_13	3	13	NA	HR	FMC_LA04+/1		
AN30	IO_L24N_T3_13	3	13	NA	HR	FMC_LA04-/1		
AD30	IO_25_13	NA	13	NA	HR	*ULED3	User LED	

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AA24	IO_0_14	NA	14	NA	HR	FPGA_RX_UART	FPGA RS-232 port	+VADJ/1
AA33	IO_L1P_T0_D00_MOSI_14	0	14	NA	HR	D00/MOSIX		
AA34	IO_L1N_T0_D01_DIN_14	0	14	NA	HR	D01/MISOX	QSPI Configuration	
Y33	IO_L2P_T0_D02_14	0	14	NA	HR	D02		
Y34	IO_L2N_T0_D03_14	0	14	NA	HR	D03		
V34	IO_L3P_T0_DQS_PUDC_B_14	0	14	NA	HR	*(1K pull-up to VCCO_14)*		
W34	IO_L3N_T0_DQS_EMCCLK_14	0	14	NA	HR	**CLK50MHZ**	Ext Conf Clk	
V32	IO_L4P_T0_D04_14	0	14	NA	HR	FMC_LA31+/1		
V33	IO_L4N_T0_D05_14	0	14	NA	HR	FMC_LA31-/1	FMC-1 LA	
W31	IO_L5P_T0_D06_14	0	14	NA	HR	FMC_LA30+/1		
W32	IO_L5N_T0_D07_14	0	14	NA	HR	FMC_LA30-/1		
V30	IO_L6P_T0_FCS_B_14	0	14	NA	HR	nFCSX	QSPI Configuration	
W30	IO_L6N_T0_D08_VREF_14	0	14	NA	HR	*(VREF_A_M2C/1)*		
V25	IO_L7P_T1_D09_14	1	14	NA	HR	FMC_LA29+/1		
W25	IO_L7N_T1_D10_14	1	14	NA	HR	FMC_LA29-/1		
V29	IO_L8P_T1_D11_14	1	14	NA	HR	FMC_LA05+/1		
W29	IO_L8N_T1_D12_14	1	14	NA	HR	FMC_LA05-/1	FMC-1 LA	
V27	IO_L9P_T1_DQS_14	1	14	NA	HR	FMC_LA06+/1		
V28	IO_L9N_T1_DQS_D13_14	1	14	NA	HR	FMC_LA06-/1		
W24	IO_L10P_T1_D14_14	1	14	NA	HR	FMC_LA20+/1		
Y24	IO_L10N_T1_D15_14	1	14	NA	HR	FMC_LA20-/1		
W26	IO_L11P_T1_SRCC_14	1	14	NA	HR	**FPCLKG+**	M-LVDS CBS Clock In[/Out]	
W27	IO_L11N_T1_SRCC_14	1	14	NA	HR	**FPCLKG-**	(usable when +VADJ/1=2.5V)	
Y26	IO_L12P_T1_MRCC_14	1	14	NA	HR	**FMC_LA00+CC/1**		
Y27	IO_L12N_T1_MRCC_14	1	14	NA	HR	**FMC_LA00-CC/1**	FMC-1 LA	
AA28	IO_L13P_T2_MRCC_14	2	14	NA	HR	**FMC_LA01+CC/1**		
AA29	IO_L13N_T2_MRCC_14	2	14	NA	HR	**FMC_LA01-CC/1**		
Y28	IO_L14P_T2_SRCC_14	2	14	NA	HR	**FPCLKH+**	M-LVDS CBS Clock In[/Out]	
Y29	IO_L14N_T2_SRCC_14	2	14	NA	HR	**FPCLKH-**	(usable when +VADJ/1=2.5V)	
AB30	IO_L15P_T2_DQS_RDWR_B_14	2	14	NA	HR	FMC_LA24+/1		
AB31	IO_L15N_T2_DQS_DOUT_CSO_B_14	2	14	NA	HR	FMC_LA24-/1		
Y31	IO_L16P_T2_CSI_B_14	2	14	NA	HR	FMC_LA09+/1		
Y32	IO_L16N_T2_A15_D31_14	2	14	NA	HR	FMC_LA09-/1		
AB32	IO_L17P_T2_A14_D30_14	2	14	NA	HR	FMC_LA10+/1		
AB33	IO_L17N_T2_A13_D29_14	2	14	NA	HR	FMC_LA10-/1		
AA30	IO_L18P_T2_A12_D28_14	2	14	NA	HR	FMC_LA33+/1		
AA31	IO_L18N_T2_A11_D27_14	2	14	NA	HR	FMC_LA33-/1		
AA26	IO_L19P_T3_A10_D26_14	3	14	NA	HR	(test point)		
AB26	IO_L19N_T3_A09_D25_VREF_14	3	14	NA	HR	*(VREF_A_M2C/1)*	FMC-1 LA	
AB27	IO_L20P_T3_A08_D24_14	3	14	NA	HR	FMC_LA32+/1		
AB28	IO_L20N_T3_A07_D23_14	3	14	NA	HR	FMC_LA32-/1		
AA25	IO_L21P_T3_DQS_14	3	14	NA	HR	FMC_LA13+/1		
AB25	IO_L21N_T3_DQS_A06_D22_14	3	14	NA	HR	FMC_LA13-/1		
AC27	IO_L22P_T3_A05_D21_14	3	14	NA	HR	FMC_LA14+/1		
AD27	IO_L22N_T3_A04_D20_14	3	14	NA	HR	FMC_LA14-/1		
AC28	IO_L23P_T3_A03_D19_14	3	14	NA	HR	FMC_LA25+/1		
AC29	IO_L23N_T3_A02_D18_14	3	14	NA	HR	FMC_LA25-/1		
AC25	IO_L24P_T3_A01_D17_14	3	14	NA	HR	FMC_LA28+/1		
AD26	IO_L24N_T3_A00_D16_14	3	14	NA	HR	FMC_LA28-/1		
AC24	IO_25_14	NA	14	NA	HR	FPGA_TX_UART	FPGA RS-232 port	

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
T24	IO_0_15	NA	15	NA	HR	(test point)	FMC-0 HB	+VIO_B/0
N28	IO_L1P_T0_AD0P_15	0	15	NA	HR	FMC_HB18+/0		
M28	IO_L1N_T0_AD0N_15	0	15	NA	HR	FMC_HB18-/0		
P29	IO_L2P_T0_AD8P_15	0	15	NA	HR	FMC_HB16+/0		
N29	IO_L2N_T0_AD8N_15	0	15	NA	HR	FMC_HB16-/0		
P27	IO_L3P_T0_DQS_AD1P_15	0	15	NA	HR	FMC_HB21+/0		
N27	IO_L3N_T0_DQS_AD1N_15	0	15	NA	HR	FMC_HB21-/0		
N24	IO_L4P_T0_AD9P_15	0	15	NA	HR	FMC_HB20+/0		
N25	IO_L4N_T0_AD9N_15	0	15	NA	HR	FMC_HB20-/0		
P25	IO_L5P_T0_AD2P_15	0	15	NA	HR	FMC_HB03+/0		
P26	IO_L5N_T0_AD2N_15	0	15	NA	HR	FMC_HB03-/0		
R24	IO_L6P_T0_15	0	15	NA	HR	(test point)		
P24	IO_L6N_T0_VREF_15	0	15	NA	HR	*(VREF_B_M2C/0)*		
R26	IO_L7P_T1_AD10P_15	1	15	NA	HR	FMC_HB02+/0		
R27	IO_L7N_T1_AD10N_15	1	15	NA	HR	FMC_HB02-/0		
T25	IO_L8P_T1_AD3P_15	1	15	NA	HR	FMC_HB05+/0		
T26	IO_L8N_T1_AD3N_15	1	15	NA	HR	FMC_HB05-/0		
U25	IO_L9P_T1_DQS_AD11P_15	1	15	NA	HR	FMC_HB01+/0		
U26	IO_L9N_T1_DQS_AD11N_15	1	15	NA	HR	FMC_HB01-/0		
U27	IO_L10P_T1_AD4P_15	1	15	NA	HR	FMC_HB04+/0		
U28	IO_L10N_T1_AD4N_15	1	15	NA	HR	FMC_HB04-/0		
T28	IO_L11P_T1_SRCC_AD12P_15	1	15	NA	HR	**FMC_HB00+CC/0**		
T29	IO_L11N_T1_SRCC_AD12N_15	1	15	NA	HR	**FMC_HB00-CC/0**		
R28	IO_L12P_T1_MRCC_AD5P_15	1	15	NA	HR	**FMC_HB17+CC/0**		
R29	IO_L12N_T1_MRCC_AD5N_15	1	15	NA	HR	**FMC_HB17-CC/0**		
R31	IO_L13P_T2_MRCC_15	2	15	NA	HR	**FMC_HB06+CC/0**		
P31	IO_L13N_T2_MRCC_15	2	15	NA	HR	**FMC_HB06-CC/0**		
P30	IO_L14P_T2_SRCC_15	2	15	NA	HR	FMC_HB14+/0		
N30	IO_L14N_T2_SRCC_15	2	15	NA	HR	FMC_HB14-/0		
M30	IO_L15P_T2_DQS_15	2	15	NA	HR	FMC_HB19+/0		
M31	IO_L15N_T2_DQS_ADV_B_15	2	15	NA	HR	FMC_HB19-/0		
N32	IO_L16P_T2_A28_15	2	15	NA	HR	FMC_HB12+/0		
M32	IO_L16N_T2_A27_15	2	15	NA	HR	FMC_HB12-/0		
N33	IO_L17P_T2_A26_15	2	15	NA	HR	FMC_HB13+/0		
M33	IO_L17N_T2_A25_15	2	15	NA	HR	FMC_HB13-/0		
P34	IO_L18P_T2_A24_15	2	15	NA	HR	FMC_HB15+/0		
N34	IO_L18N_T2_A23_15	2	15	NA	HR	FMC_HB15-/0		
U30	IO_L19P_T3_A22_15	3	15	NA	HR	(test point)		
U31	IO_L19N_T3_A21_VREF_15	3	15	NA	HR	*(VREF_B_M2C/0)*		
T30	IO_L20P_T3_A20_15	3	15	NA	HR	FMC_HB10+/0		
T31	IO_L20N_T3_A19_15	3	15	NA	HR	FMC_HB10-/0		
U32	IO_L21P_T3_DQS_15	3	15	NA	HR	FMC_HB07+/0		
U33	IO_L21N_T3_DQS_A18_15	3	15	NA	HR	FMC_HB07-/0		
R32	IO_L22P_T3_A17_15	3	15	NA	HR	FMC_HB08+/0		
P32	IO_L22N_T3_A16_15	3	15	NA	HR	FMC_HB08-/0		
T33	IO_L23P_T3_FOE_B_15	3	15	NA	HR	FMC_HB11+/0		
T34	IO_L23N_T3_FWE_B_15	3	15	NA	HR	FMC_HB11-/0		
R33	IO_L24P_T3_RS1_15	3	15	NA	HR	FMC_HB09+/0		
R34	IO_L24N_T3_RS0_15	3	15	NA	HR	FMC_HB09-/0		
V24	IO_25_15	NA	15	NA	HR	(test point)		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
J29	IO_0_16	NA	16	NA	HR	USER6	DIP Switch Input	+VADJ/0
A29	IO_L1P_T0_16	0	16	NA	HR	FMC_HA21+/0	FMC-0 HA	
A30	IO_L1N_T0_16	0	16	NA	HR	FMC_HA21-/0		
B30	IO_L2P_T0_16	0	16	NA	HR	FMC_HA15+/0		
A31	IO_L2N_T0_16	0	16	NA	HR	FMC_HA15-/0		
B31	IO_L3P_T0_DQS_16	0	16	NA	HR	FMC_HA14+/0		
B32	IO_L3N_T0_DQS_16	0	16	NA	HR	FMC_HA14-/0		
B33	IO_L4P_T0_16	0	16	NA	HR	FMC_HA16+/0		
A33	IO_L4N_T0_16	0	16	NA	HR	FMC_HA16-/0		
C32	IO_L5P_T0_16	0	16	NA	HR	FMC_HA12+/0		
C33	IO_L5N_T0_16	0	16	NA	HR	FMC_HA12-/0		
C29	IO_L6P_T0_16	0	16	NA	HR	(test point)		
C30	IO_L6N_T0_VREF_16	0	16	NA	HR	*(VREF_A_M2C/0)*		
D29	IO_L7P_T1_16	1	16	NA	HR	FMC_HA20+/0		
D30	IO_L7N_T1_16	1	16	NA	HR	FMC_HA20-/0		
F34	IO_L8P_T1_16	1	16	NA	HR	FMC_HA09+/0		
E34	IO_L8N_T1_16	1	16	NA	HR	FMC_HA09-/0		
D34	IO_L9P_T1_DQS_16	1	16	NA	HR	FMC_HA13+/0		
C34	IO_L9N_T1_DQS_16	1	16	NA	HR	FMC_HA13-/0		
F33	IO_L10P_T1_16	1	16	NA	HR	FMC_HA08+/0		
E33	IO_L10N_T1_16	1	16	NA	HR	FMC_HA08-/0		
E31	IO_L11P_T1_SRCC_16	1	16	NA	HR	**FMC_HA00+CC/0**		
D31	IO_L11N_T1_SRCC_16	1	16	NA	HR	**FMC_HA00-CC/0**		
E32	IO_L12P_T1_MRCC_16	1	16	NA	HR	**FMC_HA17+CC/0**		
D32	IO_L12N_T1_MRCC_16	1	16	NA	HR	**FMC_HA17-CC/0**		
G30	IO_L13P_T2_MRCC_16	2	16	NA	HR	**FMC_HA18+CC/0**		
F30	IO_L13N_T2_MRCC_16	2	16	NA	HR	**FMC_HA18-CC/0**		
G31	IO_L14P_T2_SRCC_16	2	16	NA	HR	**FMC_HA01+CC/0**		
F31	IO_L14N_T2_SRCC_16	2	16	NA	HR	**FMC_HA01-CC/0**		
H29	IO_L15P_T2_DQS_16	2	16	NA	HR	FMC_HA10+/0		
H30	IO_L15N_T2_DQS_16	2	16	NA	HR	FMC_HA10-/0		
F29	IO_L16P_T2_16	2	16	NA	HR	FMC_HA19+/0		
E29	IO_L16N_T2_16	2	16	NA	HR	FMC_HA19-/0		
H32	IO_L17P_T2_16	2	16	NA	HR	FMC_HA04+/0		
G32	IO_L17N_T2_16	2	16	NA	HR	FMC_HA04-/0		
H33	IO_L18P_T2_16	2	16	NA	HR	FMC_HA05+/0		
G33	IO_L18N_T2_16	2	16	NA	HR	FMC_HA05-/0		
J30	IO_L19P_T3_16	3	16	NA	HR	(test point)		
J31	IO_L19N_T3_VREF_16	3	16	NA	HR	*(VREF_A_M2C/0)*		
L31	IO_L20P_T3_16	3	16	NA	HR	FMC_HA11+/0		
K31	IO_L20N_T3_16	3	16	NA	HR	FMC_HA11-/0		
K32	IO_L21P_T3_DQS_16	3	16	NA	HR	FMC_HA02+/0		
J32	IO_L21N_T3_DQS_16	3	16	NA	HR	FMC_HA02-/0		
L33	IO_L22P_T3_16	3	16	NA	HR	FMC_HA06+/0		
K33	IO_L22N_T3_16	3	16	NA	HR	FMC_HA06-/0		
L34	IO_L23P_T3_16	3	16	NA	HR	FMC_HA07+/0		
K34	IO_L23N_T3_16	3	16	NA	HR	FMC_HA07-/0		
J34	IO_L24P_T3_16	3	16	NA	HR	FMC_HA03+/0		
H34	IO_L24N_T3_16	3	16	NA	HR	FMC_HA03-/0		
L30	IO_25_16	NA	16	NA	HR	(test point)		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
H23	IO_0_17	NA	17	NA	HR	USER4	DIP Switch Input	+VADJ/0
C24	IO_L1P_T0_17	0	17	NA	HR	FMC_HA22+/0	FMC-0 HA	
C25	IO_L1N_T0_17	0	17	NA	HR	FMC_HA22-/0		
A24	IO_L2P_T0_17	0	17	NA	HR	FMC_HA23+/0		
A25	IO_L2N_T0_17	0	17	NA	HR	FMC_HA23-/0		
B25	IO_L3P_T0_DQS_17	0	17	NA	HR	AMC/TX3SEL+	A=1 (CBS) connects to AMC Port 3 TX pins (ext 100ohm term)	
A26	IO_L3N_T0_DQS_17	0	17	NA	HR	AMC/TX3SEL-		
B26	IO_L4P_T0_17	0	17	NA	HR	FMC_LA13+/0	FMC-0 LA	
B27	IO_L4N_T0_17	0	17	NA	HR	FMC_LA13-/0		
B28	IO_L5P_T0_17	0	17	NA	HR	FMC_LA14+/0		
A28	IO_L5N_T0_17	0	17	NA	HR	FMC_LA14-/0		
C27	IO_L6P_T0_17	0	17	NA	HR	(test point)		
C28	IO_L6N_T0_VREF_17	0	17	NA	HR	*(VREF_A_M2C/0)*		
F24	IO_L7P_T1_17	1	17	NA	HR	FMC_LA20+/0		
E24	IO_L7N_T1_17	1	17	NA	HR	FMC_LA20-/0		
D26	IO_L8P_T1_17	1	17	NA	HR	FMC_LA10+/0		
D27	IO_L8N_T1_17	1	17	NA	HR	FMC_LA10-/0		
D24	IO_L9P_T1_DQS_17	1	17	NA	HR	FMC_LA19+/0		
D25	IO_L9N_T1_DQS_17	1	17	NA	HR	FMC_LA19-/0		
F28	IO_L10P_T1_17	1	17	NA	HR	FMC_LA09+/0		
E28	IO_L10N_T1_17	1	17	NA	HR	FMC_LA09-/0		
F25	IO_L11P_T1_SRCC_17	1	17	NA	HR	**FPCLKA+**	Quad PLL Clock In (external 100ohm termination)	
F26	IO_L11N_T1_SRCC_17	1	17	NA	HR	**FPCLKA-**		
E26	IO_L12P_T1_MRCC_17	1	17	NA	HR	**FMC_LA17+CC/0**	FMC-0 LA	
E27	IO_L12N_T1_MRCC_17	1	17	NA	HR	**FMC_LA17-CC/0**		
H27	IO_L13P_T2_MRCC_17	2	17	NA	HR	**FMC_LA18+CC/0**		
G27	IO_L13N_T2_MRCC_17	2	17	NA	HR	**FMC_LA18-CC/0**		
G25	IO_L14P_T2_SRCC_17	2	17	NA	HR	**FPCLKB+**	Quad PLL Clock In (external 100ohm termination)	
G26	IO_L14N_T2_SRCC_17	2	17	NA	HR	**FPCLKB-**		
J24	IO_L15P_T2_DQS_17	2	17	NA	HR	FMC_LA03+/0	FMC-0 LA	
J25	IO_L15N_T2_DQS_17	2	17	NA	HR	FMC_LA03-/0		
H24	IO_L16P_T2_17	2	17	NA	HR	FMC_LA11+/0		
H25	IO_L16N_T2_17	2	17	NA	HR	FMC_LA11-/0		
H28	IO_L17P_T2_17	2	17	NA	HR	FMC_LA08+/0		
G28	IO_L17N_T2_17	2	17	NA	HR	FMC_LA08-/0		
J26	IO_L18P_T2_17	2	17	NA	HR	FMC_LA12+/0		
J27	IO_L18N_T2_17	2	17	NA	HR	FMC_LA12-/0		
L25	IO_L19P_T3_17	3	17	NA	HR	(test point)		
L26	IO_L19N_T3_VREF_17	3	17	NA	HR	*(VREF_A_M2C/0)*		
L24	IO_L20P_T3_17	3	17	NA	HR	FMC_LA04+/0		
K24	IO_L20N_T3_17	3	17	NA	HR	FMC_LA04-/0		
K28	IO_L21P_T3_DQS_17	3	17	NA	HR	FMC_LA05+/0		
K29	IO_L21N_T3_DQS_17	3	17	NA	HR	FMC_LA05-/0		
K26	IO_L22P_T3_17	3	17	NA	HR	FMC_LA07+/0		
K27	IO_L22N_T3_17	3	17	NA	HR	FMC_LA07-/0		
M26	IO_L23P_T3_17	3	17	NA	HR	FMC_LA02+/0		
M27	IO_L23N_T3_17	3	17	NA	HR	FMC_LA02-/0		
L28	IO_L24P_T3_17	3	17	NA	HR	FMC_LA06+/0		
L29	IO_L24N_T3_17	3	17	NA	HR	FMC_LA06-/0		
M25	IO_25_17	NA	17	NA	HR	USER5	DIP Switch Input	

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
G16	IO_0_18	NA	18	NA	HR	USER0	DIP Switch Input	+VADJ/0
H19	IO_L1P_T0_18	0	18	NA	HR	nLVDS_CBS_RST	Clocking Reset Control	
H20	IO_L1N_T0_18	0	18	NA	HR	nPLL_RST		
H17	IO_L2P_T0_18	0	18	NA	HR	nPRSNT_M2C0	FMC-0 Status	
H18	IO_L2N_T0_18	0	18	NA	HR	CLK_DIR0		
G17	IO_L3P_T0_DQS_18	0	18	NA	HR	nPRSNT_M2C1	FMC-1 Status	
G18	IO_L3N_T0_DQS_18	0	18	NA	HR	CLK_DIR1		
F16	IO_L4P_T0_18	0	18	NA	HR	FMC_LA32+/0	FMC-0 LA	
E16	IO_L4N_T0_18	0	18	NA	HR	FMC_LA32-/0		
E17	IO_L5P_T0_18	0	18	NA	HR	FMC_LA33+/0		
E18	IO_L5N_T0_18	0	18	NA	HR	FMC_LA33-/0		
F18	IO_L6P_T0_18	0	18	NA	HR	USER1	DIP Switch Input	
F19	IO_L6N_T0_VREF_18	0	18	NA	HR	*(VREF_A_M2C/0)*	FMC-0 LA	
G20	IO_L7P_T1_18	1	18	NA	HR	FMC_LA15+/0		
G21	IO_L7N_T1_18	1	18	NA	HR	FMC_LA15-/0		
F20	IO_L8P_T1_18	1	18	NA	HR	FMC_LA27+/0		
F21	IO_L8N_T1_18	1	18	NA	HR	FMC_LA27-/0		
G22	IO_L9P_T1_DQS_18	1	18	NA	HR	FMC_LA23+/0		
G23	IO_L9N_T1_DQS_18	1	18	NA	HR	FMC_LA23-/0		
F23	IO_L10P_T1_18	1	18	NA	HR	FMC_LA16+/0		
E23	IO_L10N_T1_18	1	18	NA	HR	FMC_LA16-/0		
D20	IO_L11P_T1_SRCC_18	1	18	NA	HR	**FPCLKC+**	Quad PLL Clock In (external 100ohm termination)	
D21	IO_L11N_T1_SRCC_18	1	18	NA	HR	**FPCLKC-**		
E21	IO_L12P_T1_MRCC_18	1	18	NA	HR	**FMC_LA00+CC/0**	FMC-0 LA	
E22	IO_L12N_T1_MRCC_18	1	18	NA	HR	**FMC_LA00-CC/0**		
E19	IO_L13P_T2_MRCC_18	2	18	NA	HR	**FMC_LA01+CC/0**		
D19	IO_L13N_T2_MRCC_18	2	18	NA	HR	**FMC_LA01-CC/0**		
C18	IO_L14P_T2_SRCC_18	2	18	NA	HR	**FPCLKD+**	Quad PLL Clock In (external 100ohm termination)	
C19	IO_L14N_T2_SRCC_18	2	18	NA	HR	**FPCLKD-**		
D16	IO_L15P_T2_DQS_18	2	18	NA	HR	FMC_LA30+/0	FMC-0 LA	
D17	IO_L15N_T2_DQS_18	2	18	NA	HR	FMC_LA30-/0		
C17	IO_L16P_T2_18	2	18	NA	HR	FMC_LA28+/0		
B17	IO_L16N_T2_18	2	18	NA	HR	FMC_LA28-/0		
B16	IO_L17P_T2_18	2	18	NA	HR	FMC_LA31+/0		
A16	IO_L17N_T2_18	2	18	NA	HR	FMC_LA31-/0		
B18	IO_L18P_T2_18	2	18	NA	HR	FMC_LA29+/0		
A18	IO_L18N_T2_18	2	18	NA	HR	FMC_LA29-/0		
D22	IO_L19P_T3_18	3	18	NA	HR	USER2	DIP Switch Input	
C22	IO_L19N_T3_VREF_18	3	18	NA	HR	*(VREF_A_M2C/0)*	FMC-0 LA	
C20	IO_L20P_T3_18	3	18	NA	HR	FMC_LA24+/0		
B20	IO_L20N_T3_18	3	18	NA	HR	FMC_LA24-/0		
A19	IO_L21P_T3_DQS_18	3	18	NA	HR	FMC_LA25+/0		
A20	IO_L21N_T3_DQS_18	3	18	NA	HR	FMC_LA25-/0		
B21	IO_L22P_T3_18	3	18	NA	HR	FMC_LA21+/0		
A21	IO_L22N_T3_18	3	18	NA	HR	FMC_LA21-/0		
B22	IO_L23P_T3_18	3	18	NA	HR	FMC_LA26+/0		
A23	IO_L23N_T3_18	3	18	NA	HR	FMC_LA26-/0		
C23	IO_L24P_T3_18	3	18	NA	HR	FMC_LA22+/0		
B23	IO_L24N_T3_18	3	18	NA	HR	FMC_LA22-/0		
H22	IO_25_18	NA	18	NA	HR	USER3	DIP Switch Input	

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AL8	MGTXTXP3_111	NA	111	NA	GTX	fmc_tx7_p/1	FMC-1 4-7 / 125MHz / 156.25MHz	n/a
AK10	MGTXRXP3_111	NA	111	NA	GTX	fmc_rx7_p/1		
AL7	MGTXTXN3_111	NA	111	NA	GTX	fmc_tx7_n/1		
AK9	MGTXRXN3_111	NA	111	NA	GTX	fmc_rx7_n/1		
AN8	MGTXTXP2_111	NA	111	NA	GTX	fmc_tx6_p/1		
AJ12	MGTXRXP2_111	NA	111	NA	GTX	fmc_rx6_p/1		
AN7	MGTXTXN2_111	NA	111	NA	GTX	fmc_tx6_n/1		
AH10	MGTREFCLK0P_111	NA	111	NA	GTX	CLK156_25MHZ0_P		
AJ11	MGTXRXN2_111	NA	111	NA	GTX	fmc_rx6_n/1		
AH9	MGTREFCLK0N_111	NA	111	NA	GTX	CLK156_25MHZ0_N		
AJ7	MGTREFCLK1N_111	NA	111	NA	GTX	CLK125MHZ0_N		
AJ8	MGTREFCLK1P_111	NA	111	NA	GTX	CLK125MHZ0_P		
AP10	MGTXTXP1_111	NA	111	NA	GTX	fmc_tx5_p/1		
AL12	MGTXRXP1_111	NA	111	NA	GTX	fmc_rx5_p/1		
AP9	MGTXTXN1_111	NA	111	NA	GTX	fmc_tx5_n/1		
AL11	MGTXRXN1_111	NA	111	NA	GTX	fmc_rx5_n/1		
AN12	MGTXTXP0_111	NA	111	NA	GTX	fmc_tx4_p/1		
AM10	MGTXRXP0_111	NA	111	NA	GTX	fmc_rx4_p/1		
AN11	MGTXTXN0_111	NA	111	NA	GTX	fmc_tx4_n/1		
AM9	MGTXRXN0_111	NA	111	NA	GTX	fmc_rx4_n/1		
AM2	MGTXTXP3_112	NA	112	NA	GTX	fmc_tx3_p/1	FMC-1 0-3 / GBTCLK0/1 / GBTCLK1/1	n/a
AJ4	MGTXRXP3_112	NA	112	NA	GTX	fmc_rx3_p/1		
AM1	MGTXTXN3_112	NA	112	NA	GTX	fmc_tx3_n/1		
AJ3	MGTXRXN3_112	NA	112	NA	GTX	fmc_rx3_n/1		
AP2	MGTXTXP2_112	NA	112	NA	GTX	fmc_tx2_p/1		
AK6	MGTXRXP2_112	NA	112	NA	GTX	fmc_rx2_p/1		
AP1	MGTXTXN2_112	NA	112	NA	GTX	fmc_tx2_n/1		
AG8	MGTREFCLK0P_112	NA	112	NA	GTX	FMC_GBTCLK0_P/1		
AK5	MGTXRXN2_112	NA	112	NA	GTX	fmc_rx2_n/1		
AG7	MGTREFCLK0N_112	NA	112	NA	GTX	FMC_GBTCLK0_N/1		
AH5	MGTREFCLK1N_112	NA	112	NA	GTX	FMC_GBTCLK1_N/1		
AH6	MGTREFCLK1P_112	NA	112	NA	GTX	FMC_GBTCLK1_P/1		
AN4	MGTXTXP1_112	NA	112	NA	GTX	fmc_tx1_p/1		
AL4	MGTXRXP1_112	NA	112	NA	GTX	fmc_rx1_p/1		
AN3	MGTXTXN1_112	NA	112	NA	GTX	fmc_tx1_n/1		
AL3	MGTXRXN1_112	NA	112	NA	GTX	fmc_rx1_n/1		
AP6	MGTXTXP0_112	NA	112	NA	GTX	fmc_tx0_p/1		
AM6	MGTXRXP0_112	NA	112	NA	GTX	fmc_rx0_p/1		
AP5	MGTXTXN0_112	NA	112	NA	GTX	rmc_tx0_n/1		
AM5	MGTXRXN0_112	NA	112	NA	GTX	fmc_rx0_n/1		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
AD2	MGTXTXP3_113	NA	113	NA	GTX	amc_tx3_p	AMC 0-1 / FMC-1 8-9 / 125MHz / 156.25MHz [NOTE: A=0 (standard) boards are routed as shown. A=1 (CMS) boards cannot use the ports shown in red since the backplane pins are re-allocated for other purposes. See the red notes in earlier banks.]	n/a
AC4	MGTXRXP3_113	NA	113	NA	GTX	amc_rx3_p		
AD1	MGTXTXN3_113	NA	113	NA	GTX	amc_tx3_n		
AC3	MGTXRXN3_113	NA	113	NA	GTX	amc_rx3_n		
AF2	MGTXTXP2_113	NA	113	NA	GTX	amc_tx2_p		
AE4	MGTXRXP2_113	NA	113	NA	GTX	amc_rx2_p		
AF1	MGTXTXN2_113	NA	113	NA	GTX	amc_tx2_n		
AC8	MGTREFCLK0P_113	NA	113	NA	GTX	CLK156_25MHZ1_P		
AE3	MGTXRXN2_113	NA	113	NA	GTX	amc_rx2_n		
AC7	MGTREFCLK0N_113	NA	113	NA	GTX	CLK156_25MHZ1_N		
AD5	MGTREFCLK1N_113	NA	113	NA	GTX	CLK125MHZ1_N		
AD6	MGTREFCLK1P_113	NA	113	NA	GTX	CLK125MHZ1_P		
AH2	MGTXTXP1_113	NA	113	NA	GTX	fmc_tx9_p/1		
AF6	MGTXRXP1_113	NA	113	NA	GTX	fmc_rx9_p/1		
AH1	MGTXTXN1_113	NA	113	NA	GTX	fmc_tx9_n/1		
AF5	MGTXRXN1_113	NA	113	NA	GTX	fmc_rx9_n/1		
AK2	MGTXTXP0_113	NA	113	NA	GTX	fmc_tx8_p/1		
AG4	MGTXRXP0_113	NA	113	NA	GTX	fmc_rx8_p/1		
AK1	MGTXTXN0_113	NA	113	NA	GTX	fmc_tx8_n/1		
AG3	MGTXRXN0_113	NA	113	NA	GTX	fmc_rx8_n/1		
V2	MGTXTXP3_114	NA	114	NA	GTX	amc_tx8_p	AMC 8-11 / 100MHz	n/a
V6	MGTXRXP3_114	NA	114	NA	GTX	amc_rx8_p		
V1	MGTXTXN3_114	NA	114	NA	GTX	amc_tx8_n		
V5	MGTXRXN3_114	NA	114	NA	GTX	amc_rx8_n		
W4	MGTXTXP2_114	NA	114	NA	GTX	amc_tx9_p		
Y6	MGTXRXP2_114	NA	114	NA	GTX	amc_rx9_p		
W3	MGTXTXN2_114	NA	114	NA	GTX	amc_tx9_n		
W8	MGTREFCLK0P_114	NA	114	NA	GTX	CLK100MHZ1_P		
Y5	MGTXRXN2_114	NA	114	NA	GTX	amc_rx9_n		
W7	MGTREFCLK0N_114	NA	114	NA	GTX	CLK100MHZ1_N		
AA7	MGTREFCLK1N_114	NA	114	NA	GTX	(test point)		
AA8	MGTREFCLK1P_114	NA	114	NA	GTX	(test point)		
Y2	MGTXTXP1_114	NA	114	NA	GTX	amc_tx10_p		
AA4	MGTXRXP1_114	NA	114	NA	GTX	amc_rx10_p		
Y1	MGTXTXN1_114	NA	114	NA	GTX	amc_tx10_n		
AA3	MGTXRXN1_114	NA	114	NA	GTX	amc_rx10_n		
AB2	MGTXTXP0_114	NA	114	NA	GTX	amc_tx11_p		
AB6	MGTXRXP0_114	NA	114	NA	GTX	amc_rx11_p		
AB1	MGTXTXN0_114	NA	114	NA	GTX	amc_tx11_n		
AB5	MGTXRXN0_114	NA	114	NA	GTX	amc_rx11_n		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
N4	MGTXTXP3_115	NA	115	NA	GTX	amc_tx4_p	AMC 4-7 / FCLKA / 100MHz	n/a
M6	MGTXRXP3_115	NA	115	NA	GTX	amc_rx4_p		
N3	MGTXTXN3_115	NA	115	NA	GTX	amc_tx4_n		
M5	MGTXRXN3_115	NA	115	NA	GTX	amc_rx4_n		
P2	MGTXTXP2_115	NA	115	NA	GTX	amc_tx5_p		
P6	MGTXRXP2_115	NA	115	NA	GTX	amc_rx5_p		
P1	MGTXTXN2_115	NA	115	NA	GTX	amc_tx5_n		
R8	MGTREFCLK0P_115	NA	115	NA	GTX	CLK100MHZ0_P		
P5	MGTXRXN2_115	NA	115	NA	GTX	amc_rx5_n		
R7	MGTREFCLK0N_115	NA	115	NA	GTX	CLK100MHZ0_N		
U7	MGTREFCLK1N_115	NA	115	NA	GTX	FCLKA_N		
U8	MGTREFCLK1P_115	NA	115	NA	GTX	FCLKA_P		
T2	MGTXTXP1_115	NA	115	NA	GTX	amc_tx6_p		
R4	MGTXRXP1_115	NA	115	NA	GTX	amc_rx6_p		
T1	MGTXTXN1_115	NA	115	NA	GTX	amc_tx6_n		
R3	MGTXRXN1_115	NA	115	NA	GTX	amc_rx6_n		
U4	MGTXTXP0_115	NA	115	NA	GTX	amc_tx7_p		
T6	MGTXRXP0_115	NA	115	NA	GTX	amc_rx7_p		
U3	MGTXTXN0_115	NA	115	NA	GTX	amc_tx7_n		
T5	MGTXRXN0_115	NA	115	NA	GTX	amc_rx7_n		
F2	MGTXTXP3_116	NA	116	NA	GTX	fmc_tx9_p/0	AMC 0-1 / FMC-0 8-9 / 125MHz / 156.25MHz	n/a
H6	MGTXRXP3_116	NA	116	NA	GTX	fmc_rx9_p/0		
F1	MGTXTXN3_116	NA	116	NA	GTX	fmc_tx9_n/0		
H5	MGTXRXN3_116	NA	116	NA	GTX	fmc_rx9_n/0		
H2	MGTXTXP2_116	NA	116	NA	GTX	fmc_tx8_p/0		
J4	MGTXRXP2_116	NA	116	NA	GTX	fmc_rx8_p/0		
H1	MGTXTXN2_116	NA	116	NA	GTX	fmc_tx8_n/0		
J8	MGTREFCLK0P_116	NA	116	NA	GTX	CLK156_25MHZ2_P		
J3	MGTXRXN2_116	NA	116	NA	GTX	fmc_rx8_n/0		
J7	MGTREFCLK0N_116	NA	116	NA	GTX	CLK156_25MHZ2_N		
L7	MGTREFCLK1N_116	NA	116	NA	GTX	CLK125MHZ2_N		
L8	MGTREFCLK1P_116	NA	116	NA	GTX	CLK125MHZ2_P		
K2	MGTXTXP1_116	NA	116	NA	GTX	amc_tx1_p		
K6	MGTXRXP1_116	NA	116	NA	GTX	amc_rx1_p		
K1	MGTXTXN1_116	NA	116	NA	GTX	amc_tx1_n		
K5	MGTXRXN1_116	NA	116	NA	GTX	amc_rx1_n		
M2	MGTXTXP0_116	NA	116	NA	GTX	amc_tx0_p		
L4	MGTXRXP0_116	NA	116	NA	GTX	amc_rx0_p		
M1	MGTXTXN0_116	NA	116	NA	GTX	amc_tx0_n		
L3	MGTXRXN0_116	NA	116	NA	GTX	amc_rx0_n		

Pin	Pin Name	Memory Byte Group	Bank	VCCAUX Group	I/O Type	AMC502 Signal	AMC502 Note	VCCO
B6	MGTXTXP3_117	NA	117	NA	GTX	fmc_tx3_p/0	FMC-0 0-3 / GBTCLK0/0 / GBTCLK1/0	n/a
C4	MGTXRXP3_117	NA	117	NA	GTX	fmc_rx3_p/0		
B5	MGTXTXN3_117	NA	117	NA	GTX	fmc_tx3_n/0		
C3	MGTXRXN3_117	NA	117	NA	GTX	fmc_rx3_n/0		
A4	MGTXTXP2_117	NA	117	NA	GTX	fmc_tx2_p/0		
D6	MGTXRXP2_117	NA	117	NA	GTX	fmc_rx2_p/0		
A3	MGTXTXN2_117	NA	117	NA	GTX	rmc_tx2_n/0		
F6	MGTREFCLK0P_117	NA	117	NA	GTX	FMC_GBTCLK0_P/0		
D5	MGTXRXN2_117	NA	117	NA	GTX	fmc_rx2_n/0		
F5	MGTREFCLK0N_117	NA	117	NA	GTX	FMC_GBTCLK0_N/0		
G7	MGTREFCLK1N_117	NA	117	NA	GTX	FMC_GBTCLK1_N/0		
G8	MGTREFCLK1P_117	NA	117	NA	GTX	FMC_GBTCLK1_P/0		
B2	MGTXTXP1_117	NA	117	NA	GTX	fmc_tx1_p/0		
E4	MGTXRXP1_117	NA	117	NA	GTX	fmc_rx1_p/0		
B1	MGTXTXN1_117	NA	117	NA	GTX	fmc_tx1_n/0		
E3	MGTXRXN1_117	NA	117	NA	GTX	fmc_rx1_n/0		
D2	MGTXTXP0_117	NA	117	NA	GTX	fmc_tx0_p/0		
G4	MGTXRXP0_117	NA	117	NA	GTX	fmc_rx0_p/0		
D1	MGTXTXN0_117	NA	117	NA	GTX	rmc_tx0_n/0		
G3	MGTXRXN0_117	NA	117	NA	GTX	fmc_rx0_n/0		
A12	MGTXTXP3_118	NA	118	NA	GTX	fmc_tx7_p/0	FMC-0 4-7 / 125MHz / 156.25MHz	n/a
C12	MGTXRXP3_118	NA	118	NA	GTX	fmc_rx7_p/0		
A11	MGTXTXN3_118	NA	118	NA	GTX	fmc_tx7_n/0		
C11	MGTXRXN3_118	NA	118	NA	GTX	fmc_rx7_n/0		
B10	MGTXTXP2_118	NA	118	NA	GTX	fmc_tx6_p/0		
D10	MGTXRXP2_118	NA	118	NA	GTX	fmc_rx6_p/0		
B9	MGTXTXN2_118	NA	118	NA	GTX	fmc_tx6_n/0		
E8	MGTREFCLK0P_118	NA	118	NA	GTX	CLK156_25MHZ3_P		
D9	MGTXRXN2_118	NA	118	NA	GTX	fmc_rx6_n/0		
E7	MGTREFCLK0N_118	NA	118	NA	GTX	CLK156_25MHZ3_N		
H9	MGTREFCLK1N_118	NA	118	NA	GTX	CLK125MHZ3_N		
H10	MGTREFCLK1P_118	NA	118	NA	GTX	CLK125MHZ3_P		
A8	MGTXTXP1_118	NA	118	NA	GTX	fmc_tx5_p/0		
E12	MGTXRXP1_118	NA	118	NA	GTX	fmc_rx5_p/0		
A7	MGTXTXN1_118	NA	118	NA	GTX	fmc_tx5_n/0		
E11	MGTXRXN1_118	NA	118	NA	GTX	fmc_rx5_n/0		
C8	MGTXTXP0_118	NA	118	NA	GTX	fmc_tx4_p/0		
F10	MGTXRXP0_118	NA	118	NA	GTX	fmc_rx4_p/0		
C7	MGTXTXN0_118	NA	118	NA	GTX	fmc_tx4_n/0		
F9	MGTXRXN0_118	NA	118	NA	GTX	fmc_rx4_n/0		