Multi Processes Control Model

0
1 a1   Vcc1   b1 5
2 a2   b2 6
3 a3   b3 7
4 a4   GND   b4 8
0

Semaphore

Signal

Wait (Sleep)

Internal Clock

33 MHz

Nano to Micro Seconds

Process 1

Start Signal

SP 1

SP 2

SP n

Process 2

SP 1

Delay

Process 3

SP 1

SP n

o o o o

Delay

Process n

SP 1

SP 2

SP n