Lab 2

Tuesday, January 26, 2021 7:27 PM

Registers

```
EAX = 0000000B EBX = 006F1000 ECX = 4B1E2DB7 EDX = 008FF93C ESI = 00421005 EDI = 00421005 EIP = 00421018 ESP = 008FF95C EBP = 008FF968 EFL = 00000202
```

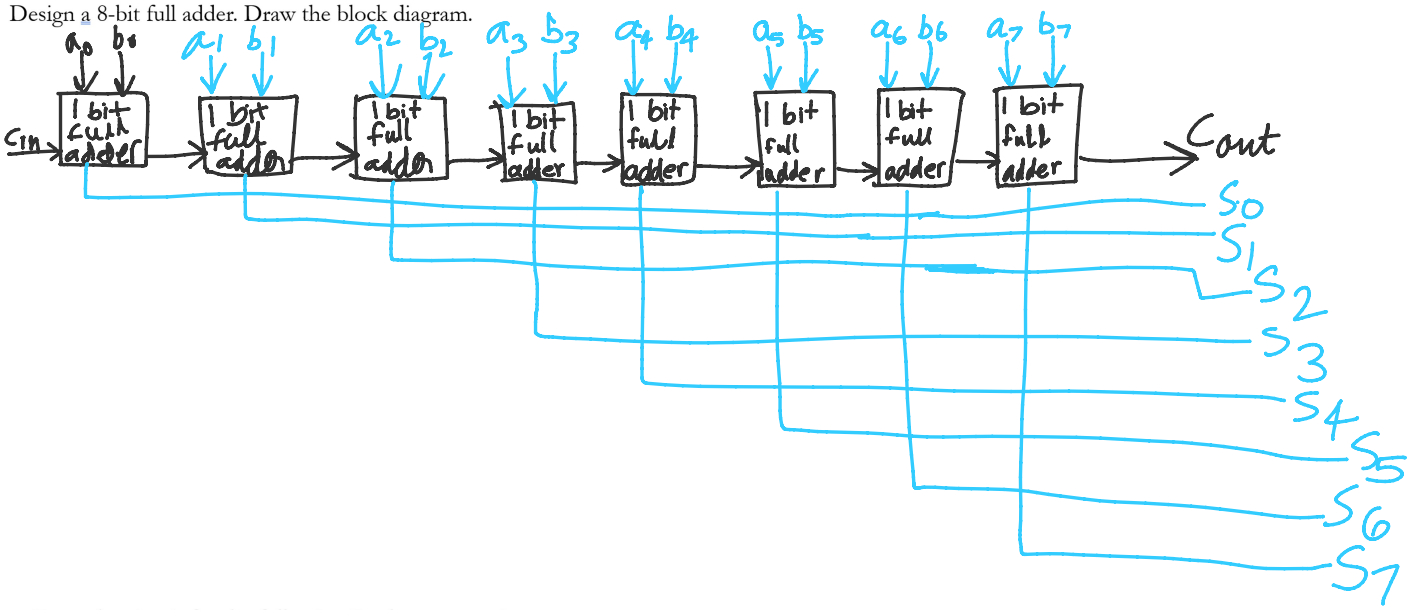
110 %

main.asm

```
1   .386
2   .model flat, stdcall
3   .stack 4096
4
5   ExitProcess PROTO, dwExitCode:DWORD
6
7   .code
8   main PROC
9       mov eax, 5
10      add eax, 6
11
12      INVOKE ExitProcess, 0  ≤ 1ms elapsed
13  main ENDP
14  END main
```

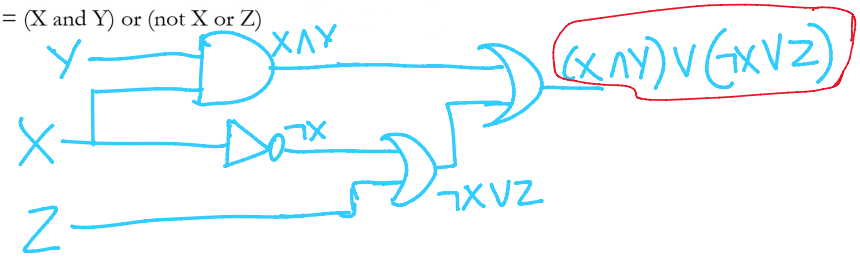
110 % No issues found Ln: 10 Ch: 12 Col: 15 TABS CRLF

Design a 8-bit full adder. Draw the block diagram.



Draw the circuit for the following Boolean expression:

P = (X and Y) or (not X or Z)



Store the following value in EAX register: 12784569h

4 bytes

Contents of EAX (Before)	Instruction	Contents of EAX (After)	CF	OF	SF	ZF
00000040	add eax, 40	00000080	0	0	0	0
FFFFFF40	add eax, 40	FFFFFF80	0	0	1	0
00000040	add eax, -40	00000000	0	0	0	1