

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

“JnanaSangama”, Belgaum -590014, Karnataka.



LAB REPORT

on

OBJECT ORIENTED MODELLING AND DESIGN

Submitted by

PREETHAM H (1BM19CS120)

in partial fulfillment for the award of the degree of

BACHELOR OF ENGINEERING

in

COMPUTER SCIENCE AND ENGINEERING



B.M.S. COLLEGE OF ENGINEERING

(Autonomous Institution under VTU)

BENGALURU-560019

May-2022 to July-2022

B. M. S. College of Engineering,
Bull Temple Road, Bangalore 560019
(Affiliated To Visvesvaraya Technological University, Belgaum)
Department of Computer Science and Engineering



CERTIFICATE

This is to certify that the Lab work entitled “**OBJECT ORIENTED MODELLING AND DESIGN**” Carried out by **PREETHAM H (1BM19CS125)**, who is bonafide student of **B. M. S. College of Engineering**. It is in partial fulfillment for the award of **Bachelor of Engineering in Computer Science and Engineering** of the Visvesvaraya Technological University, Belgaum during the year 2022. The Lab report has been approved as it satisfies the academic requirements in respect of **Object oriented modelling and design-(20CS6PCOMD)** work prescribed for the said degree.

Name of the Lab-In charge
Designation
Department of CSE
BMSCE, Bengaluru

Dr. Nandhini Vineeth
Assistant Professor
Department of CSE
BMSCE, Bengaluru

Index Sheet

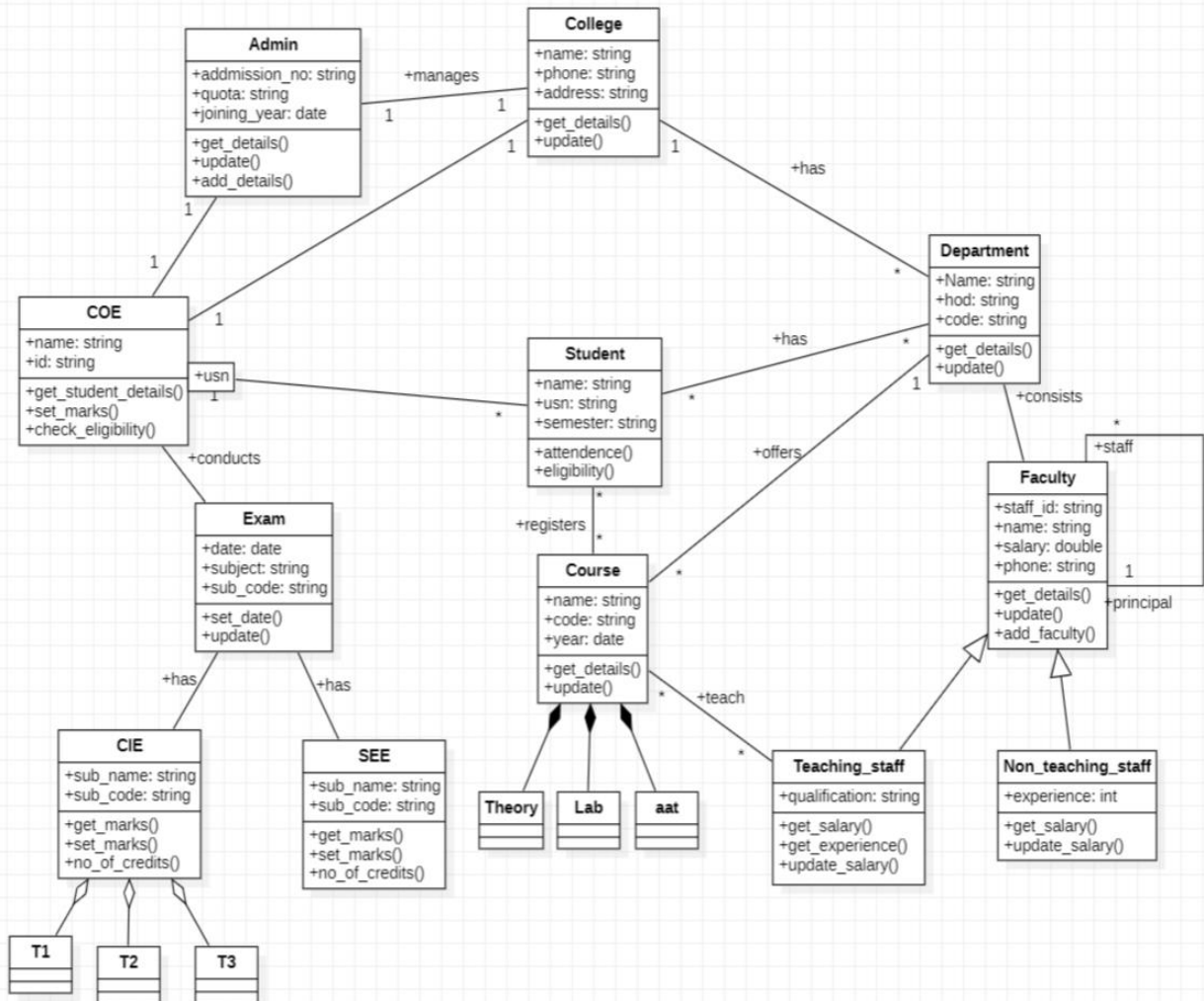
Sl. No.	Experiment Title	Page No.
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Course Outcome

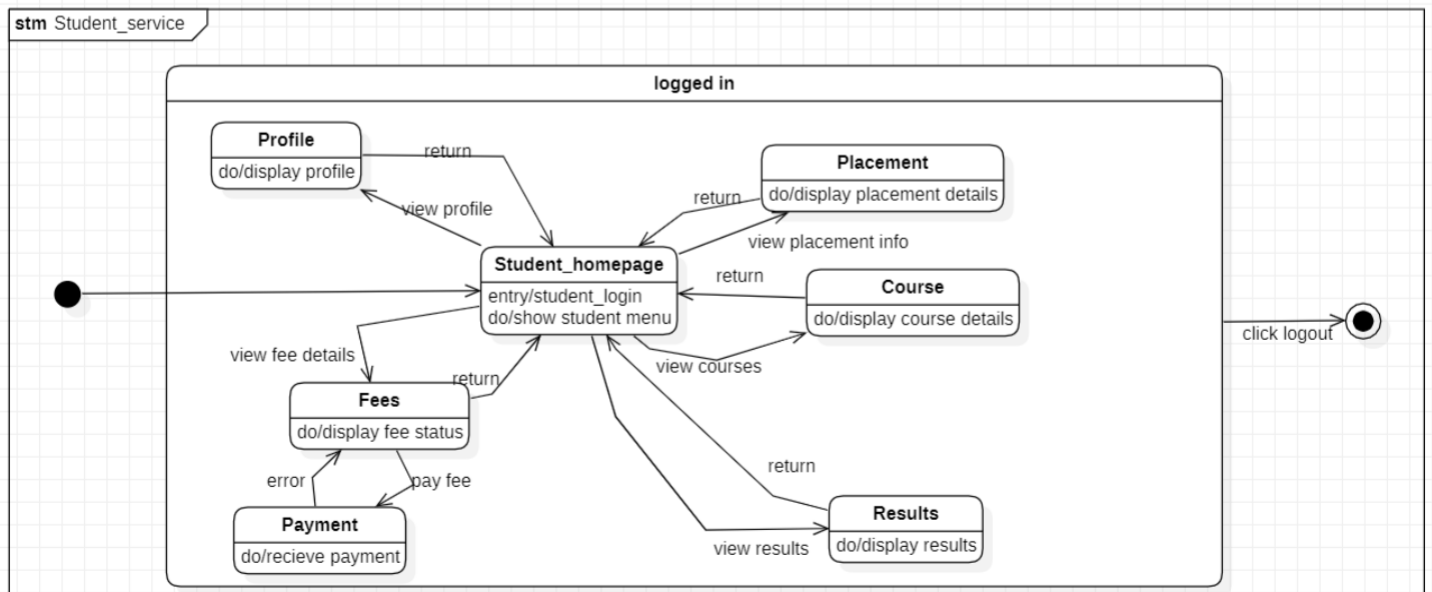
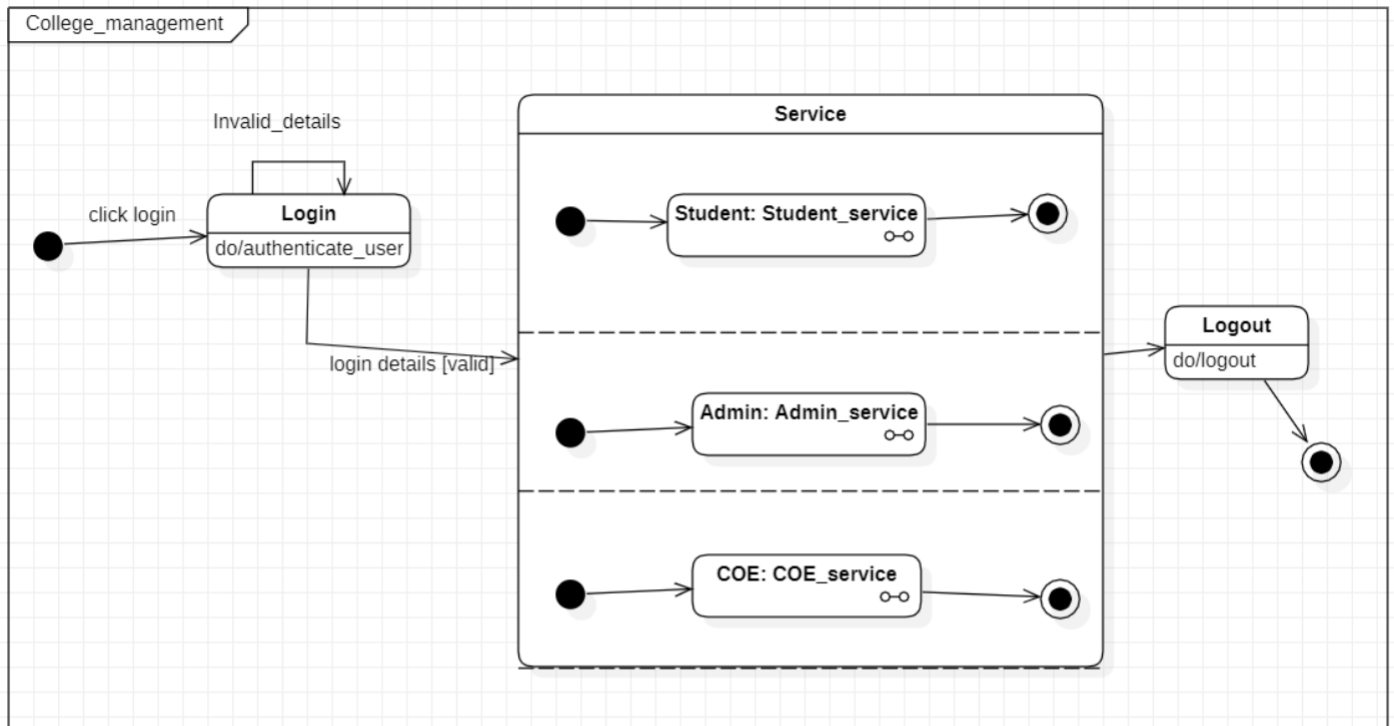
CO 1	Ability to apply the knowledge of class, State & Interaction Modelling using Unified Modeling Language to solve a given problem.
CO 2	Ability to analyze a System for a given requirement using Unified Modeling language
CO 3	Ability to design a given system using high level strategy.
CO 4	Ability to conduct practical experiment to solve a given problem using Unified Modeling language.

LAB 1

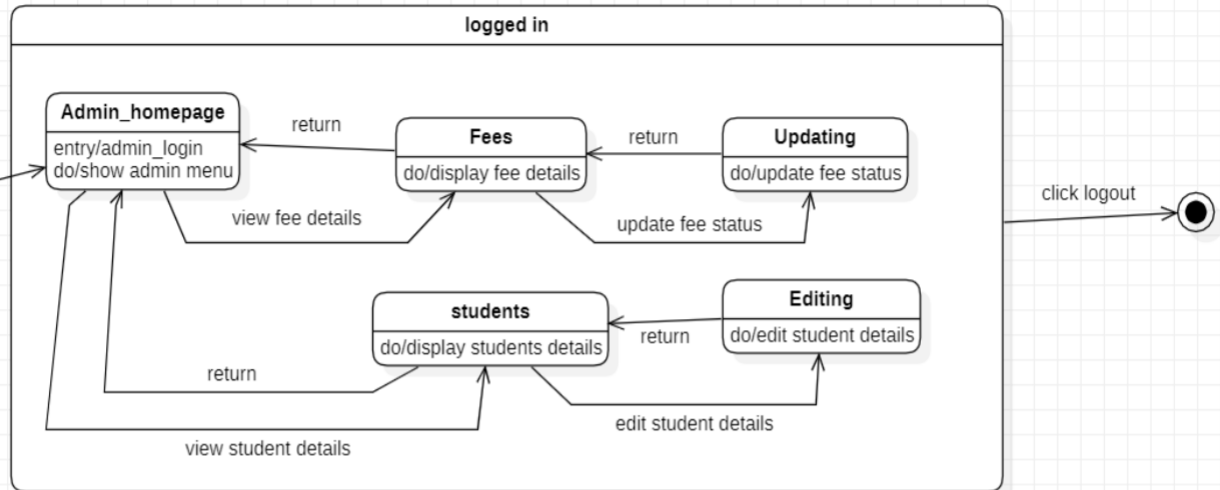
Class Diagram:



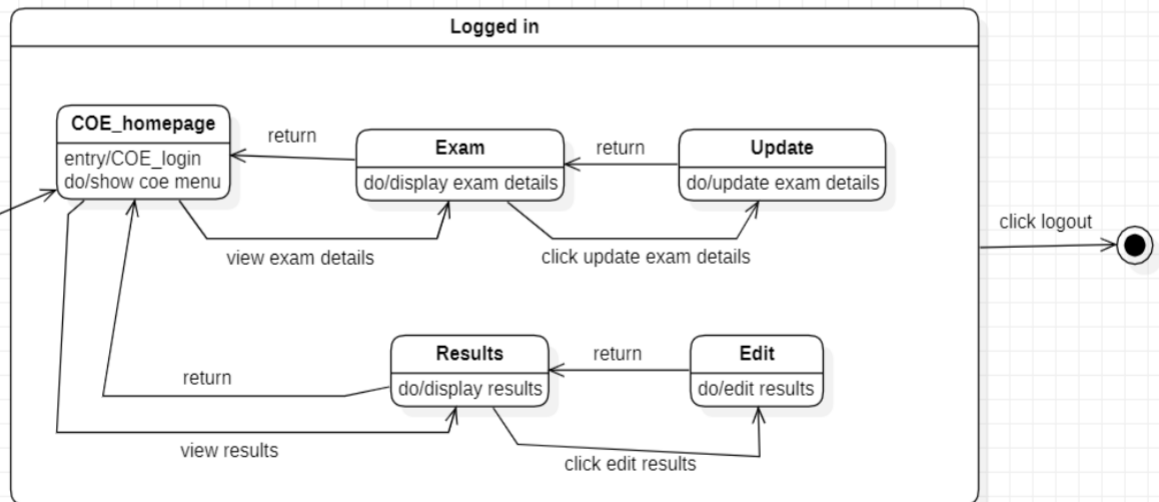
State diagram:



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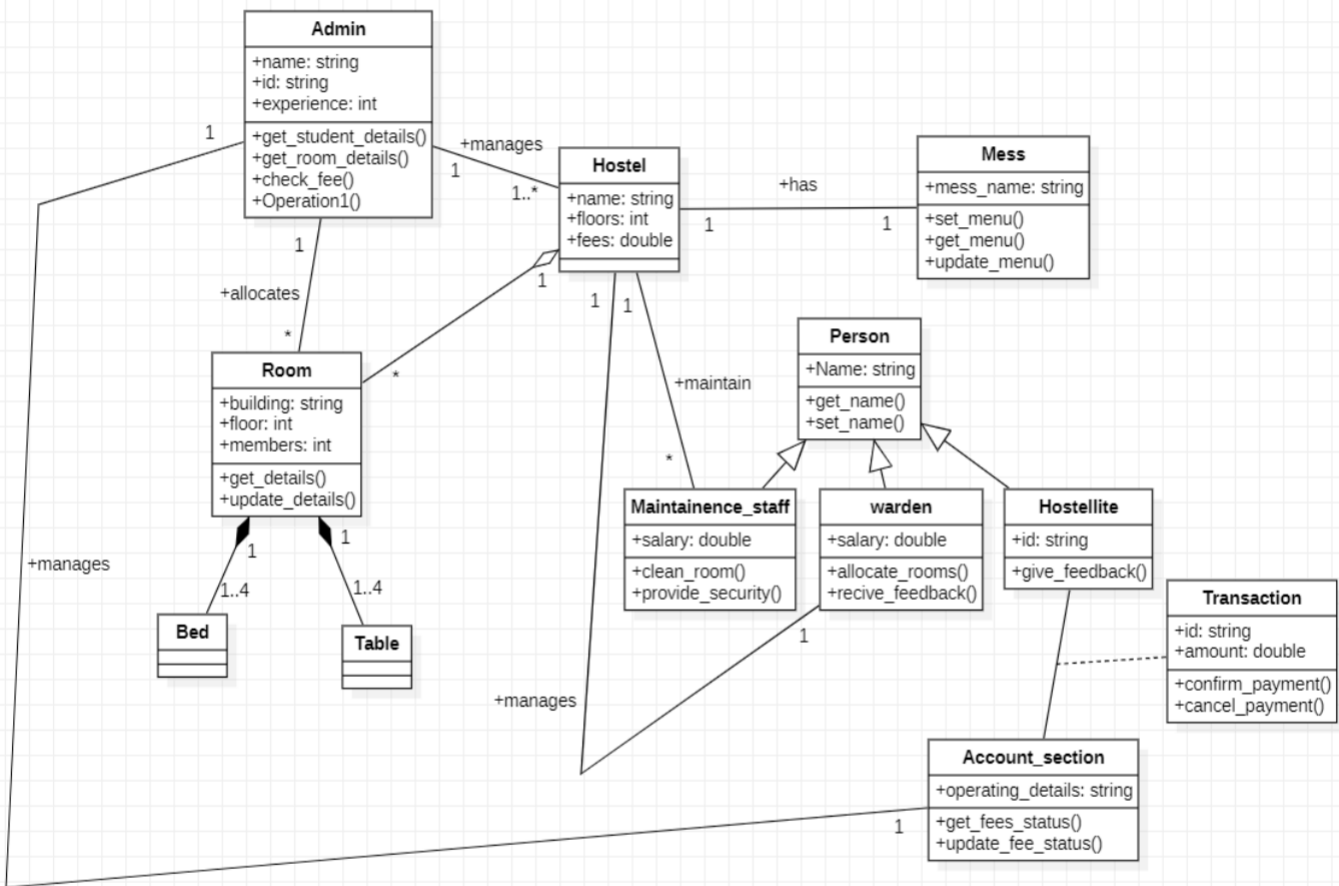


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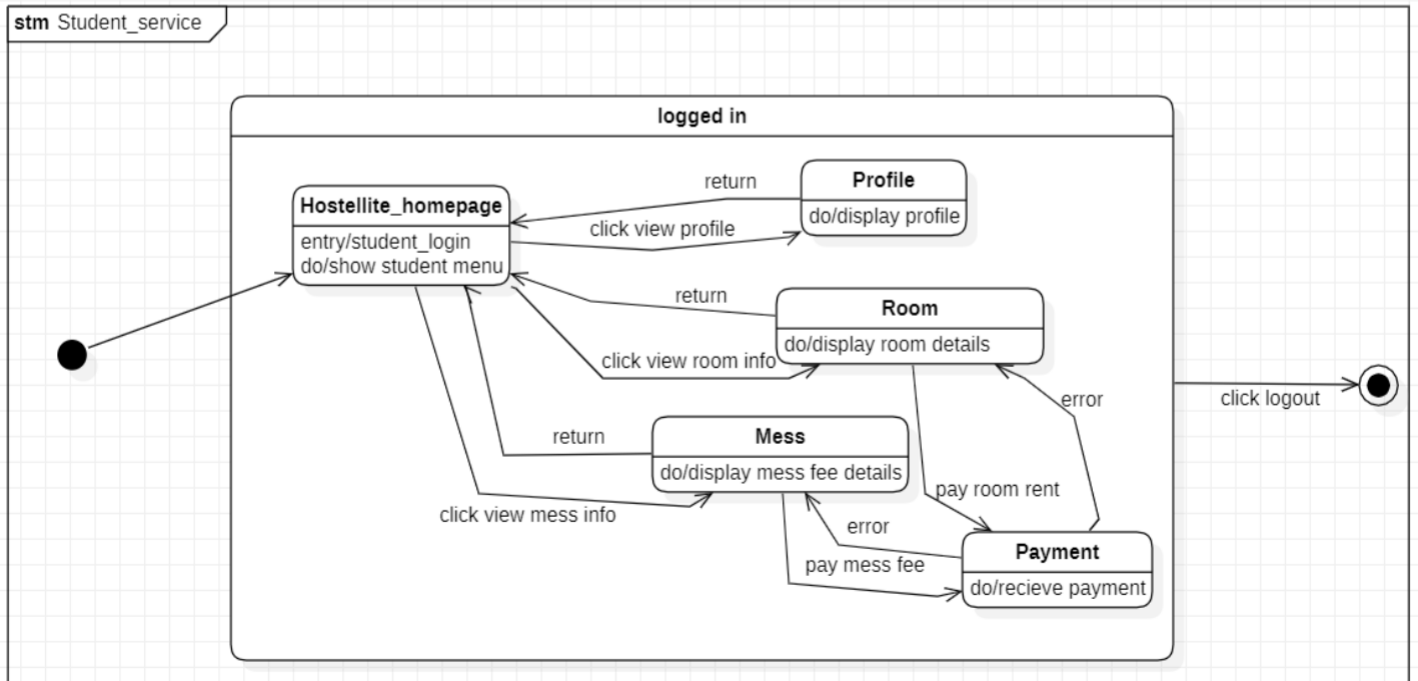
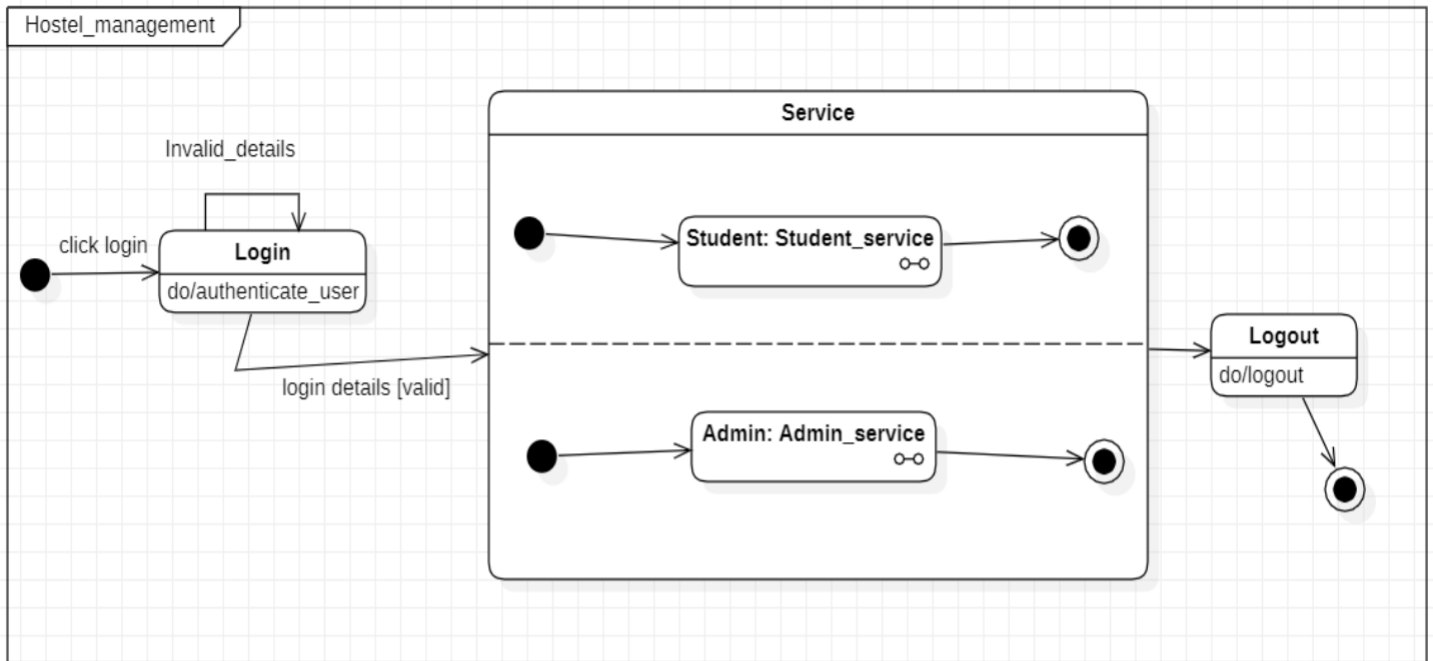


LAB 2

Class diagram:

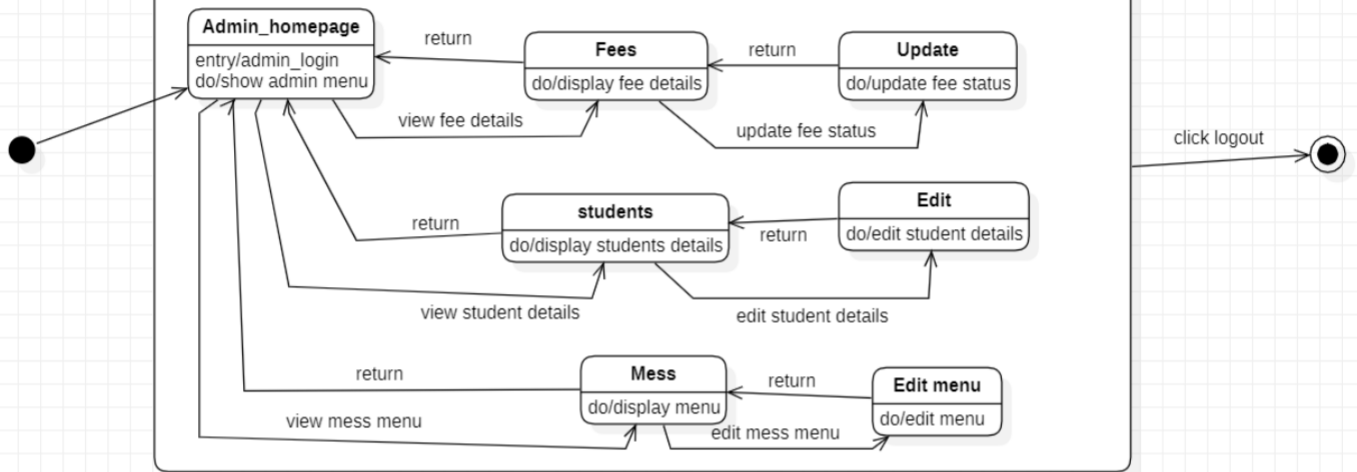


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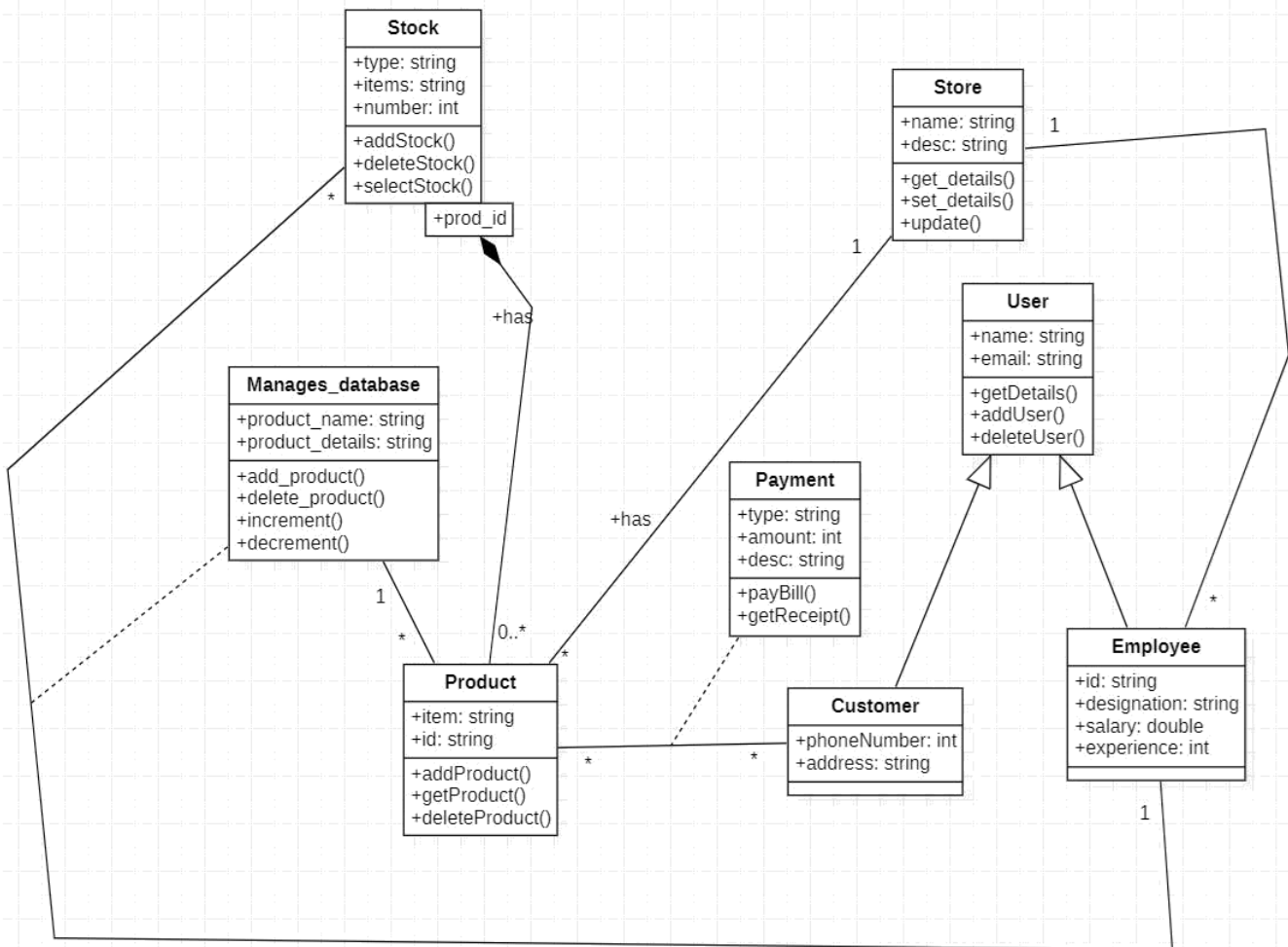
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logged in

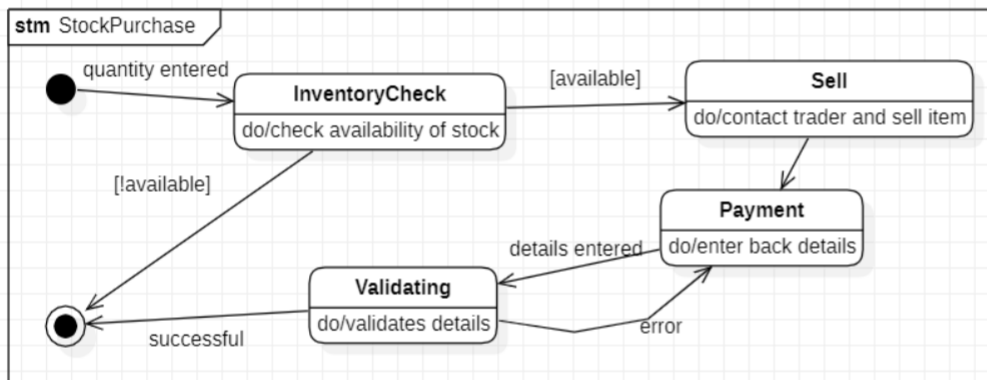
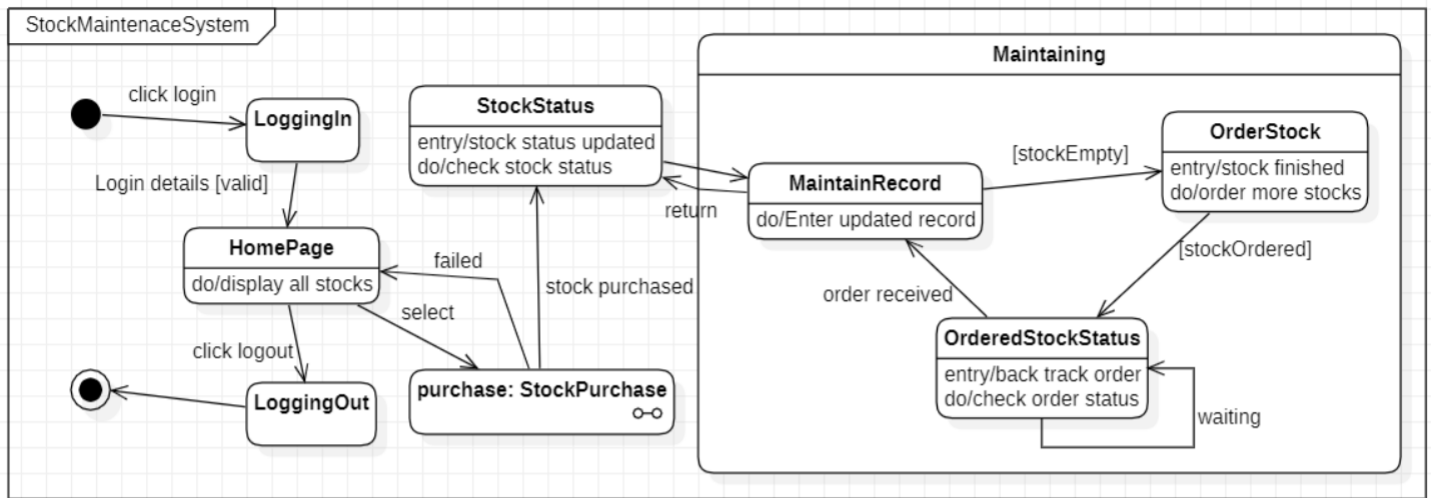


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Class diagram:

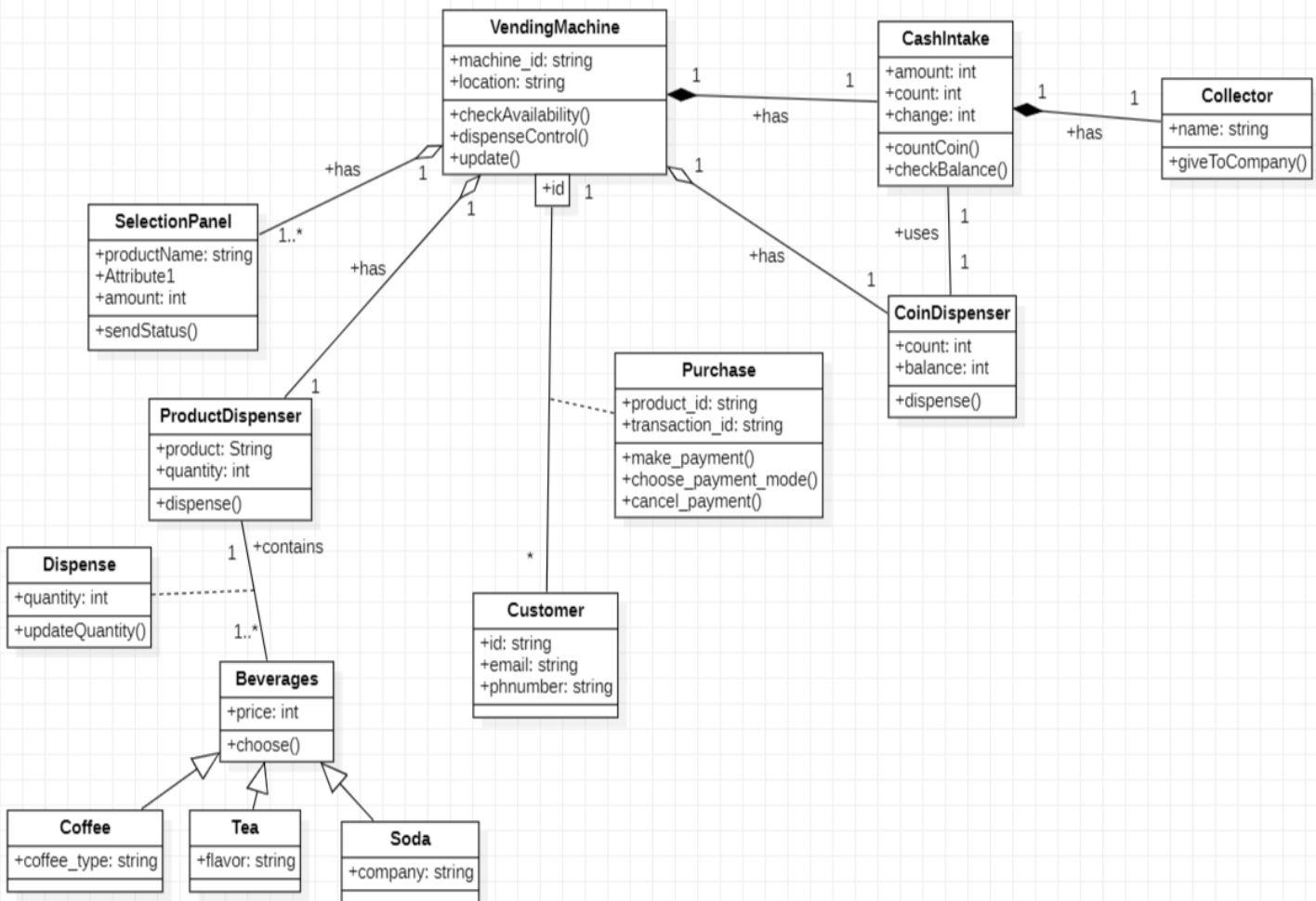


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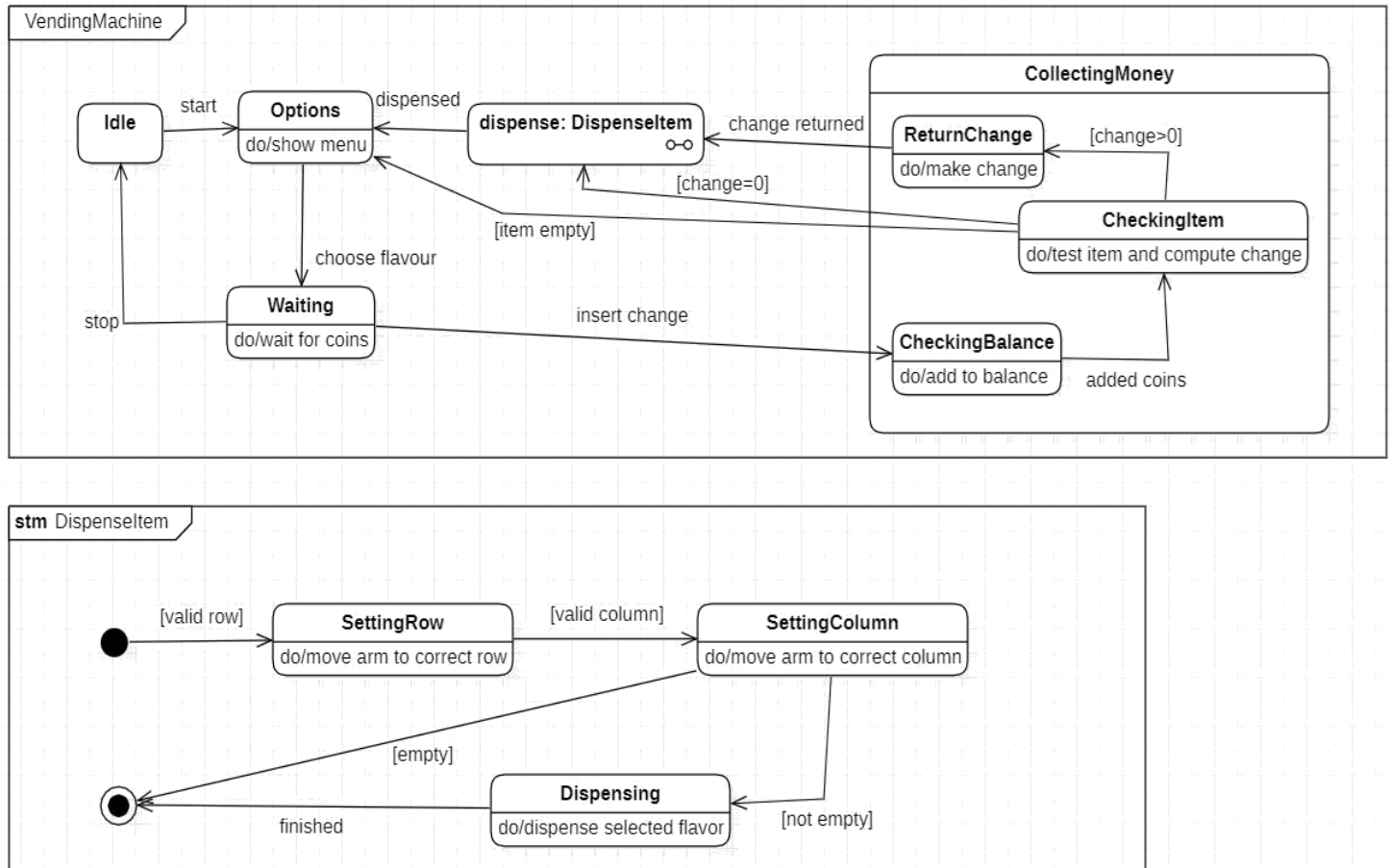


LAB 4

Class diagram:

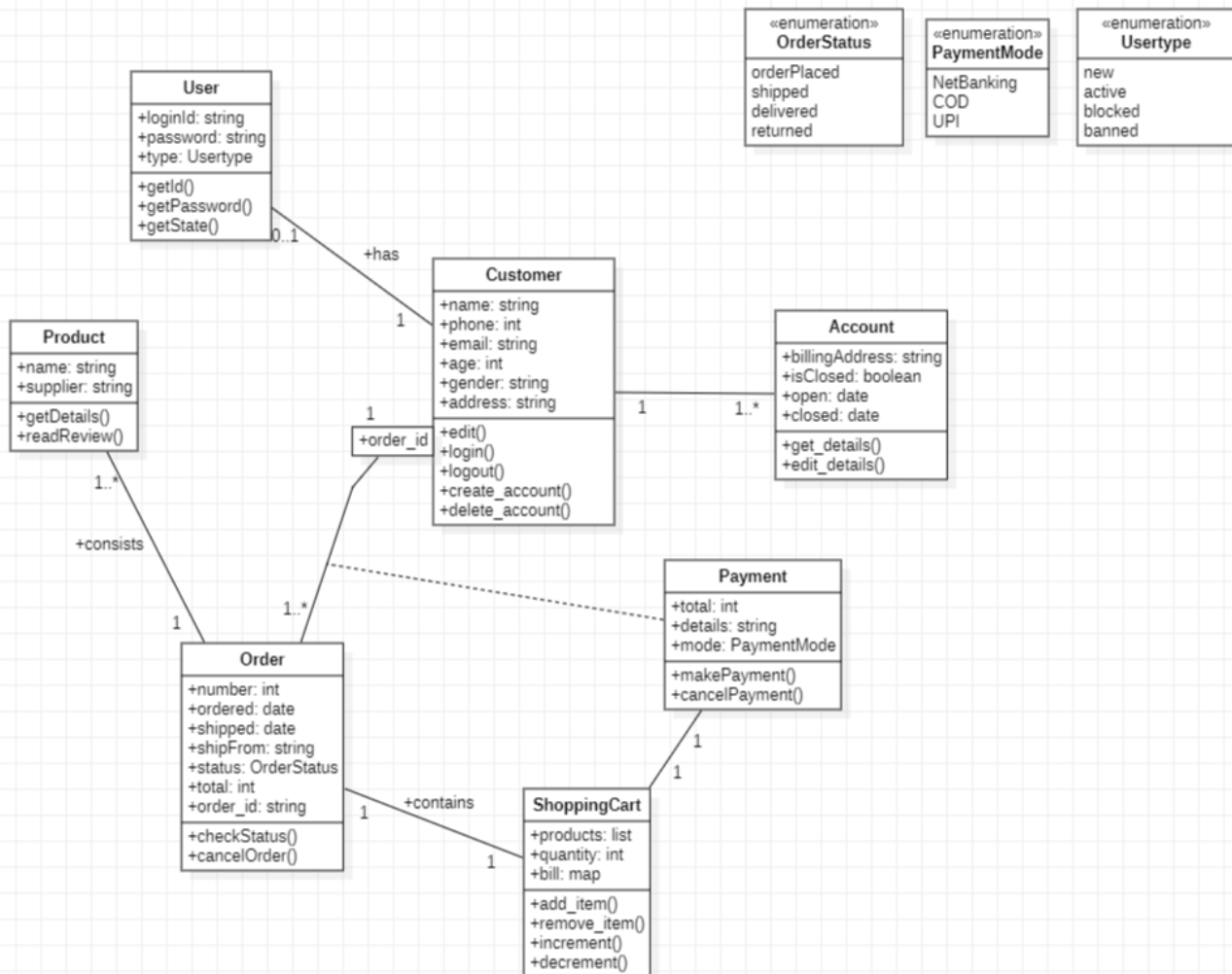


State diagram:

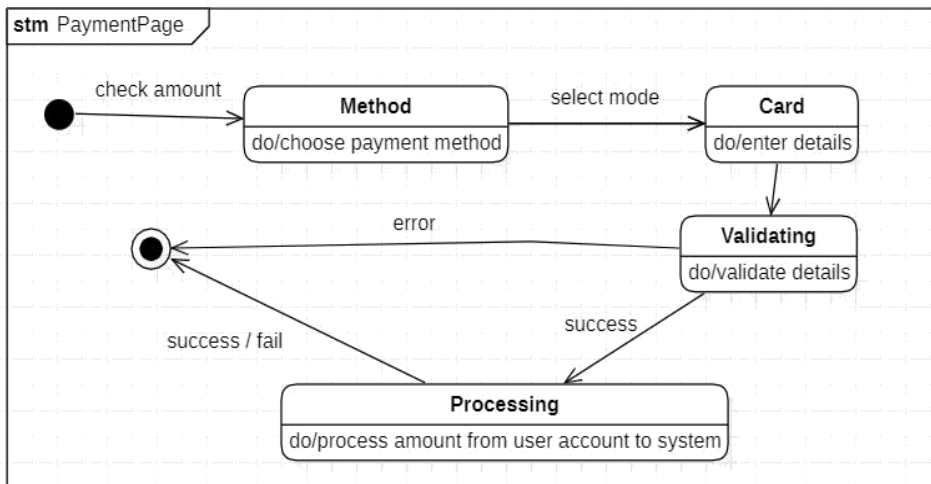
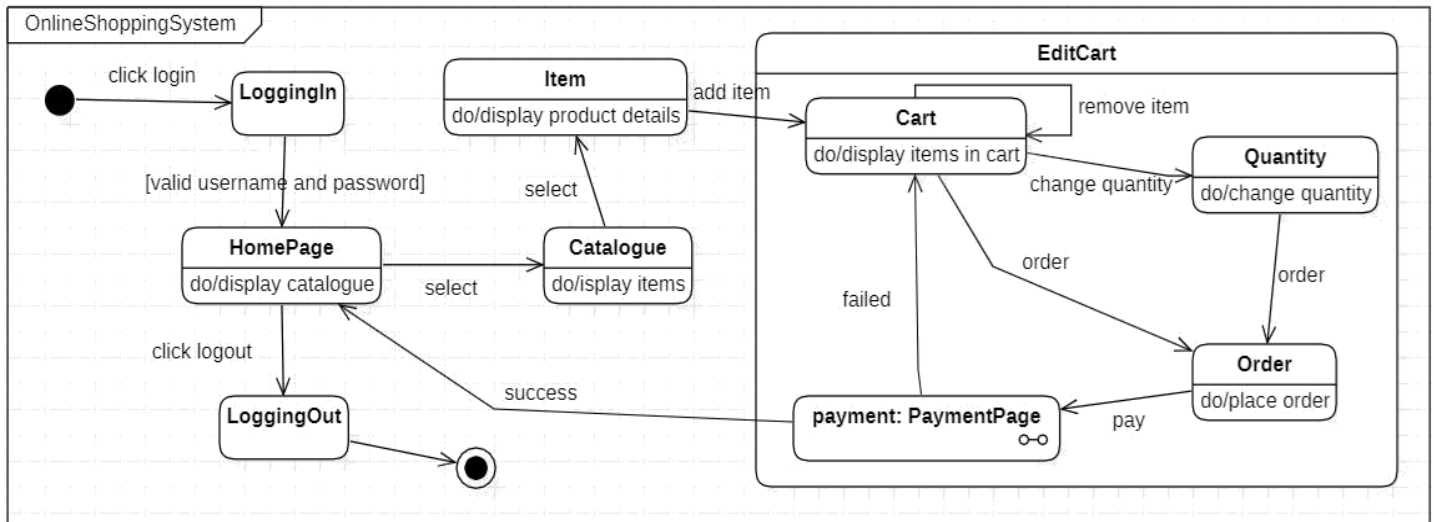


LAB 5

Class diagram:

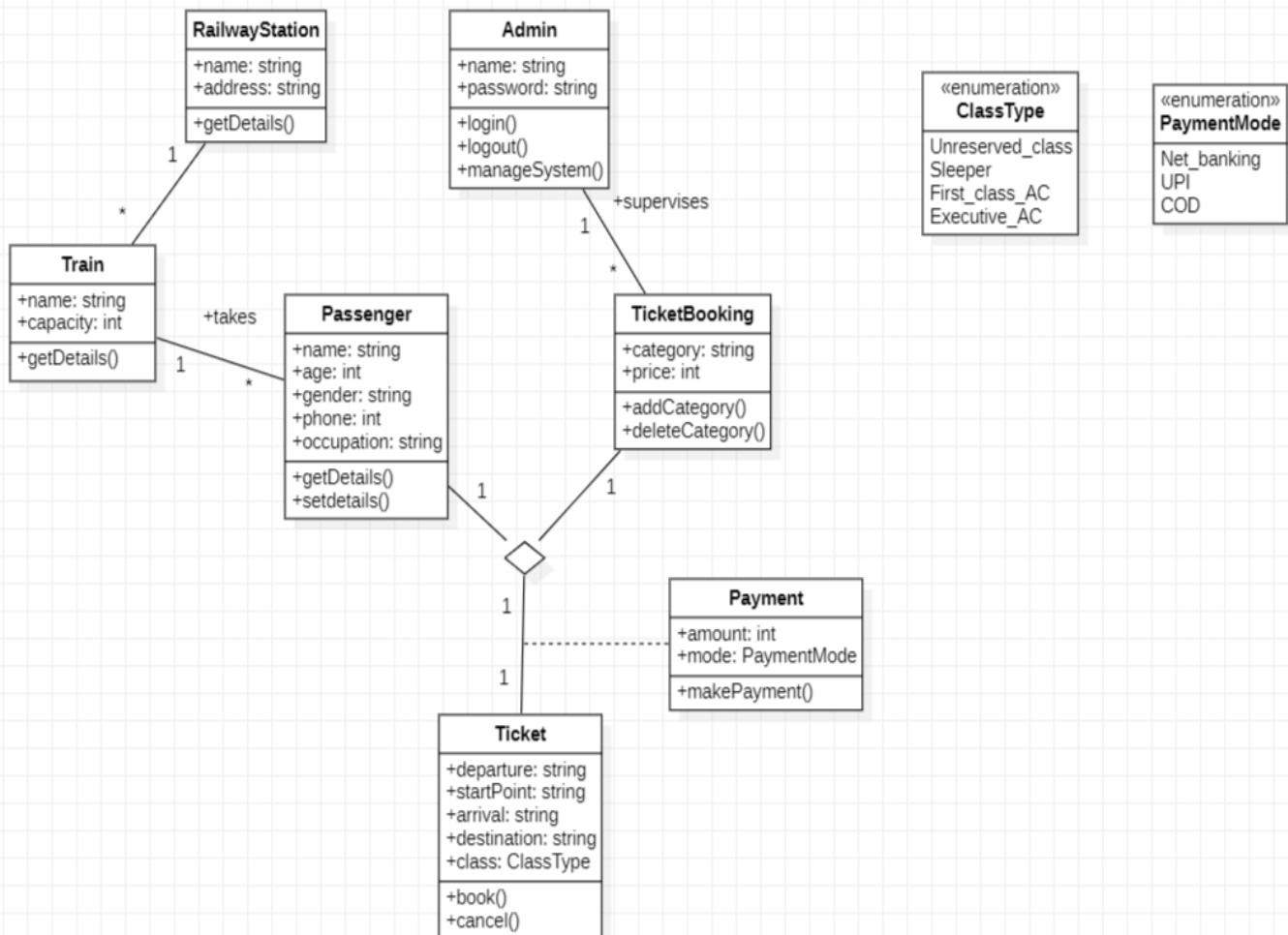


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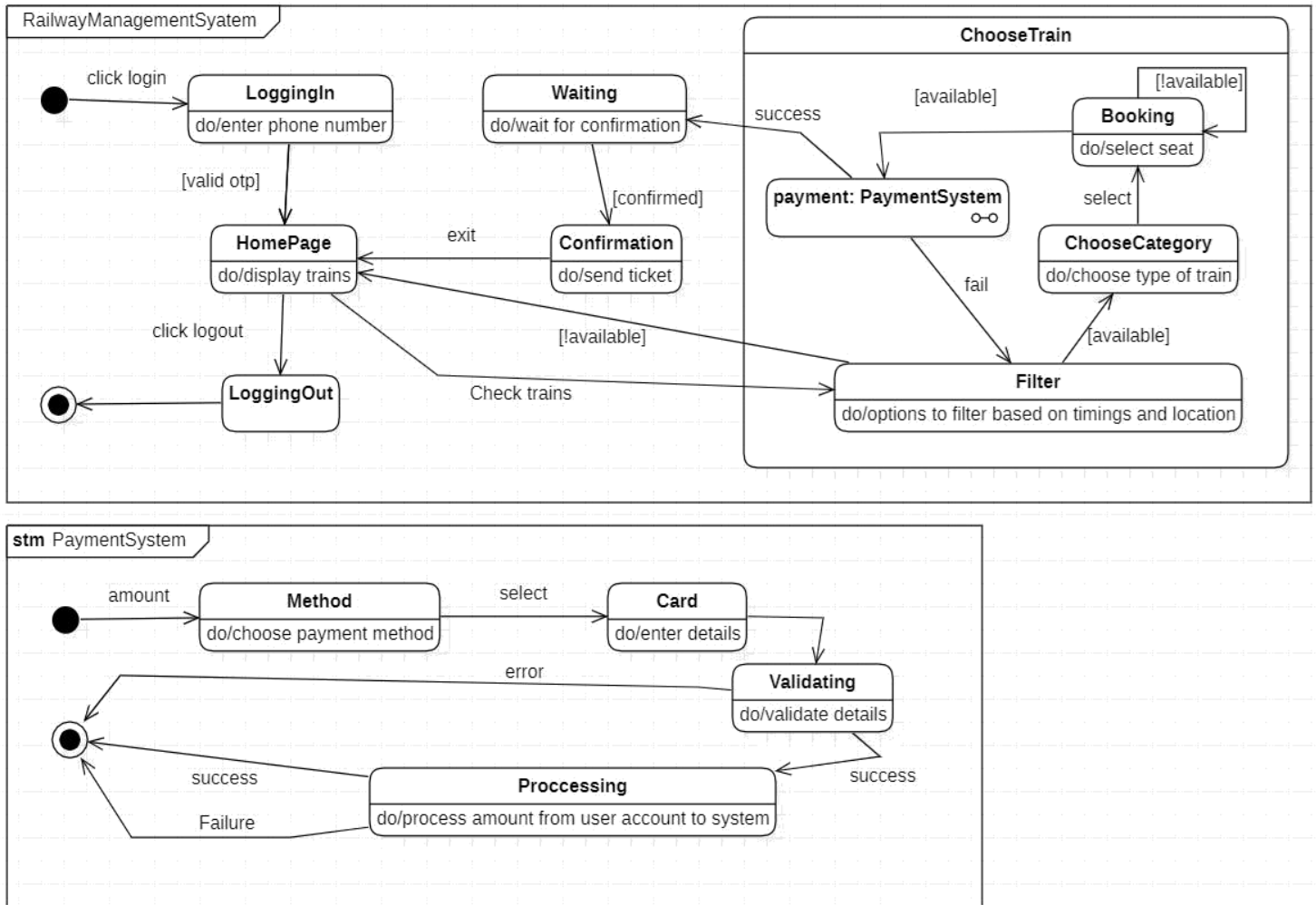


LAB 6

Class diagram:

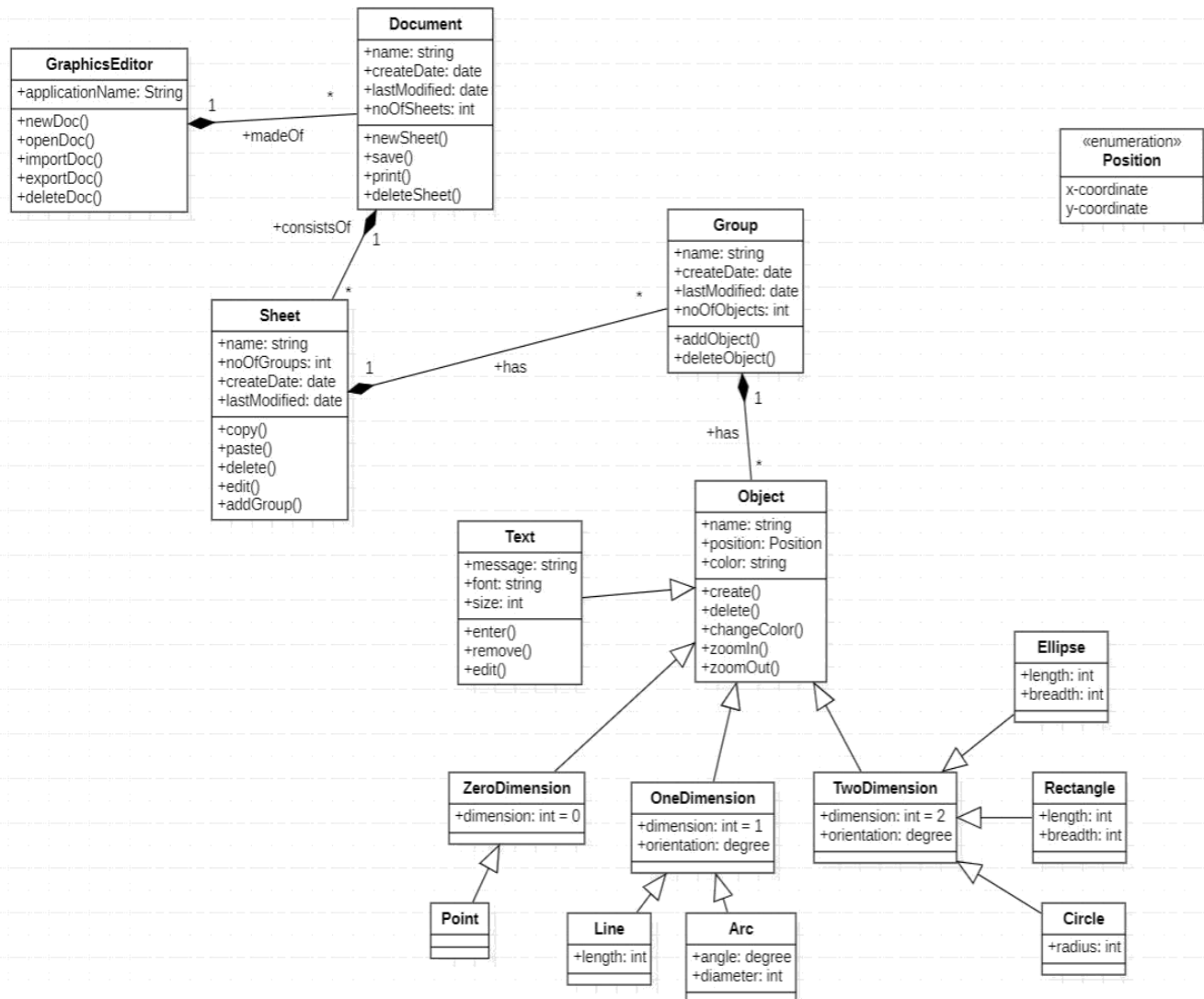


State diagram:



LAB 7

Class diagram:



State diagram:

