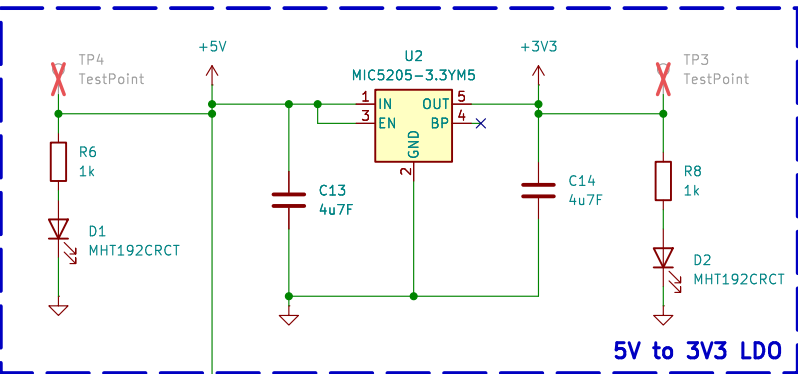
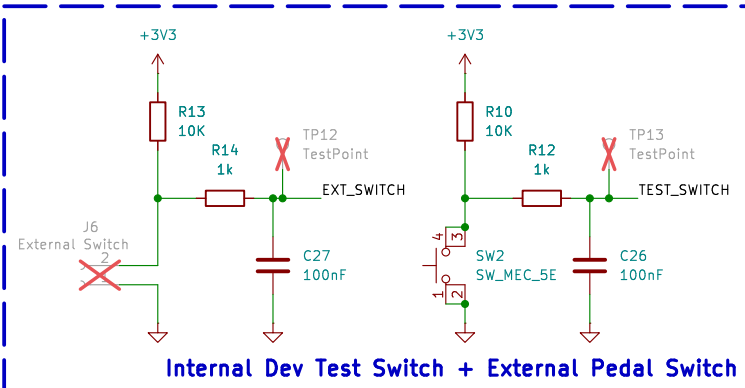


STM32WB55xx Datasheet
Page 82 – Decoupling Configuration
Page 38 – Wake up capable pins (PA0, PC13,
PC12, PA2, PC5)

MIC5205
150mA maximum current



5V to 3V3 LDO

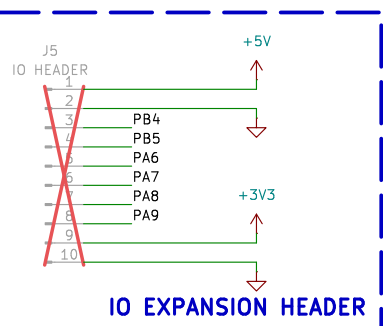


Internal Dev Test Switch + External Pedal Switch

NON BOM ITEMS			
<input checked="" type="checkbox"/> FID1	Fiducial	<input checked="" type="checkbox"/> H1	MountingHole
<input checked="" type="checkbox"/> FID2	Fiducial	<input checked="" type="checkbox"/> H2	MountingHole
<input checked="" type="checkbox"/> FID3	Fiducial	<input checked="" type="checkbox"/> H3	MountingHole
		<input checked="" type="checkbox"/> H4	MountingHole

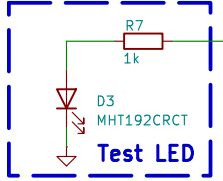
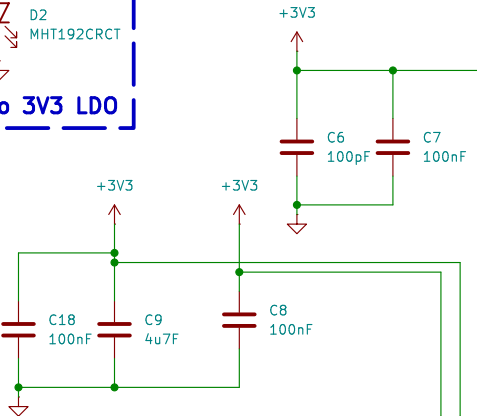
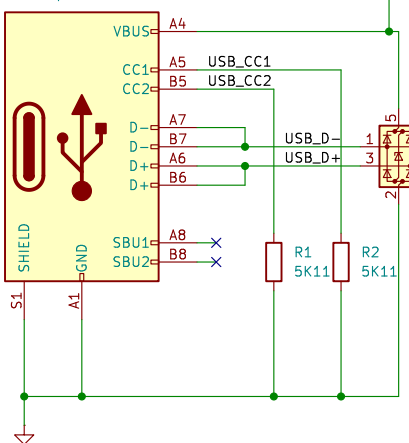
FABRICATION NOTE – Fiducials
Optimum size: 1mm Copper, 2mm mask
3–4 Fiducials on the edge of the PCB
3.35mm from edge of the board,
including clearance of the fiducial marker.

FABRICATION NOTE – Edge Clearance
0.5mm minimum edge clearance
Edge Rails – 5mm Minimum

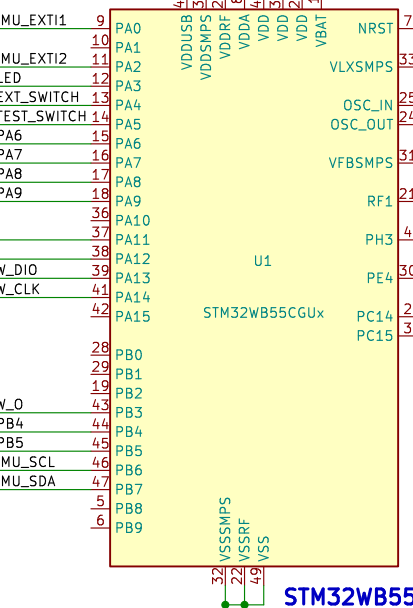


IO EXPANSION HEADER

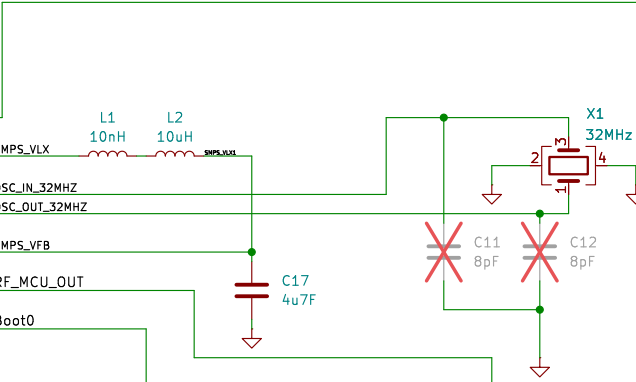
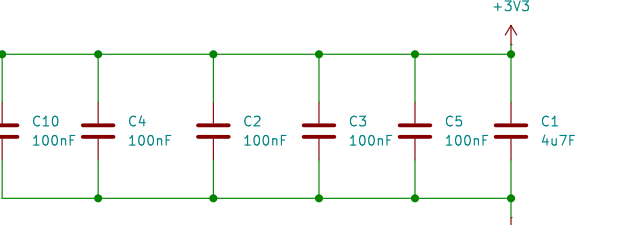
J1
USB_C_Receptacle_USB2.0_16P



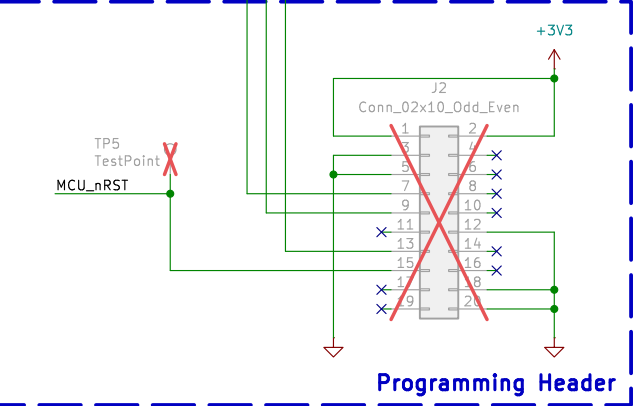
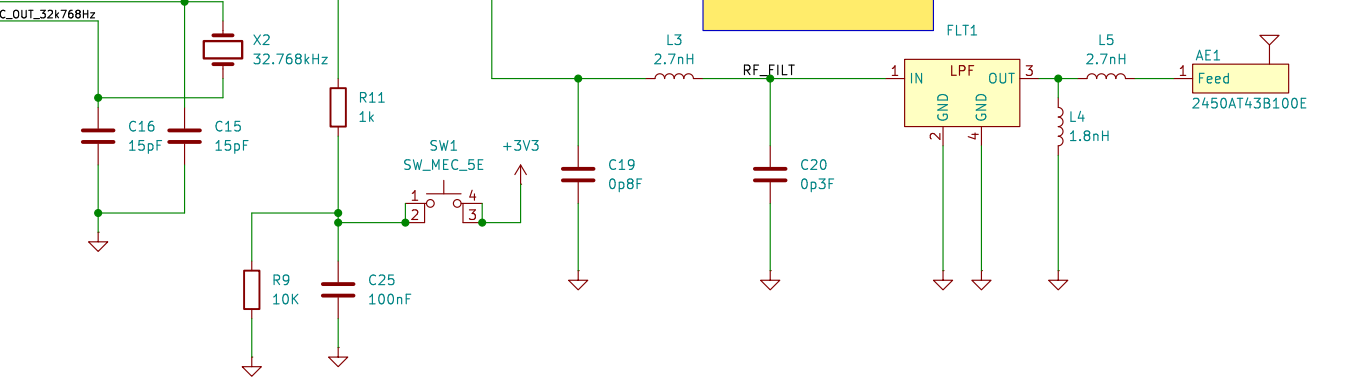
Test LED



STM32WB55



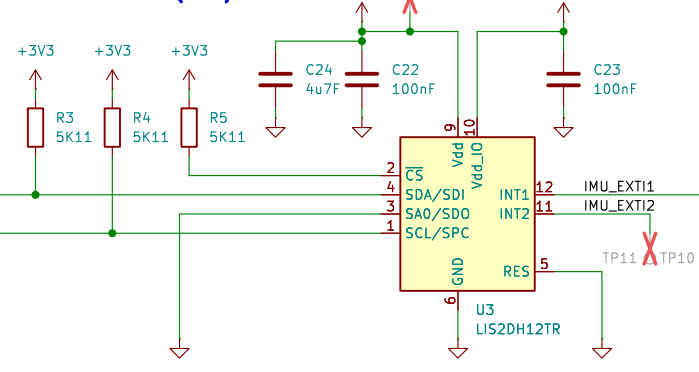
DESIGN NOTE:
AN5165 RF Hardware Design



Programming Header

ST-LINK V2 ADAPTER
ARM-JTAG-20-10

Accelerometer (IMU)



Design Note
CS is for SPI Enable
1 – I2C Enabled
0 – I2C Disabled

IMU Slave Address
0b0011000b
SA0 is the LSB for Address
SA0 is internally pulled high
~20k ohms at 3.3V

The Engineering Experience

Sheet: /
File: Blue_Pedal.kicad_sch

Title: Blue Pedal

Size: A3
Date: 2024-05-27
KiCad E.D.A. 8.0.7

Rev: A
Id: 1/1