

CHANAKYA UNIVERSITY

DATA STRUCTURE AND ALGORITHMS

NAME: PRERANA V

BRANCH: ELECTRONICS ENGINEERING(VLSI AND EMBEDDED
SYSTEMS)

REG NO: 24UG00452

OUTPUT:

1. The program first asks the user for the number of rows and columns, then takes the grid letters as input.

2. Next, the user enters a target word to search for in the grid.

3. The program then scans the grid:

Horizontally: It compares each sequence of letters in a row with the word.

Vertically: It compares each sequence of letters in a column with the word.

4. If the word is found, it prints the starting and ending positions (row and column indices).

5. If the word does not appear anywhere, it prints "Word not found."

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Enter number of rows and columns: 5 5
Enter the grid (uppercase letters only):
P R E R A
R P R E R
E R E R A
R A P R E
A R E R A
Enter the target word: PRERA
Start: (0,0) End: (0,4)
Start: (0,0) End: (4,0)

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Process exited after 28.53 seconds with return value 0
Press any key to continue . . .
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