

CHANAKYA UNIVERSITY

DATA STRUCTURE AND ALGORITHMS

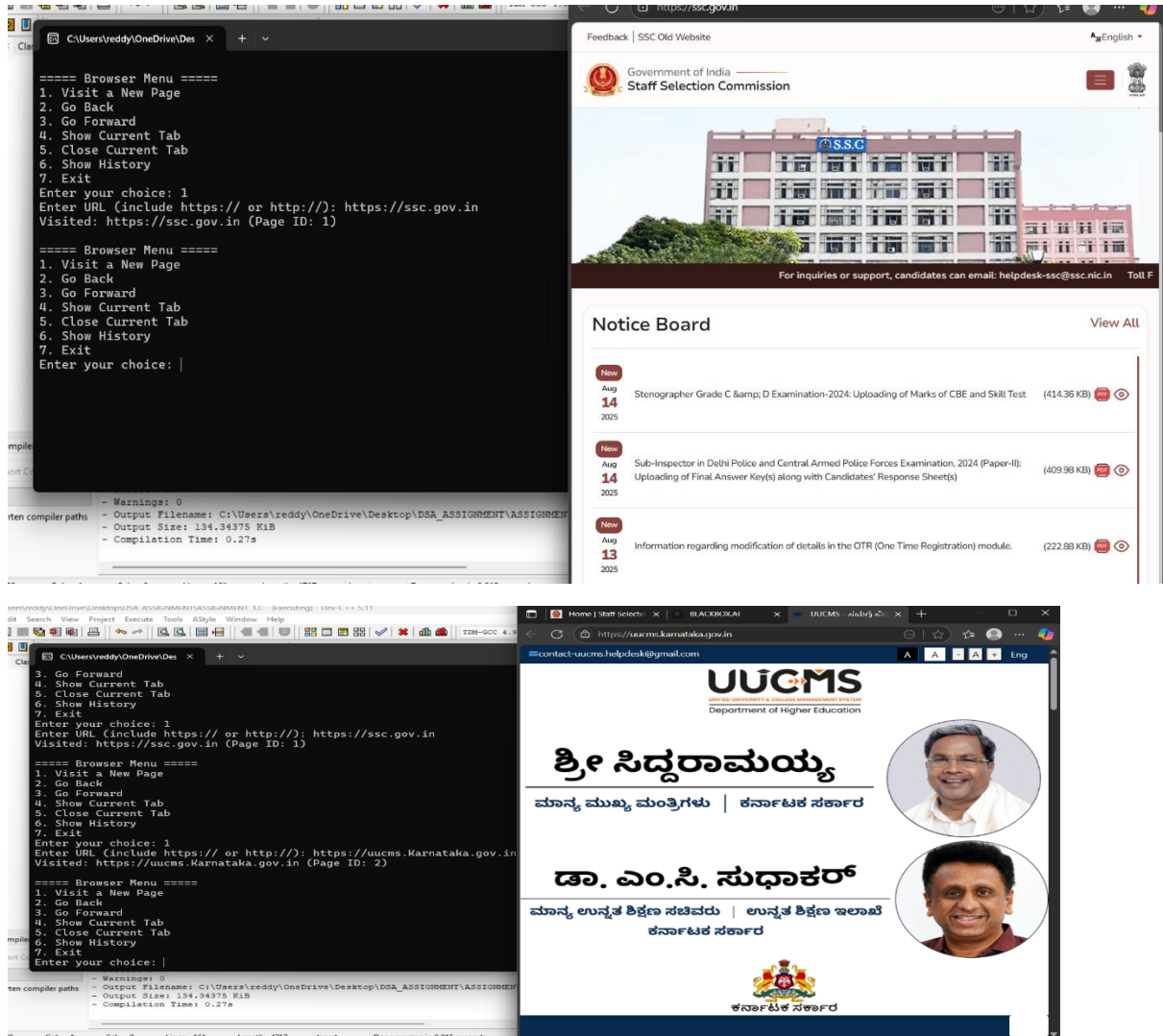
NAME: PRERANA V

BRANCH: ELECTRONICS ENGINEERING(VLSI AND EMBEDDED SYSTEMS)

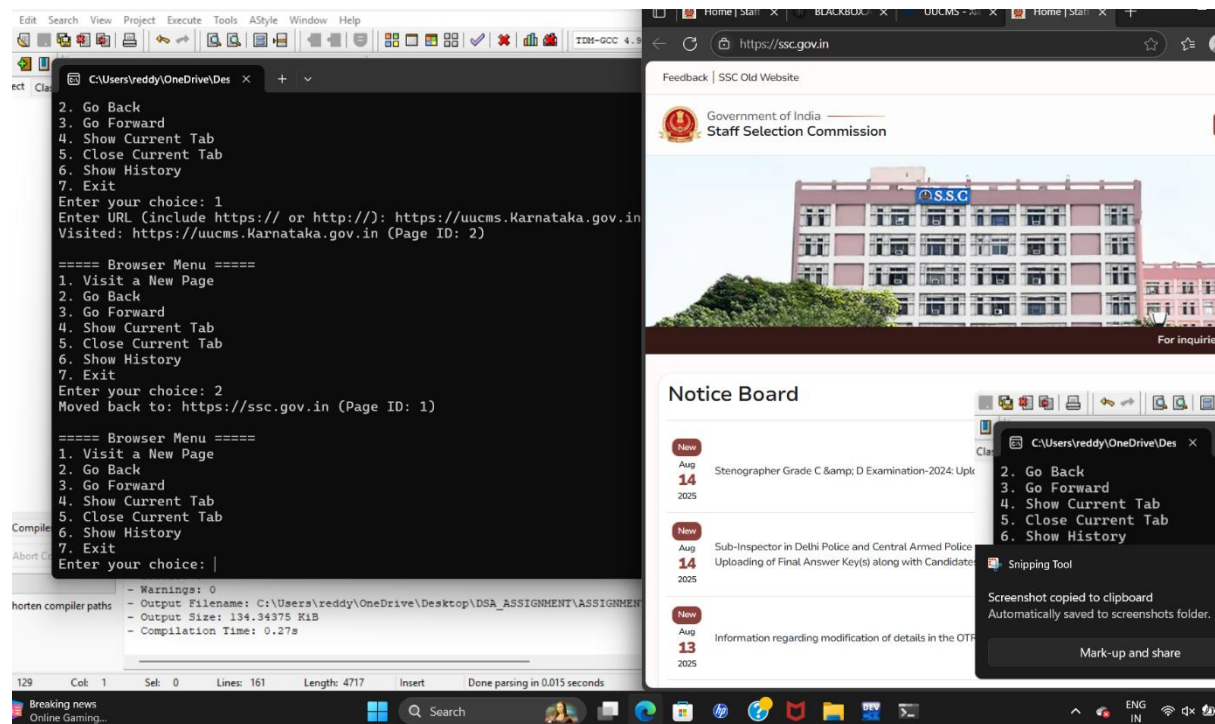
REG NO: 24UG00452

OUTPUTS OF CODE:

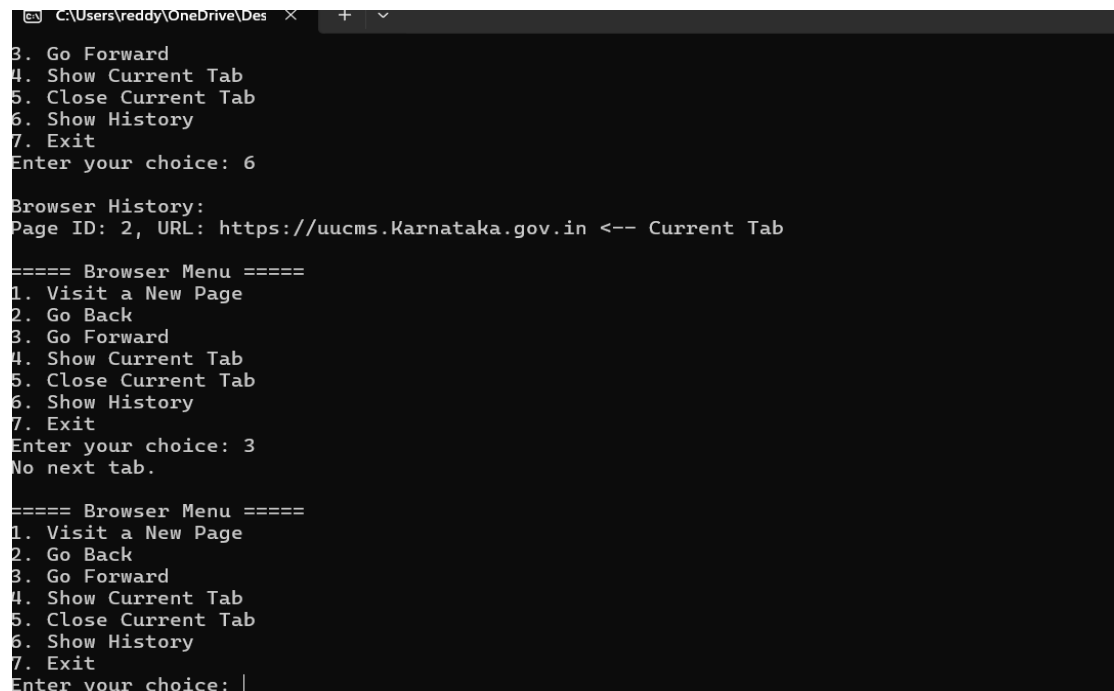
OUTPUT 1:



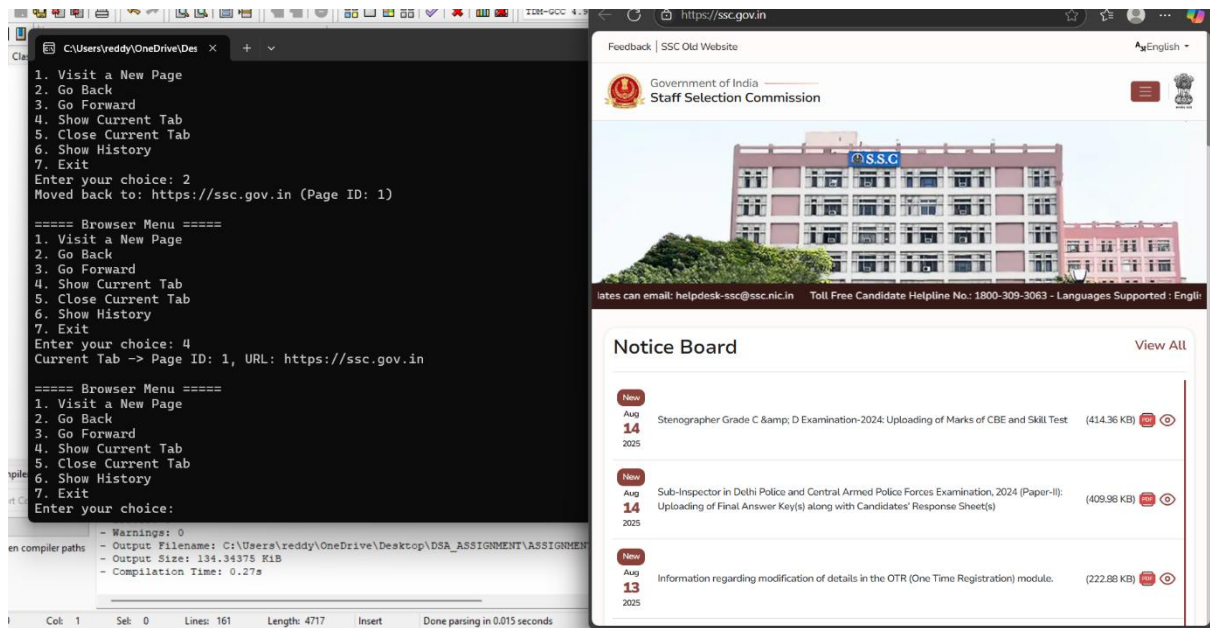
OUTPUT 2:



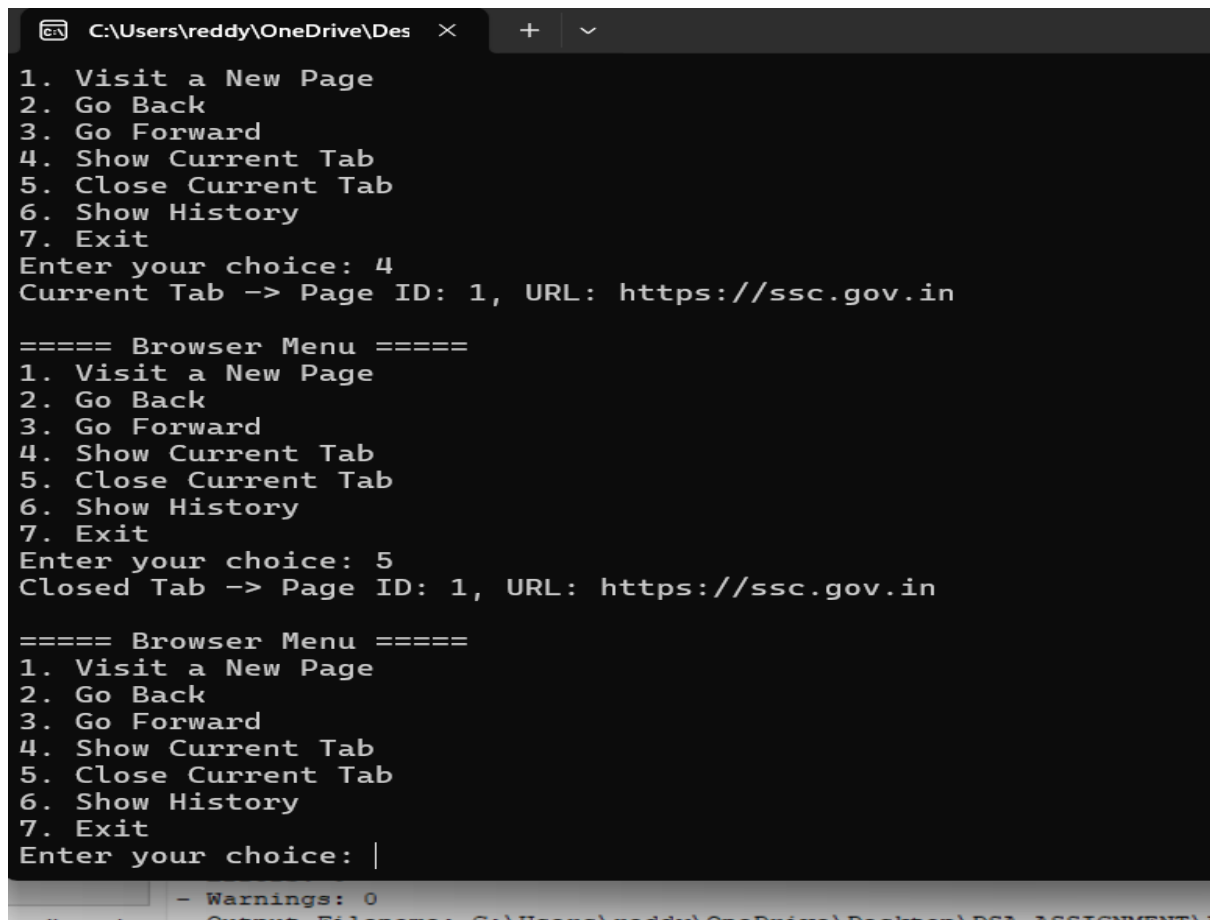
OUTPUT 3:



OUTPUT 4:



OUTPUT 5:



OUTPUT 6:

```
C:\Users\reddy\OneDrive\Des  X  +  v
3. Go Forward
4. Show Current Tab
5. Close Current Tab
6. Show History
7. Exit
Enter your choice: 5
Closed Tab -> Page ID: 1, URL: https://ssc.gov.in

===== Browser Menu =====
1. Visit a New Page
2. Go Back
3. Go Forward
4. Show Current Tab
5. Close Current Tab
6. Show History
7. Exit
Enter your choice: 6

Browser History:
Page ID: 2, URL: https://uucms.Karnataka.gov.in <-- Current Tab

===== Browser Menu =====
1. Visit a New Page
2. Go Back
3. Go Forward
4. Show Current Tab
5. Close Current Tab
6. Show History
7. Exit
Enter your choice: |
```

OUTPUT 7:

```
C:\Users\reddy\OneDrive\Des  X  +  v
Enter your choice: 6

Browser History:
Page ID: 2, URL: https://uucms.Karnataka.gov.in <-- Current Tab

===== Browser Menu =====
1. Visit a New Page
2. Go Back
3. Go Forward
4. Show Current Tab
5. Close Current Tab
6. Show History
7. Exit
Enter your choice: 3
No next tab.

===== Browser Menu =====
1. Visit a New Page
2. Go Back
3. Go Forward
4. Show Current Tab
5. Close Current Tab
6. Show History
7. Exit
Enter your choice: 7
Exiting browser...

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Process exited after 236.1 seconds with return value 0
Press any key to continue . . . |
```