**SCADDER**

Spacecraft Cyber Anomaly Threat Detection, Deterrent and Reconnaissance

horizontal line

# 

# Introduction

# Setting up the Testbed

PetaLinux needs to be installed on Xilinx-Zynq-ZC706 FPGA. Make sure Vivado is licensed and pre-installed on the workstation.

Steps to boot the pre-built Image from SD Card:

1. Mount your SD card on the host machine
2. Copy the files ‘*BOOT.BIN*’ and ‘*image.ub*’ from *<plnx-proj-root>/pre-built/linux/images/* into the root directory of the first FAT partition of the SD card
3. Connect between the FPGA board and workstation using the Ethernet wire
4. Open the console on the workstation
5. Power off the FPGA board
6. On FPGA board, change ‘Boot mode’ into ‘SD boot’
7. Remove SD card from the host machine and plug it into the FPGA board
8. Power on the board
9. In console, hit any key to stop the automatic boot

# MIL-STD-1553 Simulation