

C3V3	C3V3	RESET_n	CLKSEL	MISO	SRCLK	DIRCHAN	HWCON	DGND	S3_D2	S3_D0	S3_BCK	DGND	S2_D2	S2_D0	S2_BCK	DGND	S1_D2	S1_D0	S1_BCK	DGND	S0_D2	S0_D0	S0_BCK
48	46	44	42	40	38	36	34	32	30	28	26	24	22	20	18	16	14	12	10	8	6	4	2
47	45	43	41	39	37	35	33	31	29	27	25	23	21	19	17	15	13	11	9	7	5	3	1
MCLK	DGND	SCLK	DGND	MOSI	SRCLK2	CS_n	HWFLAG	DGND	S3_D3	S3_S1	S3_LRCK	DGND	S2_D3	S2_D1	S2_LRCK	DGND	S1_D3	S1_D1	S1_LRCK	DGND	S0_D3	S0_D1	S0_LRCK