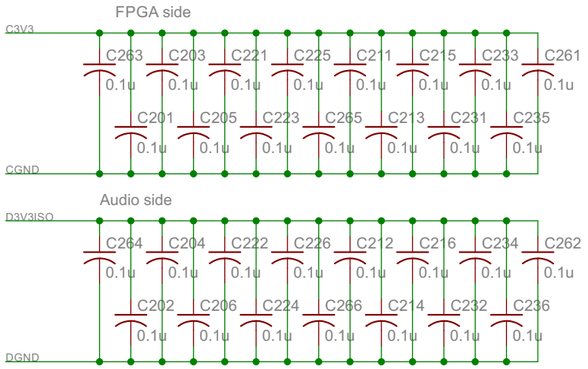
Decoupling for Isolators

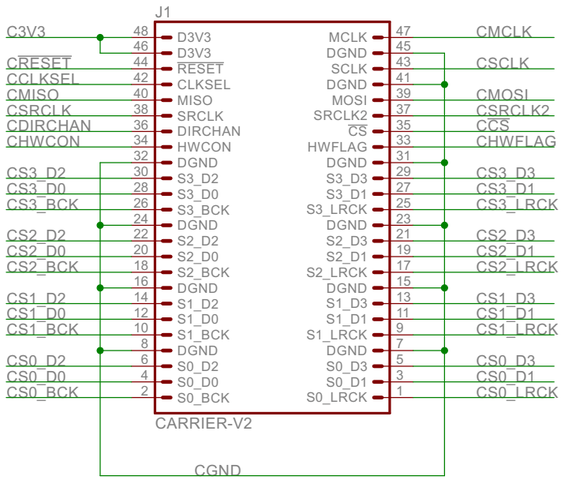
Only needed if using isolators

Place between pins 1/2 and 15/16

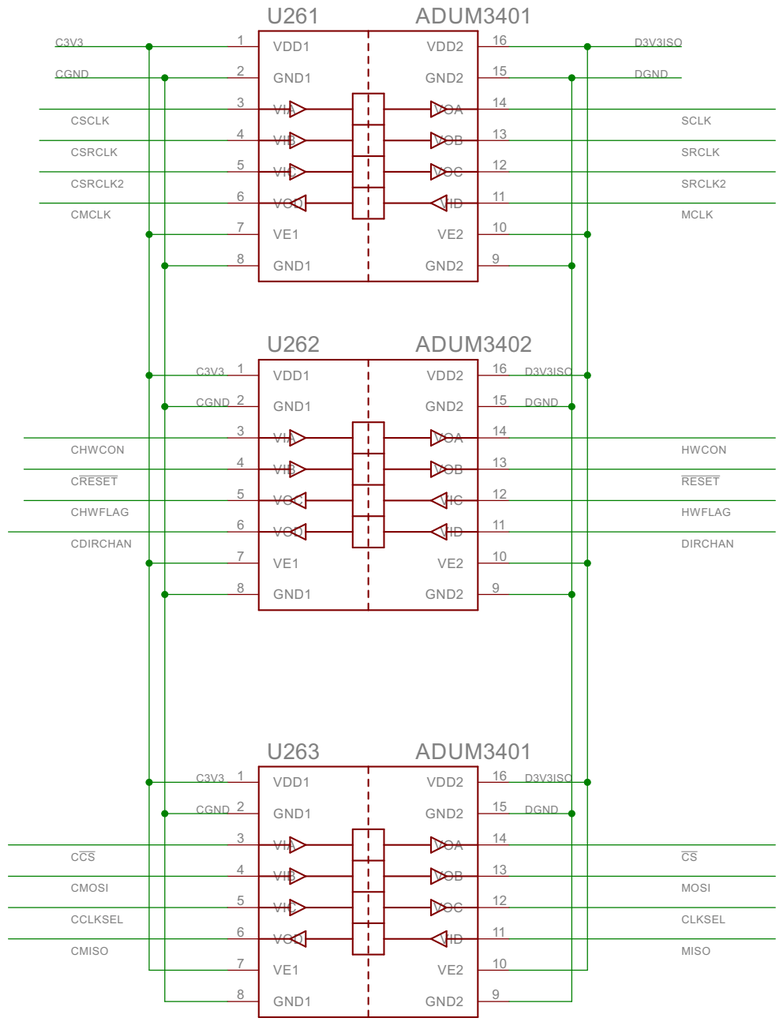


FPGA/RPi Carrier Board

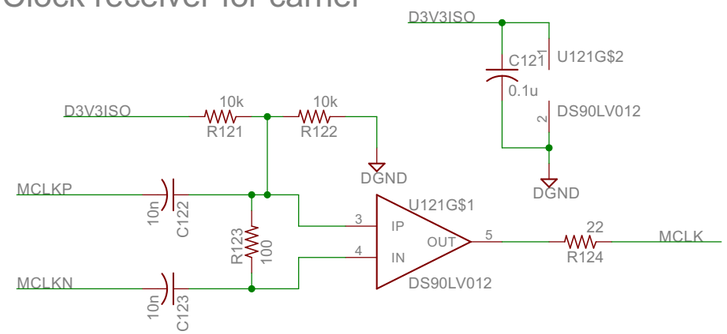
Signals from carrier are labeled with a "C" in front



Unidirectional Isolators

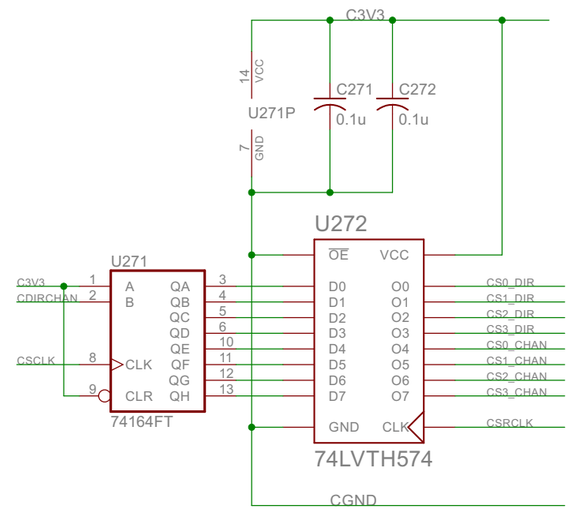


Clock receiver for carrier

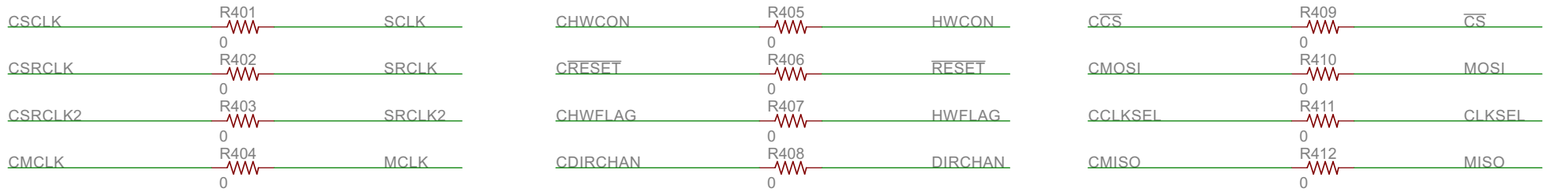


DIRCHAN deserializer - carrier side

Needed to control isolator direction
Only needed if using isolators

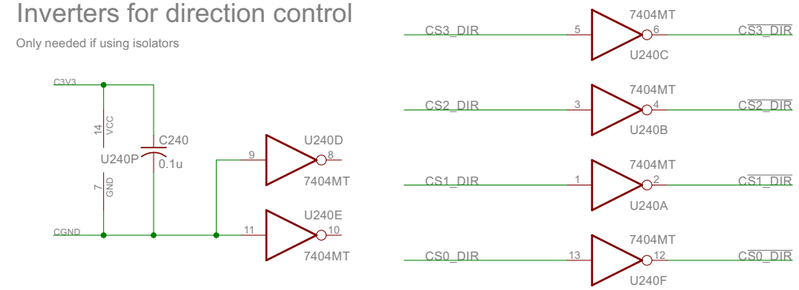


Resistor Bypass



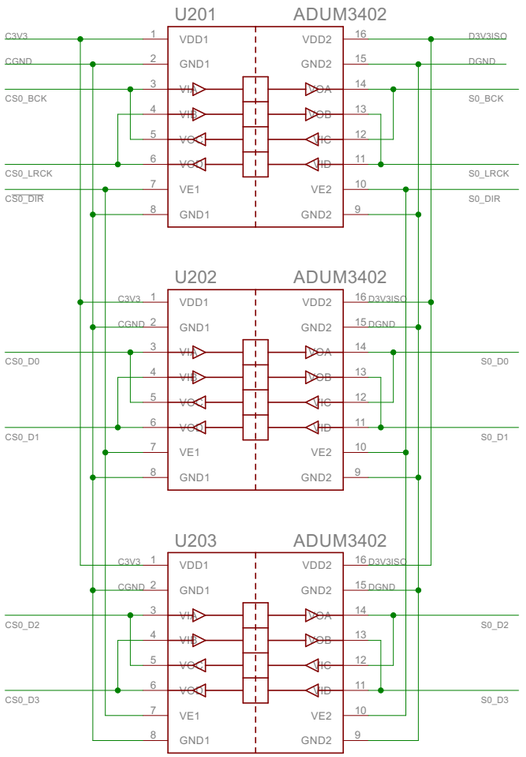
Inverters for direction control

Only needed if using isolators

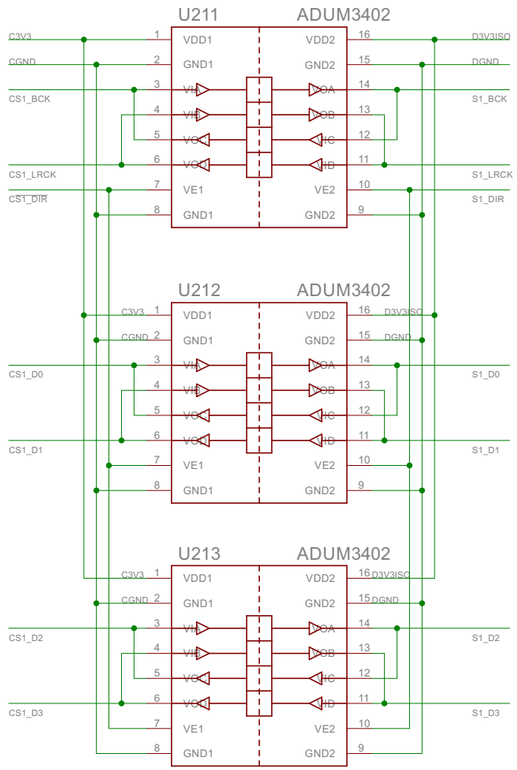


Bidirectional isolators (I2S)

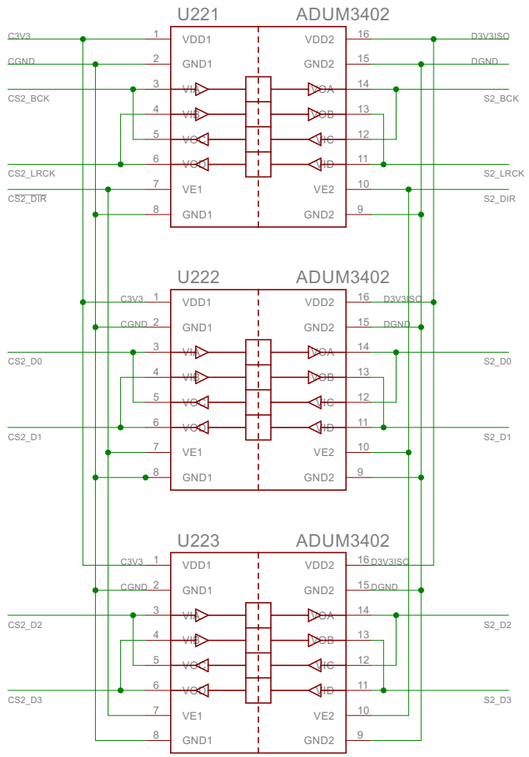
Module 0 data



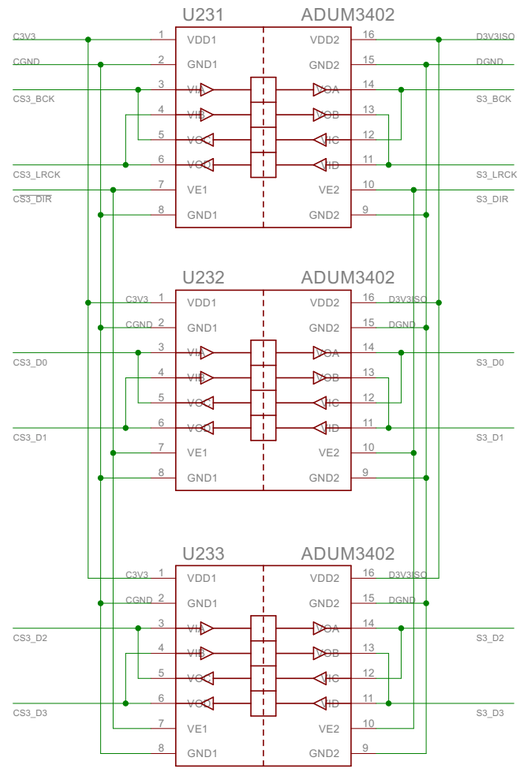
Module 1 data



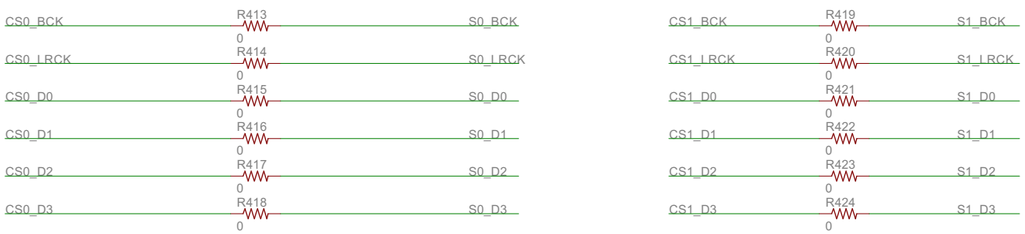
Module 2 data



Module 3 data

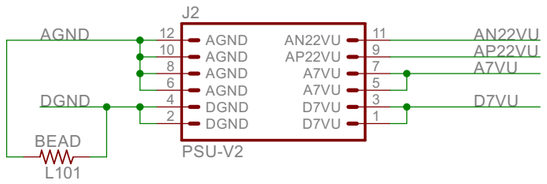


Resistor bypass (stuffing option) - if not using isolators



Power Supply Inputs and Regulation (1/2)

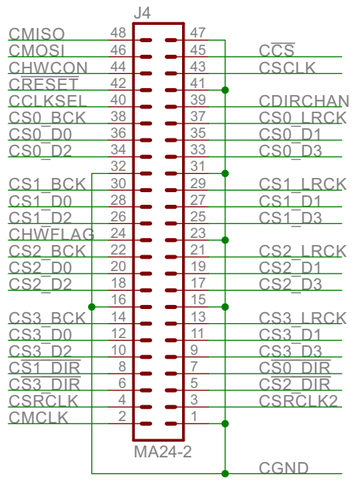
Unregulated Input Connector



Bead can be replaced by resistor/jumper

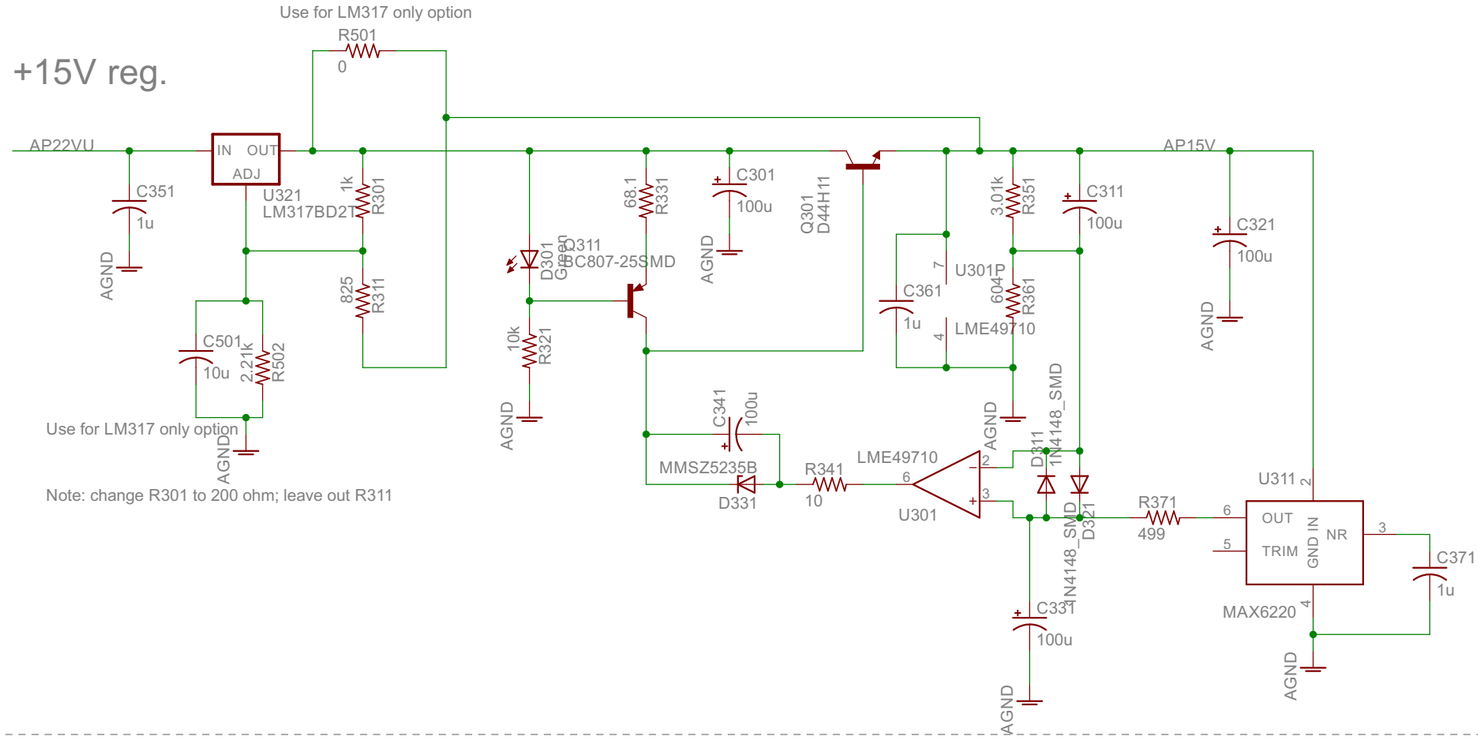
Debug headers

Debug for FPGA/RPi Carrier Board

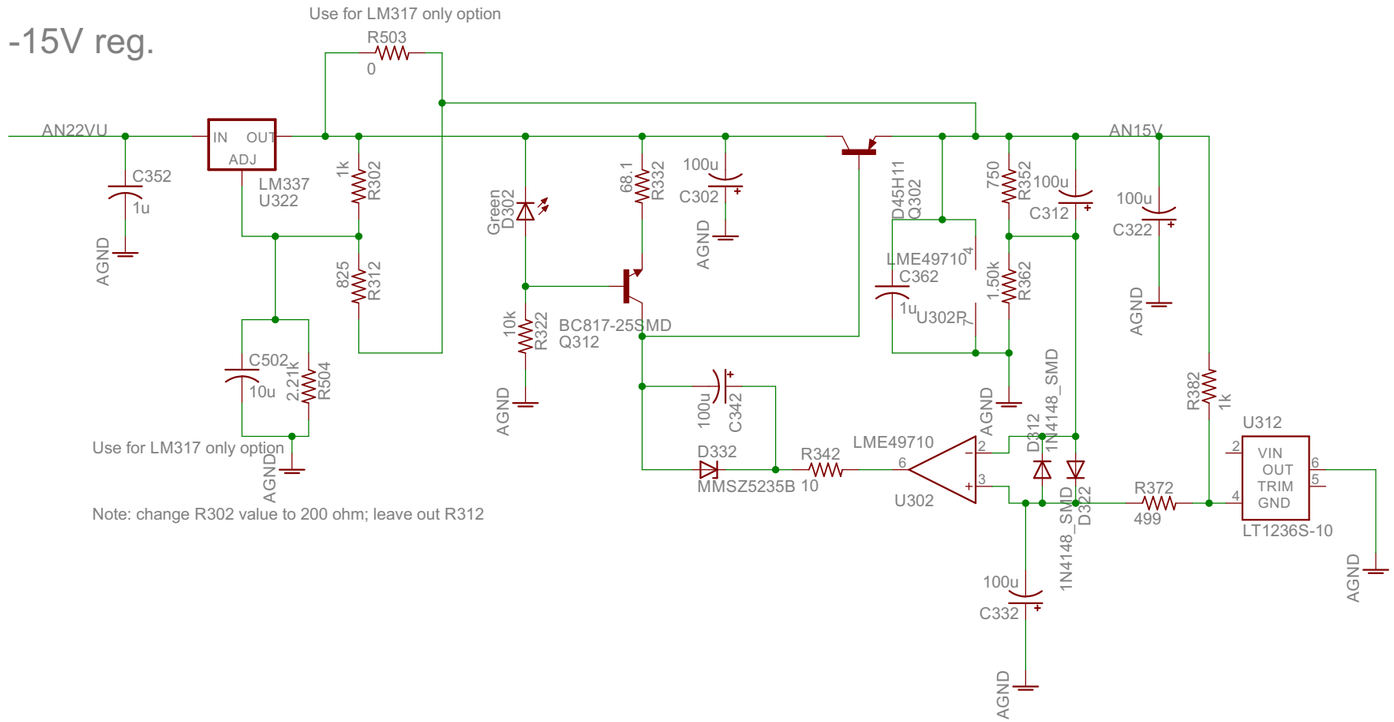


Non-isolated option

+15V reg.



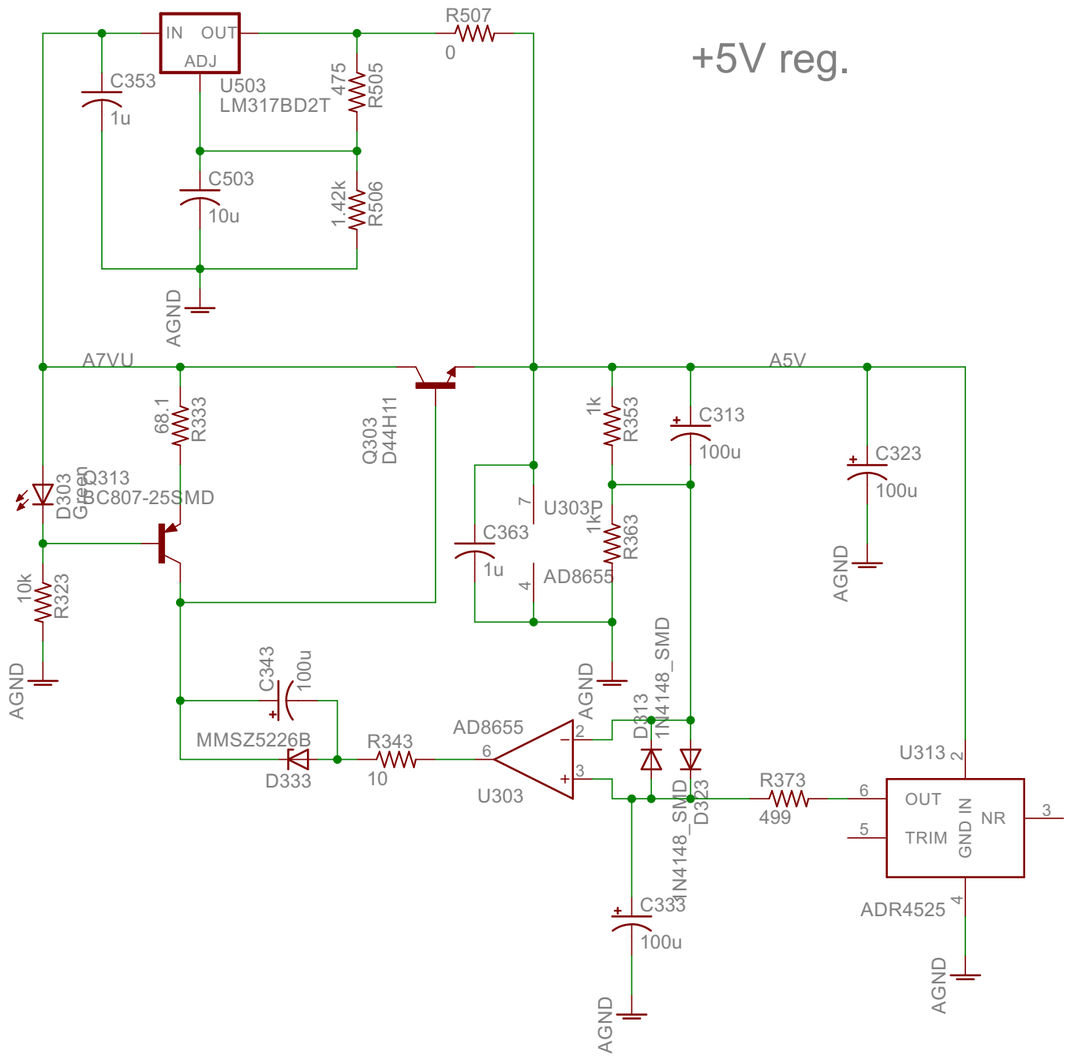
-15V reg.



Power Supply Inputs and Regulation (2/2)

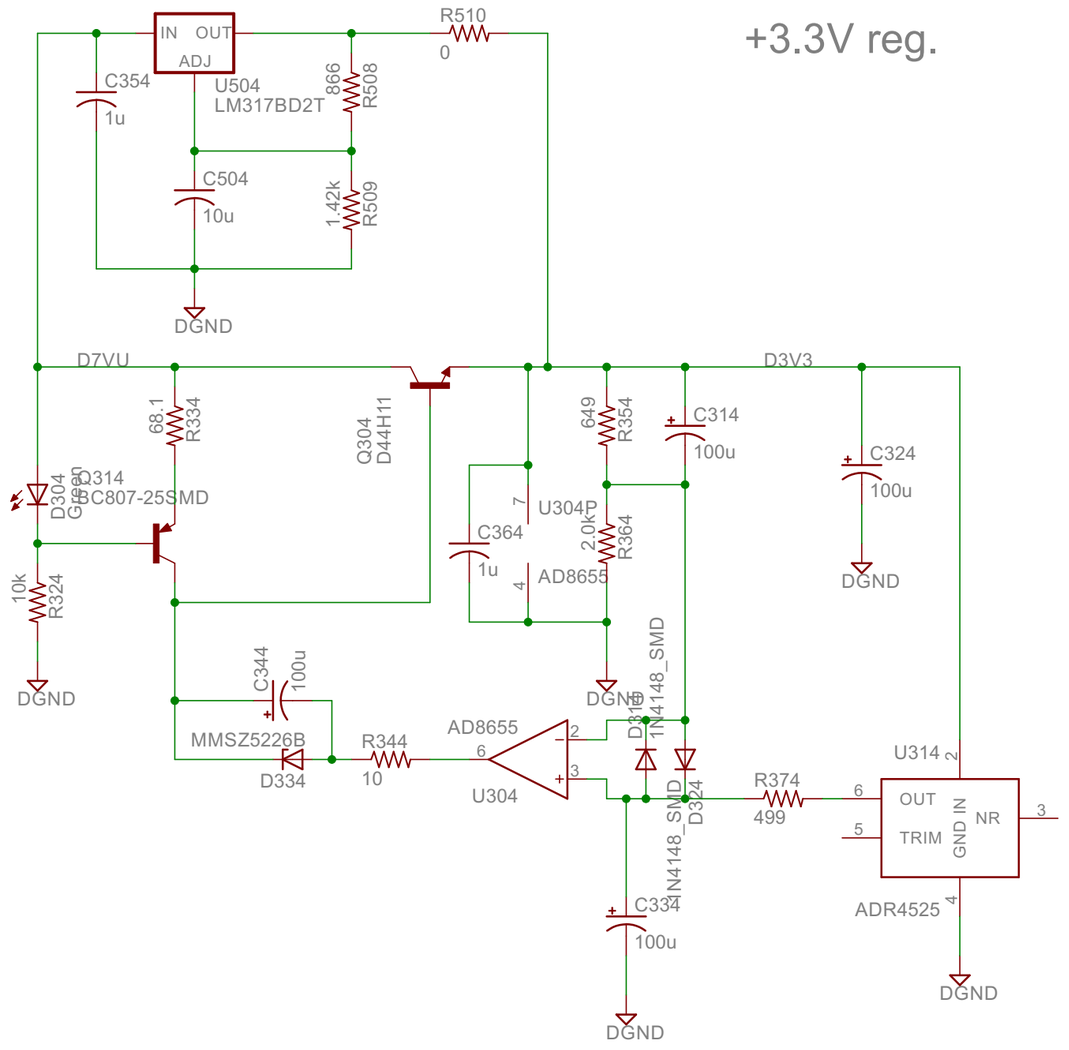
Use for LM317 only option

+5V reg.



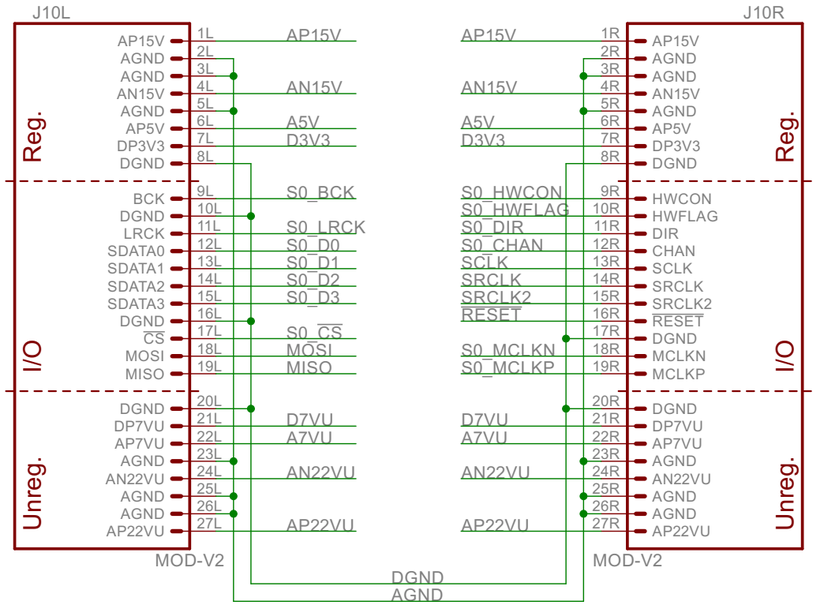
Use for LM317 only option

+3.3V reg.

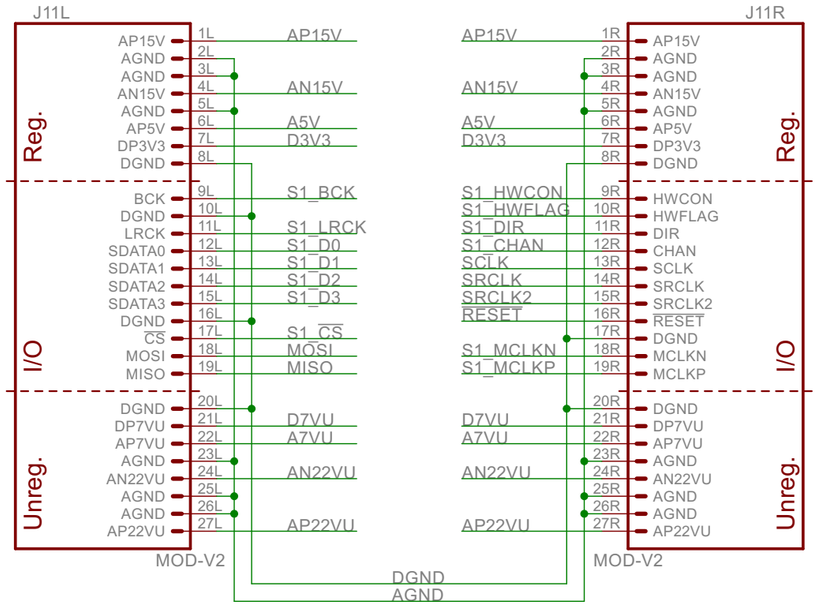


Module connectors

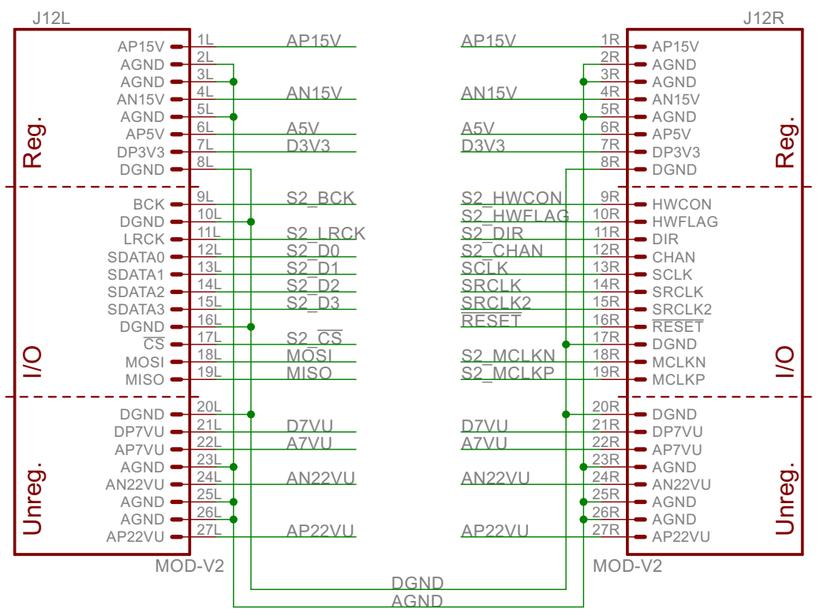
Module 0



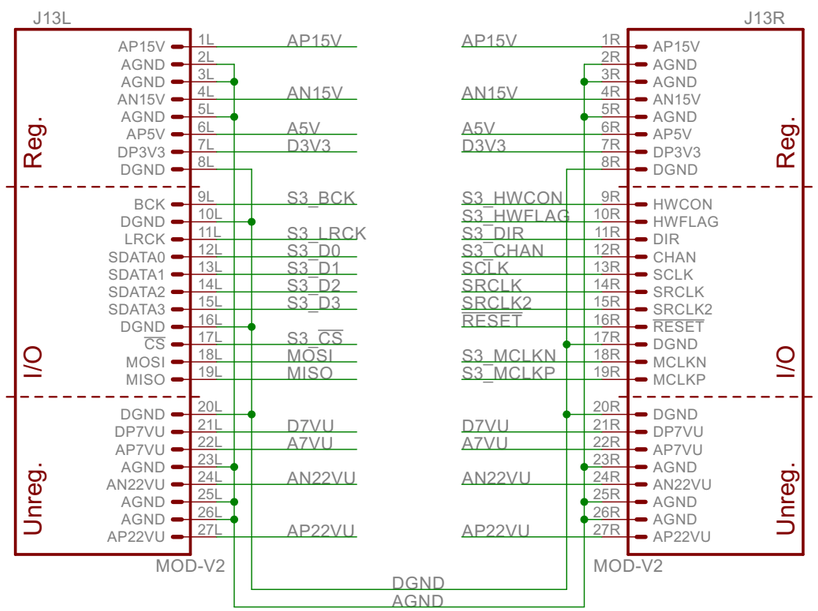
Module 1



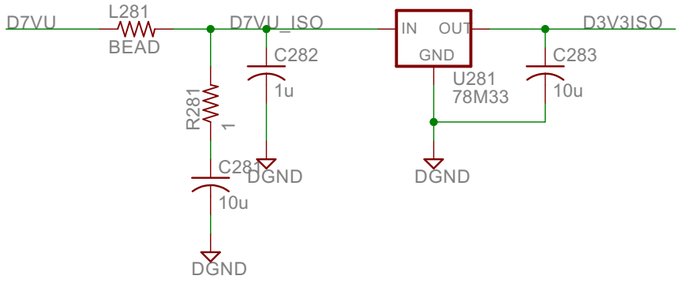
Module 2



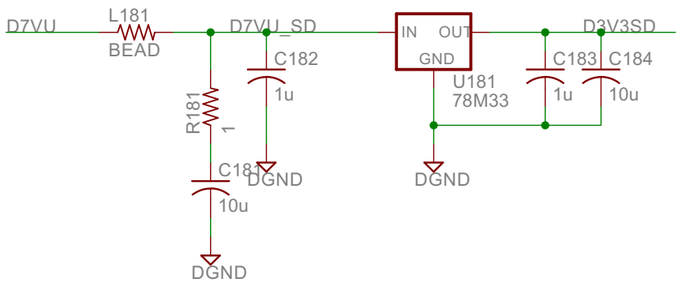
Module 3



Regulator for isolators

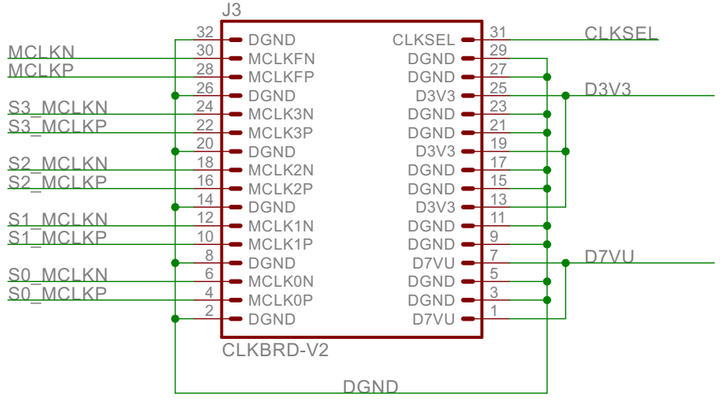


Regulator for ser/des gates



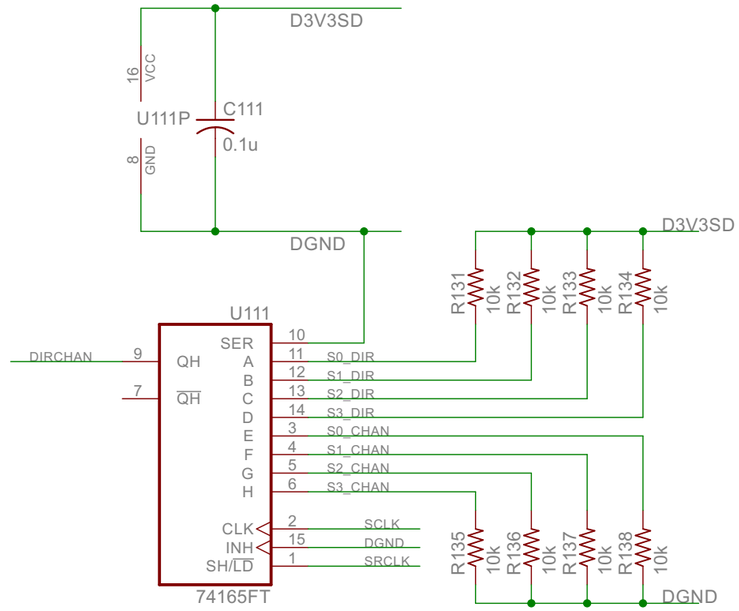
Clock Board

On "audio side" of isolation barrier



DIRCHAN bits

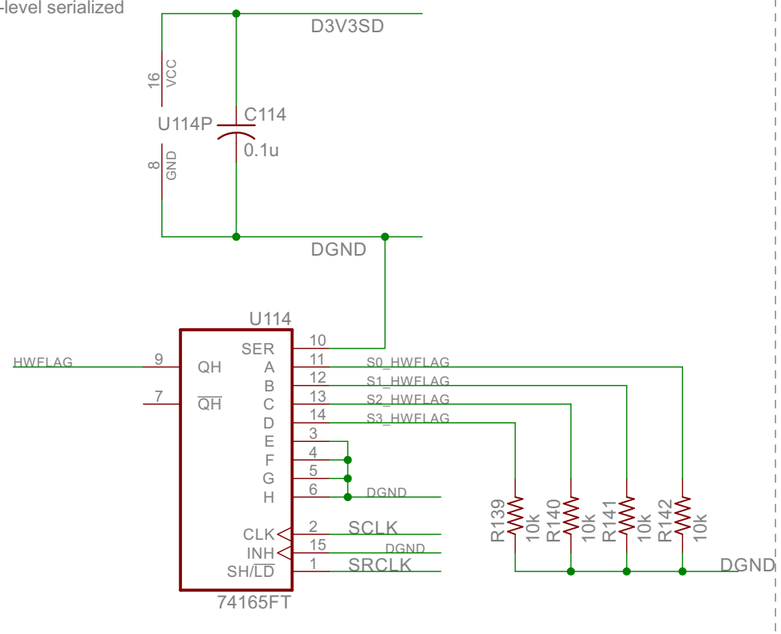
These are constant outputs from each module



HWFLAG bits

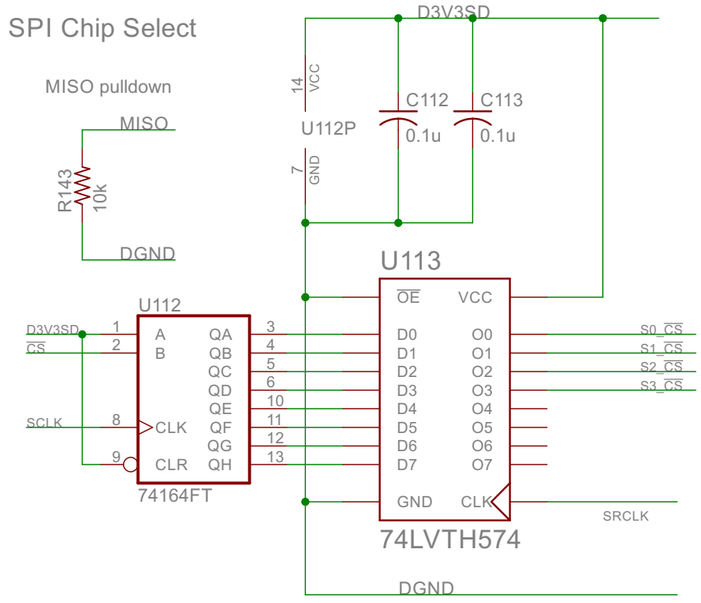
These are status outputs from each module

* 2-level serialized



SPI Chip Select

MISO pulldown



HWCON bits

These are config inputs to each module

* 2-level serialized