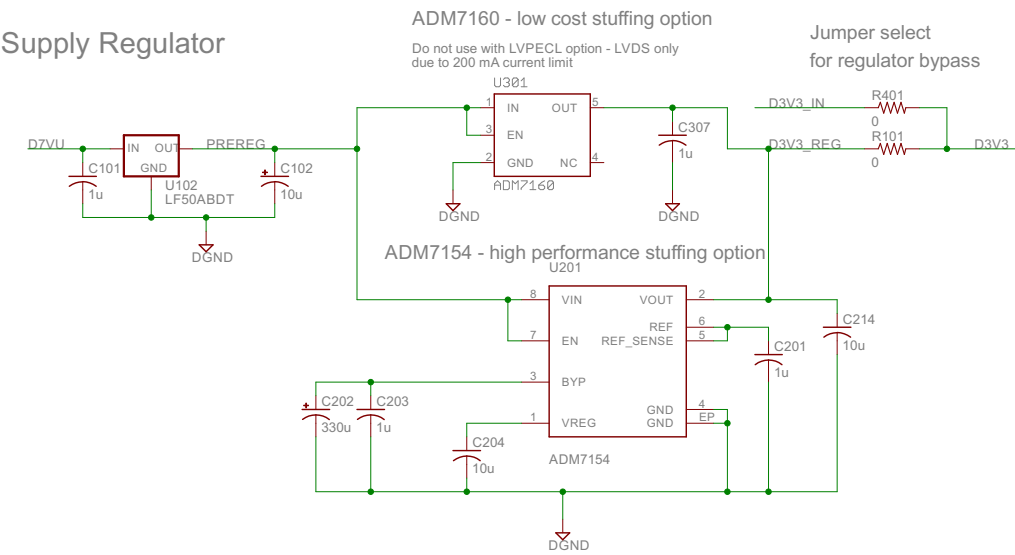# Differential Clock Select/Driver
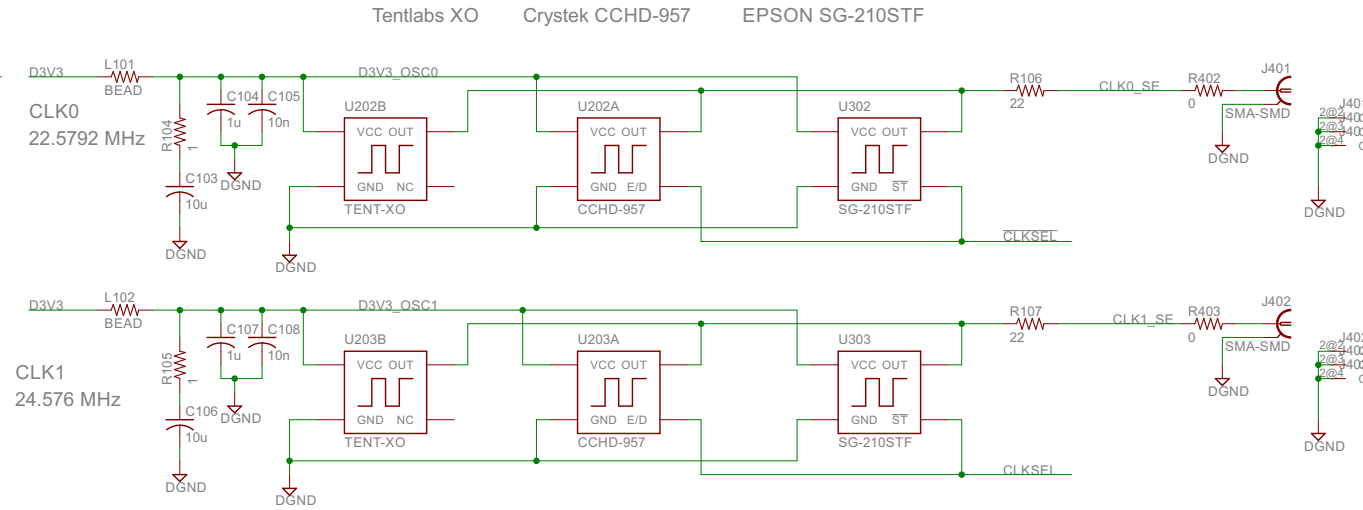
Revised: 10/31/2017 - added preregulator and replaced UA78M33 with ADM7160
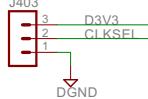
Supply Regulator

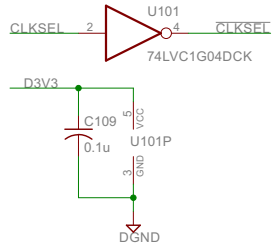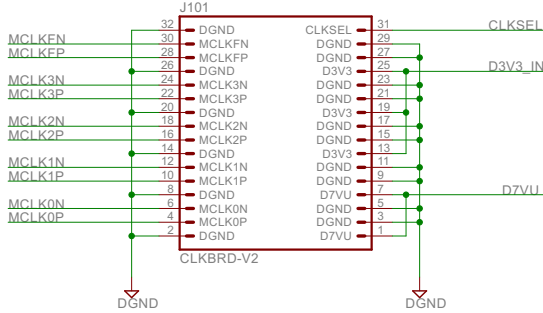**ADM7160 - low cost stuffing option**

Do not use with LVPECL option - LVDS only
due to 200 mA current limit

Jumper select
for regulator bypass

Oscillator Options

Tentlabs XO   Crystek CCHD-957   EPSON SG-210STF

CLKSEL jumper

CLKSEL inverter

ADM7154 - high performance stuffing option

CLK0
22.5792 MHz

CLK1
24.576 MHz

Connector to Isolator PCB

LVDS Mux/Output
(Low cost stuffing option)

Output CM = 1.25 V
Output swing = 350 mV diff
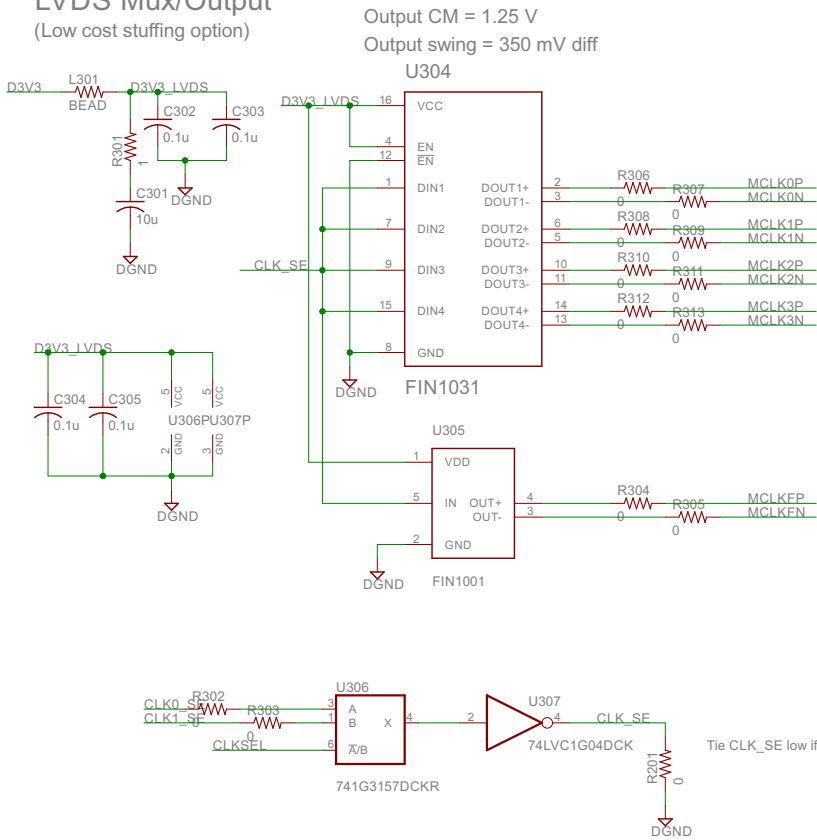
LVPECL Mux/Output
(High performance stuffing option)

Output CM = 1.6 V
Output swing = 1.6 V diff

Tie CLK_SE low if using LVPECL option