

Proceso	Llegada	CPU	I/O (rec, ins, dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	TR	TE
P1	0	4	(R1, 2, 1)	>1	2	R1			3	4<																7	3
P2	2	6	(R2, 3, 1) (R2, 5, 2)			>1	2	3	R2						4	5	R2	R2		6<						15	9
P3	3	4					>				1	2	3	4<												8	4
P4	6	5	(R3, 1, 2) (R3, 3, 1)							>							1	R3			2	3	R3	4	5<	16	11
P5	8	2									>							1	2<							8	6
FCFS				1	2	4	3	2	4	5	2	4														10.8	6.6

DUDA: le agrego una columna???

Proceso	Llegada	CPU	I/O (rec, ins, dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	TR	TE
P1	0	4	(R1, 2, 1)	>1	2	R1			3	4<																6	2
P2	2	6	(R2, 3, 1) (R2, 5, 2)			>1	2					3	R2					4	5	R2	R2	6<				17	11
P3	3	4					>			1	2			3	4<											9	5
P4	6	5	(R3, 1, 2) (R3, 3, 1)							>			1	R3	R3				2	3	R3	4	5<			15	10
P5	8	2									>					1	2<									6	4
RR - TV		Q = 2		1	2	4	3	2	4	3	5	2	4	2	4											10.6	6.4

Proceso	Llegada	CPU	I/O (rec, ins, dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	TR	TE
P1	0	4	(R1, 2, 3) (R1, 3, 2)	>1	2	R1	R1	R1				3	R1	R1				4<								15	11
P2	2	6	(R2, 3, 2)			>1	2	3	R2	R2			4	5	6<											10	4
P3	3	4	(R2, 2, 3)				>		1	2	R2	R2	R2						3	4<						14	10
P4	6	5	(R1, 1, 2)							>	1			R1	R1					2	3	4	5<			15	10
P5	8	2									>					1	2<									6	4
FCFS			RQueue	1	2	3	1	4	2	5	1	3	4													12	7.8
			R1 Queue	1	1	4																					
			R2 Queue	2	3																						

Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20		TR	TE
P1	0	4		(R1, 2, 3) (R1, 3, 2)	>1	2	R1	R1	R1			3	R1	R1				4<									14	10
P2	2	6		(R2, 3, 2)			>1	2			3			R2	R2				4	5			6<				17	11

[illegible]