

# Rajiv Gandhi Proudhyogiki Vishwavidyalaya, Bhopal

B.E. V Semester (Electrical Engineering)

## Subject wise distribution of marks and corresponding credits

S.No.	Subject Code	Subject Name & Title	Maximum Marks Allotted							Credits Allotted Subject wise			Total Credits	Remark
			Theory Slot			Practical Slot		Total Marks						
			End Sem.	Mid Sem. MST (Two tests average)	Quiz, Assignment	End Sem	Term work		Period per week					
							Lab work & sessional	Assignment/ quiz		L	T	P		
1	EE -501	Electrical Machine-II	70	20	10	30	10	10	150	3	1	2	06	
2	EE-502	Electronic Instrumentation	70	20	10	30	10	10	150	3	1	2	06	
3	EE -503	Signals & Systems	70	20	10	-	-	-	100	3	1	-	04	
4	EE -504	Digital Electronics & Logic Design	70	20	10	30	10	10	150	3	1	2	06	
5	EE -505	Principles of Management and Managerial Economics	70	20	10	-	-	-	100	3	1	-	04	
6	EE -506	Electrical Engg. Simulation Lab	-	-	-	30	10	10	50	0	0	2	02	
7	EE -507	Self study (Internal Assessment)	-	-	-	-	-	50	50	0	0	2	02	Grand Total
8.	EE -508	Seminar / Group Discussion (Internal Assessment)	-	-	-	-	-	50	50	0	0	2	02	
		Total	350	100	50	120	40	140	800	15	5	12	32	800

**MST: Mid Semester Tests Taken at Least twice Per Semester**

**L: Lecture -    T: Tutorial -    P: Practical**