

Course of Study and Scheme of Examination
B.E. (PTDC) Electronics & Communication Engineering
SEMESTER – III

S. No	Course Code (New)	Subject	Periods Per week				Distribution of Marks						
							Theory Exam			Practical Block			Total
			L	T	P	C	End Sem	Mid Sem	Total I	End Sem	TW	Total II	I+II
1	EC-401	Electronics-II	3	1	2	6	100	20	120	50	30	80	200
2	EC-405	Electro magnetic Theory	3	1	0	4	100	20	120				120
3	EC-502	Data Structures	3	1	2	6	100	20	120	50	30	80	200
4	EC-503	Control Engg.	3	1	0	4	100	20	120		30	30	150
5	EC-306	Computer Programming -III	0	0	4	4	-	-	-		30	30	30
Total			12	4	8	24	400	80	480	100	120	220	700

Minimum Pass Marks :

[A] Theory (End Sem) :35 Percent
 Practical (End Sem) :50 Percent

[B] Theory Block Aggregate : 35 Percent
 Practical Block Aggregate :50 Percent

[C] Duration of Theory Paper
 (ES) : 3 Hrs.