Rajiv Gandhi Proudhyogiki Vishwavidyalaya, Bhopal

B.E. IV Semester (Electrical & Electronics Engineering)

Subject wise distribution of marks and corresponding credits

S.No. Subject Name & Title End Mid Sem. Sem. MST Characteristic Assignment Sem. Lab Assignment Assignment Assignment Sem. Lab Assignment Assignment Sem. Lab Assignment Assignment Sem. Lab Assignment Assignment Sem. Lab Assignment Sem. Lab Assignment Sem. Sem				Maximum Marks Allotted								Credits Allotted		Total Credits	Remark
S.No. Subject Subject Name & Time Sem. MST CTwo tests average Part Code Sem. CTwo tests average Part CTWo tests averag				Theory Slot			Practical Slot				Subject		ect	Creans	
Sem.				End							_				
1 BE-401 Mathematics III 70 20 10 - - - 100 3 1 - 04 2 EX-402 Electrical and Electronics 70 20 10 - - - 100 3 1 - 04 3 EX-403 Digital Electronics Logic Design -I 70 20 10 30 10 10 150 3 1 2 06 4 EX-404 Electrical Machine -I 70 20 10 30 10 10 150 3 1 2 06 5 EX-405 Electronic Devices and Circuits - II 70 20 10 30 10 10 150 3 1 2 06 6 EX-406 Electrical Engineering Simulation Lab -I - - - - - - 50 50 0 0 2 02 Grand Total 8 EX -407 Self study (Internal Assessment) - - - - - -	S.No.			Sem.	(Two tests	_	Sem	work &					k		
2 EX-402 Electrical and Electronics Material 70 20 10 - - - 100 3 1 - 04 3 EX -403 Digital Electronics Logic Design -I 70 20 10 30 10 10 150 3 1 2 06 4 EX -404 Electrical Machine -I 70 20 10 30 10 10 150 3 1 2 06 5 EX -405 Electronic Devices and Circuits - II 70 20 10 30 10 10 150 3 1 2 06 6 EX -406 Electrical Engineering Simulation Lab -I - - - - - 50 50 0 0 2 02 Grand Total 8 EX -407 Self study (Internal Assessment) - <											L	Т	P		
Material	1	BE-401	Mathematics III	70	20	10	-	-	-	100	3	1	-	04	
4 EX -404 Electrical Machine -I 70 20 10 30 10 10 150 3 1 2 06 5 EX -405 Electronic Devices and Circuits - II 70 20 10 30 10 10 150 3 1 2 06 6 EX -406 Electrical Engineering Simulation Lab -I - - - - 30 10 10 50 0 0 2 02 7 EX -407 Self study (Internal Assessment) - - - - - 50 50 0 0 2 02 Grand Total 8. EX -408 Seminar / Group Discussion (Internal Assessment) - <t< td=""><td>2</td><td>EX-402</td><td></td><td>70</td><td>20</td><td>10</td><td>-</td><td>-</td><td>-</td><td>100</td><td>3</td><td>1</td><td>-</td><td>04</td><td></td></t<>	2	EX-402		70	20	10	-	-	-	100	3	1	-	04	
5 EX -405 Electronic Devices and Circuits - II 70 20 10 30 10 10 150 3 1 2 06 6 EX -406 Electrical Engineering Simulation Lab -I - - - 30 10 10 50 0 0 2 02 7 EX -407 Self study (Internal Assessment) - - - - - 50 50 0 0 2 02 Grand Total 8. EX -408 Seminar / Group Discussion (Internal Assessment) - - - - - - - 50 50 0 0 2 02	3	EX -403	Digital Electronics Logic Design -I	70	20	10	30	10	10	150	3	1	2	06	
II	4	EX -404	Electrical Machine -I	70	20	10	30	10	10	150	3	1	2	06	
Tab - I	5	EX -405		70	20	10	30	10	10	150	3	1	2	06	
8. EX -408 Seminar / Group Discussion (Internal Assessment) 50 50 0 0 2 02	6	EX -406	0 0	-	-	-	30	10	10	50	0	0	2	02	
(Internal Assessment)	7	EX -407	Self study (Internal Assessment)	-	-	-	-	-	50	50	0	0	2	02	
Total 350 100 50 120 40 140 800 15 5 12 32 800	8.	EX -408	_	-	-	-	-	-	50	50	0	0	2	02	
			Total	350	100	50	120	40	140	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

L: Lecture -T: Tutorial - P: Practical