



# Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal(M.P.)

## Scheme of Examination

(w.e.f. 2012-13)

### Second Semester- M.E./M.Tech (Digital Electronics)

S.No.	Subject Code	Subject Name	Periods per week			Credits	Maximum Marks (Theory Slot)			Maximum Marks (Practical Slot)		Total Marks
			L	T	P		End Sem. Exam.	Tests (Two)	Assignments /Quiz	End Sem. Practical /Viva	Practical Record/ Assignment/Quiz /Presentation	
1.	MTDE 201	Electronic System Design	3	1	-	4	70	20	10	-	-	100
2.	MTDE 202	Advance Digital Signal Processing	3	1	-	4	70	20	10	-	-	100
3.	MTDE 203	Micro Electronics	3	1	-	4	70	20	10	-	-	100
4.	MTDE 204	Embedded System	3	1	-	4	70	20	10	-	-	100
5.	MTDE 205	Fault Tolerant Systems	3	1	-	4	70	20	10	-	-	100
6.	MTDE 206	Lab-III Electronic System Design	-	-	6	6	-	-	-	90	60	150
7.	MTDE 207	Lab-IV Advance Digital Signal Processing	-	-	6	6	-	-	-	90	60	150
		Total	15	5	12	32	350	100	50	180	120	800

**L: Lecture - T: Tutorial - P: Practical**