Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal

New Scheme of Examination as per AICTE Flexible Curricula

Bachelor of Technology (B.Tech.)

Electronics Engineering (VLSI Design and Technology)

IV Semester

S. No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Total Marks	Contact Hours per week			Total Credits
				Theory			Practical						
				End Sem.	Mid Sem. Exam.	Quiz/ Assignment	End Sem.	Termwork		L	T	P	
								Lab Work & Sessional					
1.	ES401	BSC	Energy & Environmental Engineering	70	20	10	-	-	100	3	1	-	4
2.	ED 402	DC	Introduction to VLSI Design Flow	70	20	10	30	20	150	2	1	2	4
3.	ED 403	DC	Microcontroller and Interfacing	70	20	10	30	20	150	3	1	2	5
4.	ED 404	DC	Control Systems	70	20	10	30	20	150	3	1	2	5
5.	ED 405	DC	Analog Circuits	70	20	10	30	20	150	3	0	2	4
6.	ED 406	DC	Computer Workshop (Introduction to Linux and Python for scripting)	-	-	-	30	20	50	-	-	4	2
7.	BT407	DLC	90hrs Internship base donusing Various software's–Internship-II	To be completedanytime duringfourthsemester.Itsevaluation/credittobeaddedinfifthsemester.									3
			Total	350	100	50	150	100	750	15	3	12	24
8.	BT408	MC	Cyber Security	Non-credit course									
9.	BT409I	MC	Indian Knowledge System	Non-credit course									
	NC001		NSS/NCC										

*Aminimum of 2 hours per week should be allotted for the Virtual Lab along with the slot fixed for the conventional lab classes.

MST:Minimum of two mid semester tests to be conducted.

*Students can earn additional credits from the University recognized MOOC courses.

1HrLecture	1Hr Tutorial	2HrPractical
1Credit	1Credit	1Credit