

Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal (M.P.) <u>Scheme of Examination</u>

Fourth Semester- M.E./M.Tech.

(Embedded System and VLSI Design, Micro electronics and VLSI Design)

	Subject	Subject Name	Periods per			Credits	Maximum Marks			Maximum Marks		Total
S.No.	Code		week				(Theory Slot)			(Practical Slot)		Marks
							End	Tests	Assign	End	Practical	
							Sem.	(Two)	ments	Sem.	Record/	
			L	T	P	j	Exam.		/Quiz	Practic	Assignm	
										al/Viva	ent/Quiz	
											/Present	
											ation	
1.	MEVD	Dissertation Part- II	-	-	20	20	-	-	-	300	200	500
	401											
		Total	-	ı	20	20	-	-	-	300	200	500

L: Lecture - T: Tutorial - P: Practical

w.e.f. July-2010