

Course of Study and Scheme of Examination
B.E. (PTDC) Electronics & Communication Engineering
SEMESTER – IV

o	Course Code (New)	Subject	Periods Per week				Distribution of Marks					
							Theory Exam	Practical Exam	Practical Block		Total	I+II+III
			L	T	P	C	I	II	MST	TW	Total	
	EC401	Electronics-II	3	1	2	6	100	50	20	30	80	200
	EE502	Electrical & Electronics Instrumentation	3	1	2	6	100	50	20	30	50	200
	EE504	Digital Electronics & Logic design	3	1	2	6	100	50	20	30	50	200
	EE505	Principles of Management & Managerial Economics	3	1	0	4	100	-	20	-	20	120
	EE406	Computer Programming-IV	0	0	2	2	-	50	-	30	30	80
Total			12	4	8	24	400	200	80	120	200	800

Minimum Pass Marks :

Theory :35 Percent

[C] Duration of Theory Paper 3 hrs

Practical (End Sem) :50 Percent