

Rajiv Gandhi Proudhyogiki Vishwavidyalaya, Bhopal
B.E. IV Semester (Electrical & Electronics Engineering)
Subject wise distribution of marks and corresponding credits

S.No.	Subject Code	Subject Name & Title	Maximum Marks Allotted							Credits Allotted Subject wise			Total Credits	Remark
			Theory Slot			Practical Slot			Total Marks					
			End Sem.	Mid Sem. MST (Two tests average)	Quiz, Assignment	End Sem	Term work			Period per week				
							Lab work & sessional	Assignment/ quiz						
										L	T	P		
1	BE-401	Mathematics III	70	20	10	-	-	-	100	3	1	-	04	
2	EX-402	Electrical and Electronics Material	70	20	10	-	-	-	100	3	1	-	04	
3	EX -403	Digital Electronics Logic Design -I	70	20	10	30	10	10	150	3	1	2	06	
4	EX -404	Electrical Machine -I	70	20	10	30	10	10	150	3	1	2	06	
5	EX -405	Electronic Devices and Circuits - II	70	20	10	30	10	10	150	3	1	2	06	
6	EX -406	Electrical Engineering Simulation Lab -I	-	-	-	30	10	10	50	0	0	2	02	
7	EX -407	Self study (Internal Assessment)	-	-	-	-	-	50	50	0	0	2	02	Grand Total
8.	EX -408	Seminar / Group Discussion (Internal Assessment)	-	-	-	-	-	50	50	0	0	2	02	
		Total	350	100	50	120	40	140	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

L: Lecture - T: Tutorial - P: Practical