

**Course of Study and Scheme of Examination**  
**B.E. (PTDC) Electrical Engineering**  
**SEMESTER – IV**

o	Course Code (New)	Subject	Periods Per week				Distribution of Marks					
							Theory Exam	Practical Exam	Practical Block		Total	
			L	T	P	C	I	II	MST	TW		
	EE404	Analog & Digital Communication	3	1	2	6	100	50	20	30	50	<b>200</b>
	EE502	Electrical & Electronics Instrumentation	3	1	2	6	100	50	20	30	50	<b>200</b>
	EE504	Digital Electronics & Logic design	3	1	2	6	100	50	20	30	50	<b>200</b>
	EE505	Principles of Management & Managerial Economics	3	1	0	4	100	-	20	-	20	<b>120</b>
	EE406	Computer Programming-IV	0	0	2	2	-	50	-	30	30	<b>80</b>
<b>Total</b>			<b>12</b>	<b>4</b>	<b>8</b>	<b>24</b>	<b>400</b>	<b>200</b>	<b>80</b>	<b>120</b>	<b>200</b>	<b>800</b>

Minimum Pass Marks :

Theory :35 Percent

[C] Duration of Theory Paper 3 hrs

Practical (End Sem) :50 Percent