

RAJIV GANDHI PROUDHYOGIKI VISHWAVIDHYALAYA, BHOPAL

Scheme of Examination

BE-PTDC VIth Sem. Electrical & Electronics Engineering

| S. No. | Sub. Code | Subject Name & Title | Maximum Marks Allotted | | | | | Credit allotted subject wise | | | Total Credits | Remarks |
|--------|-----------|-----------------------------------|------------------------|--------------|------------------|----------------|---------------------|------------------------------|----|----|---------------|--------------------|
| | | | Theory Slot | | | Practical Slot | | L | T | P | | |
| | | | End Sem | Mid Sem. MST | Quiz, Assignment | End Sem. | Lab Work/Assignment | | | | | |
| 1 | PTEX-601 | Utilization of Electrical Energy | 70 | 20 | 10 | -- | -- | 3 | 1 | -- | 4 | |
| 2 | PTEX-602 | Control System | 70 | 20 | 10 | 30 | 20 | 3 | 1 | 2 | 6 | |
| 3 | PTEX-603 | Switch Gear and Protection | 70 | 20 | 10 | 30 | 20 | 3 | 1 | 2 | 6 | |
| 4 | PTEX-604 | Microprocessor & Microcontrollers | 70 | 20 | 10 | 30 | 20 | 3 | 1 | 2 | 6 | |
| 5 | PTEX-605 | Minor Project | -- | -- | -- | 50 | 50 | - | - | 2 | 2 | |
| | | | | | | | | | | | | Total Marks |
| | | Total | 280 | 80 | 40 | 140 | 110 | 12 | 04 | 08 | 24 | 650 |