

Course of Study and Scheme of Examination
B.E. (PTDC) Electrical Engineering
SEMESTER – I

S. No	Course Code (New)	Subject	Periods Per week				Distribution of Marks						
							Theory Exam			Practical Block			Total
			L	T	P	C	End Sem	Mid Sem	Total I	End Sem	TW	Total II	I+II
1	BE-202	Mathematics-II	3	1	0	4	100	20	120	-	-	-	120
2	EE-305	Circuit Theory	3	1	2	6	100	20	120	50	30	80	200
3	EE-302	Energy Ecology, Environment & Society	3	1	0	4	100	20	120	-	-	-	120
4	EE-304	Electronics	3	1	2	6	100	20	120	50	30	80	200
5	BE-107	Computer Programming-I	0	0	4	4	-	-	-	30	30	60	60
Total			12	4	8	24	400	80	480	130	90	220	700

Minimum Pass Marks :

[A] Theory (End Sem) :35 Percent
 Practical (End Sem) :50 Percent

[B] Theory Block Aggregate : 35 Percent
 Practical Block Aggregate :50 Percent

[C] Duration of Theory Paper
 (ES) : 3 Hrs.