

# **Rajiv Gandhi Proudvyogiki Vishwavidyalaya, Bhopal**

**New Scheme of Examination as per AICTE Flexible Curricula**

**Bachelor of Technology (B.Tech.)**

**Electronics Engineering (VLSI Design and Technology)**

## **III Semester**

S.No.	Subject Code	Category	Subject Name	Maximum Marks Allotted					Total Marks	Contact Hours per week			Total Credits
				Theory			Practical			L	T	P	
				End Sem.	Mid Sem Exam.	Quiz/ Assignment	End Sem	Term work Lab Work & Sessional					
1.	BT301	BSC-5	Mathematics-III	70	20	10	-	-	100	3	1	-	4
2.	ED 302	DC-1	Introduction to HDL	70	20	10	-	-	100	3	1	-	4
3.	ED 303	DC-2	Digital Electronics	70	20	10	30	20	150	3	-	2	4
4.	ED 304	DC-3	Electronic Devices	70	20	10	30	20	150	3	-	2	4
5.	ED 305	DC-4	Circuit Analysis	70	20	10	30	20	150	3	-	2	4
6.	ED 306	DLC-3	Verilog HDL Lab	-	-	-	30	20	50	-	-	4	2
7.	BT107	DLC-1	Evaluation of Internship-I completed at I year level	-	-	-	-	50	50			4	2
8.	BT307	DLC-4	90 hrs Internship based on using various software's – Internship -II	To be completed anytime during Third/ fourth semester. Its evaluation/credit to be added in fifthsemester.									
			Total	350	100	50	120	130	750	15	2	14	24
9.	BT308	MC	Indian Constitution	Non-credit course									
NC301		NSS/NCC											

\*Students can earn additional credits from the University recognized MOOC courses.

1 Hr Lecture	1 Hr Tutorial	2 Hr Practical
1 Credit	1 Credit	1 Credit