Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal

B.E.VII Semester (Electronics & Instrumentation Engineering) (Grading System w.e.f-2015-16)

Subject wise distribution of marks and corresponding credits

S.No.	Subject Code	Subject Name & Title	Maximum Marks Allotted							(Credits Allotted		Total Credits	Remark
			Theory Slot			Practical Slot			Total Marks	Subject wise				
			End Sem.	Mid Sem. MST (Two tests average)	Quiz, Assig- nment	End Sem	Term wor Lab work & sessional	Assignm ent/ quiz			Period per week			
										L T P	P			
1	EI-701	VLSI Design	70	20	10	30	10	10	150	3	1	2	06	
2	EI702	Process Control	70	20	10	30	10	10	150	3	1	2	06	
3	EI-703-New	Introduction to Microcontrollers for Embedded System	70	20	10	30	10	10	150	3	1	2	06	
4	EI-710	Elective -I	70	20	10	-	-	-	100	3	1	-	04	-
5	EI-720	Elective -II	70	20	10	-	-	-	100	3	1	-	04	-
6	EI-706	Major Project (Planning & Literature Survey)	-	-	-	30	10	10	50	0	0	2	02	
7	EI-707	Industrial Training(2Weeks)	-	-	-	30	10	10	50	0	0	2	02	Grand Total
8.	EI-708	Seminar / Group Discussion/ Electronic Work Shop	-	-	-	-	-	50	50	0	0	2	02	
		Total	350	100	50	150	50	100	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

Elective –I EI7101 Safety & Reliability Engg. EI7102Data Acquisition System EI7103 MIS L: Lecture - T: Tuto

T: Tutorial - P: Practical

Elective –II EI 7201 AI

EI 7202 Environmental Instrumentation

EI 7203 Bio Medical DSP

EI7104 Ad. DSP EI 7204 TQM