

Course of Study and Scheme of Examination
B.E. (PTDC) Electronics & Communication Engineering
SEMESTER – V

o	Course Code (New)	Subject	Periods Per week				Distribution of Marks					
							Theory Exam	Practical Exam	Practical Block		Total	
			L	T	P	C	I	II	MST	TW		
							I	II	III		Total	I+II+III
	EC601	Microprocessor & Interfacing	3	1	2	6	100	50	20	30	50	200
	EC602	Antenna & Wave propagation	3	1	2	6	100	50	20	30	50	200
	EC603	Digital Signal Processing	3	1	0	4	100	-	20	-	20	120
	EC604	Industrial Electronics	3	1	0	4	100	-	20	-	20	120
	EC406	Computer Programming-IV	0	0	2	2	-	50	-	30	30	80
Total			12	4	8	24	400	130	80	90	170	700

Minimum Pass Marks :

Theory :35 Percent

[C] Duration of Theory Paper 3 hrs

Practical (End Sem) :50 Percent