PROJECTS

1. **VHDL Implementation Of Radix-4 Booth Multiplier For High Speed Operations**

   Duration: 8 months

   Role: Evaluating algorithms and FPGA implementation

   Team size: 4

   Description: To make multiplication more fast and to reduce the number of partial products a Radix-4 Booth multiplier was simulated using Xilinx ISE 13.2.1 software and implemented on Spartan 3e FPGA

2. **Token Counter System**

   Duration: 4 months

   Role: Project Leader

   Team size: 4

   Description: A token counter system to reduce the heavy rush in offices and banks. The system consists of a token section and a counter section. There are 3 switches used for calling the token number, hold the number (In case of absence) and recall. We used a micro controller, micro switch, LCD display (16*2) for displaying the token.

EDUCATION

Bachelor of Technology (Certificate awaiting)

2014 - 2018

Vidya Academy of Science and Technology, Thrissur
University Of Calicut

Higher Secondary (86.5%)

2012 - 2014

Vivekodayam Boy's Higher Secondary School, Thrissur
Kerala State Board

High School (93%)

2011 - 2012

Vivekodayam Boy's Higher Secondary School, Thrissur
Kerala State Board

SKILLS AND ACHIEVEMENTS

1. Logical and structured thinking
2. Proficiency in Windows and Ubuntu system
3. Project papers on VHDL presented in International Journal of Engineering Sciences & Research Technology

PERSONAL SKILLS

1. Hard working and honest
2. Always try to see the bigger picture.

Athithyaraj

06th July 1997

CAREER OBJECTIVE

An electronic engineering graduate who is seeking to find the opportunity to work in a fun and challenging working environment that will encourage him to improve and learn new and necessary skills as well as be motivated by the company to do his best for the sake of helping himself and the company advance in the engineering industry.

CONTACT

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INTEREST