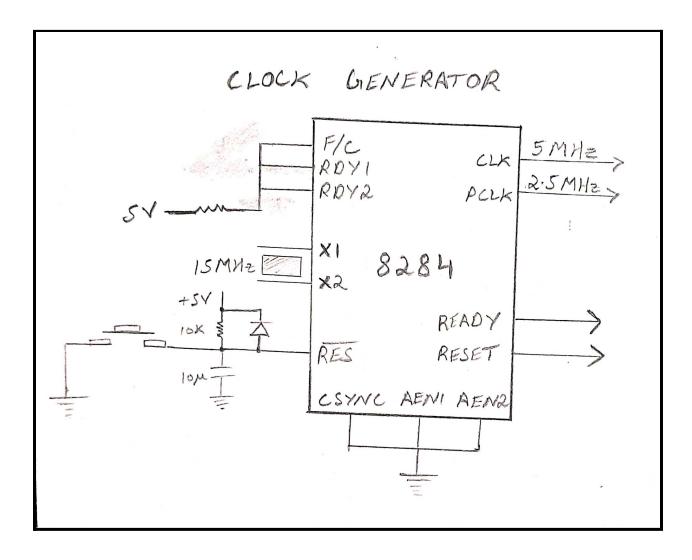
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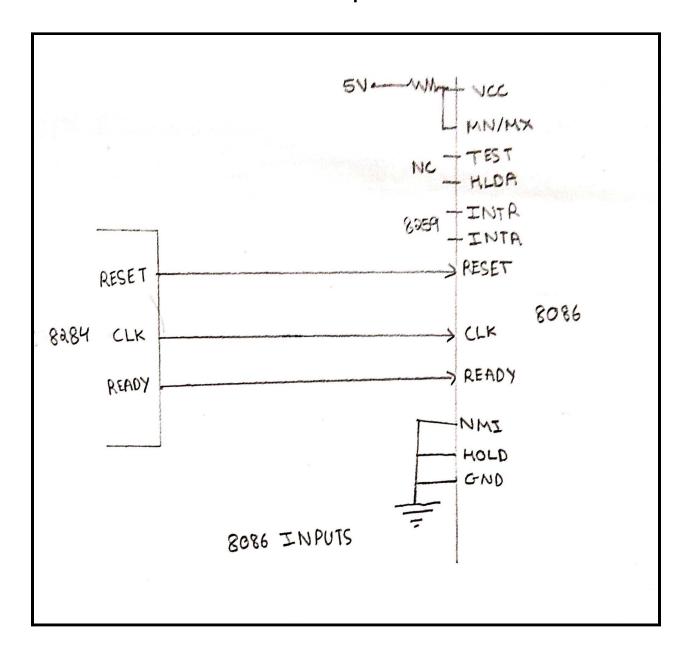
Clock Generator [8284]
8086 Inputs
8086
Address Bus [8086]
Data Bus [8086]
Control Bus [8086]
Memory Decoder
Memory Interfacing
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8255
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Sensors + LM2596

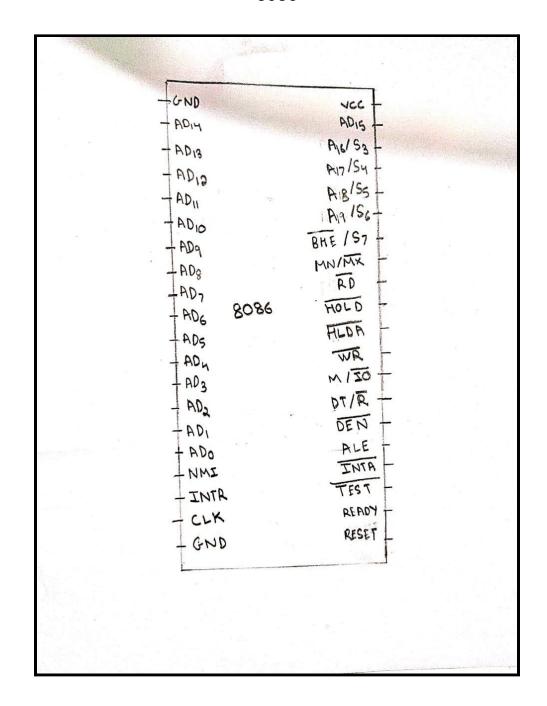
Motors + Valve + Relays

Clock Generator [8284]

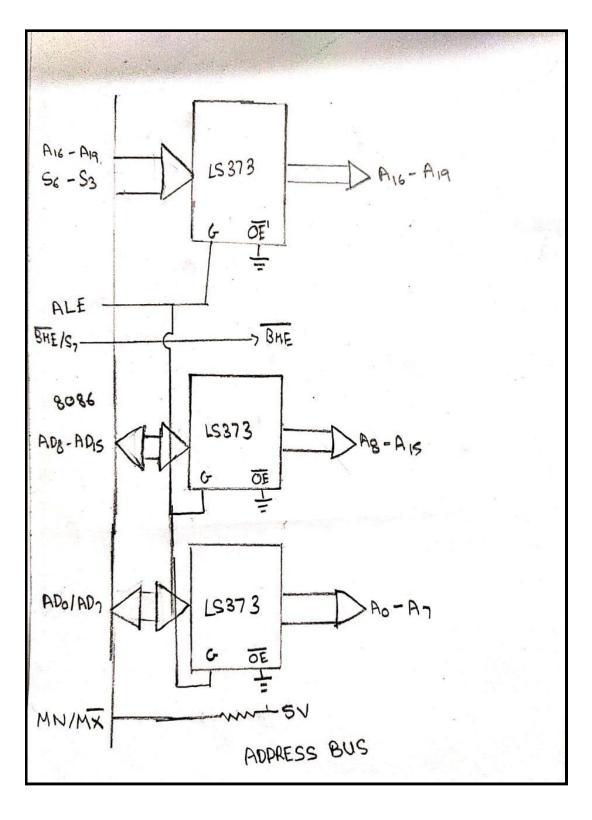


8086 Inputs

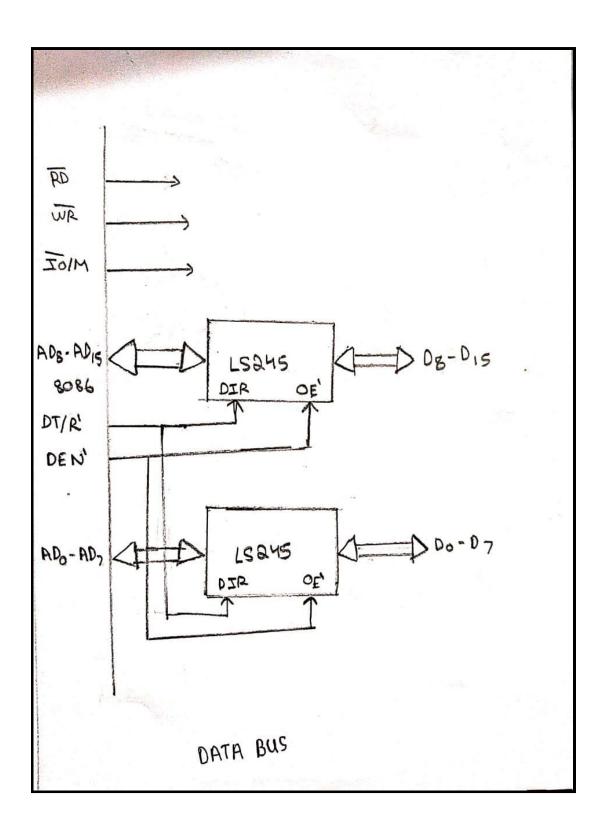




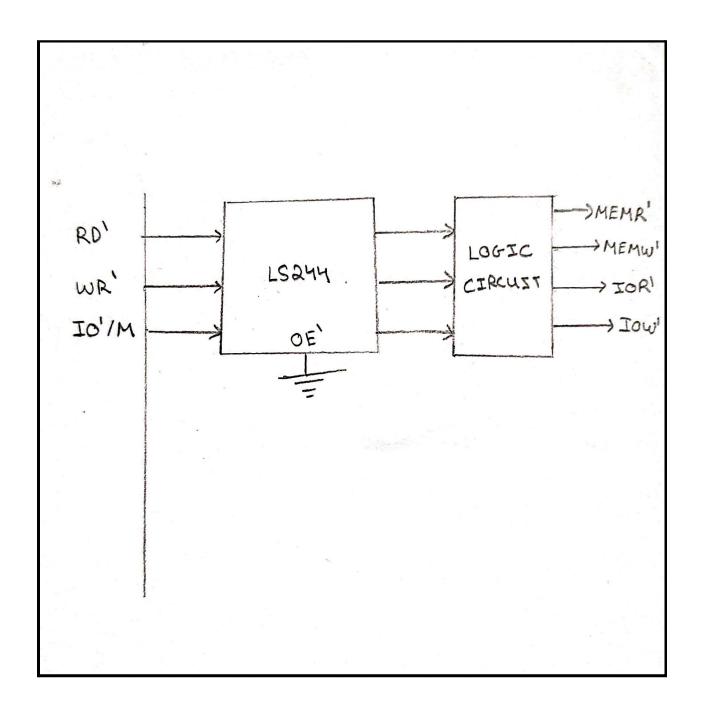
Address Bus [8086]



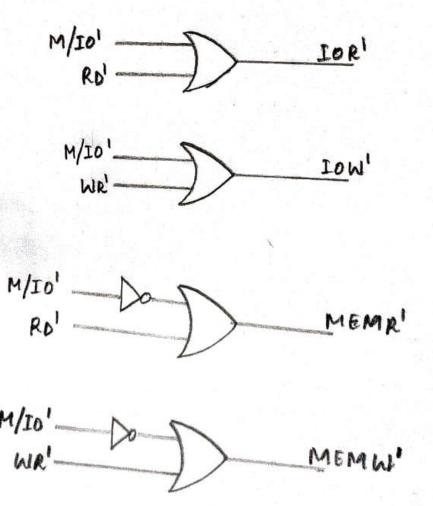
Data Bus [8086]



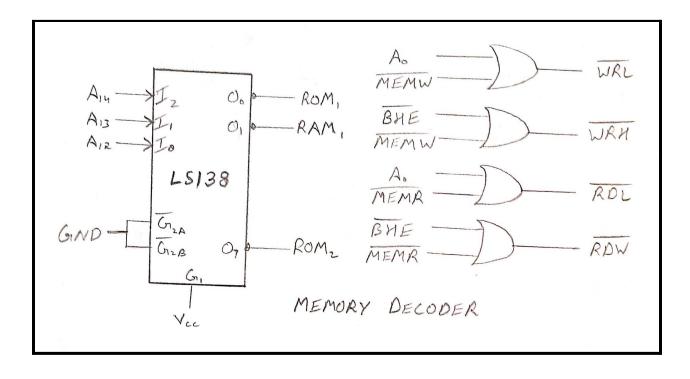
Control Bus [8086]



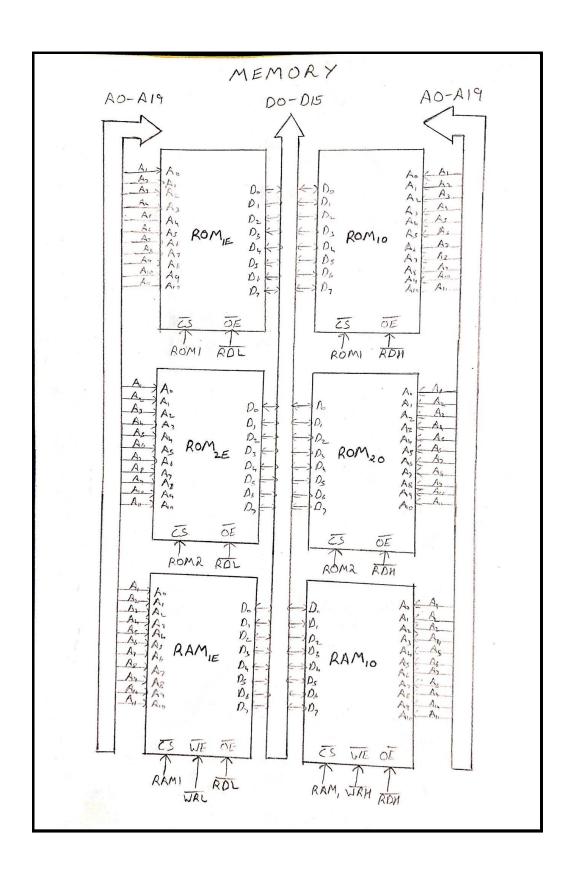
M/IO'	RD'	WR1	BUS CYCLE
1	0	1	MEMR'
1	1	0	WEM M
0	D Company Control of the Control of	1 1	IOR1
0	1	0	IOW'



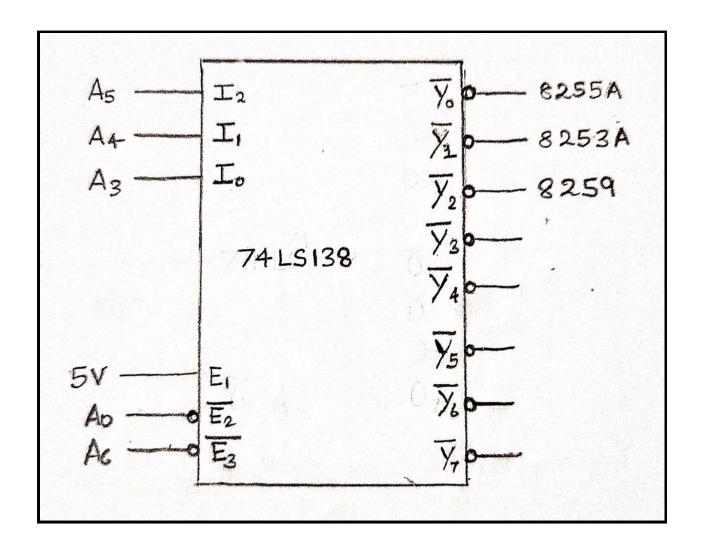
Memory Decoder

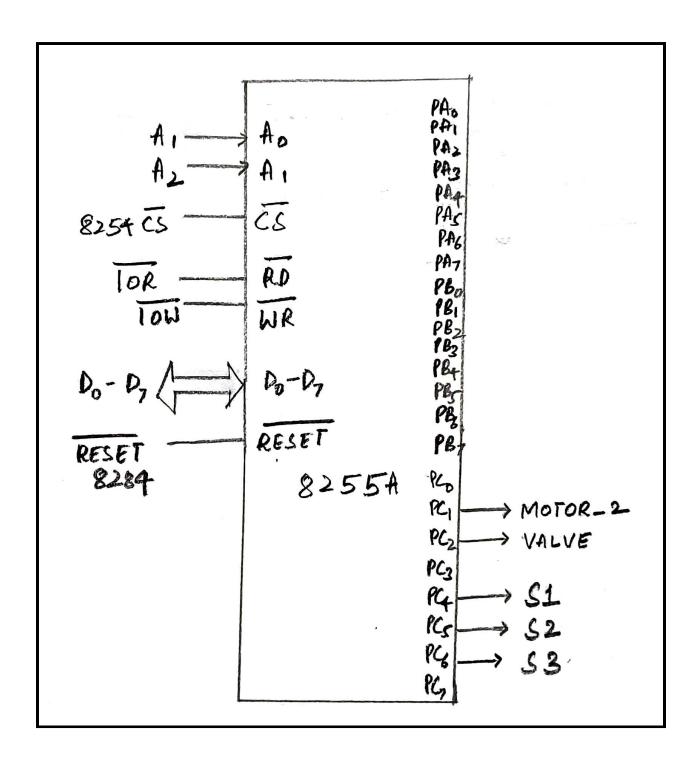


Memory Interfacing

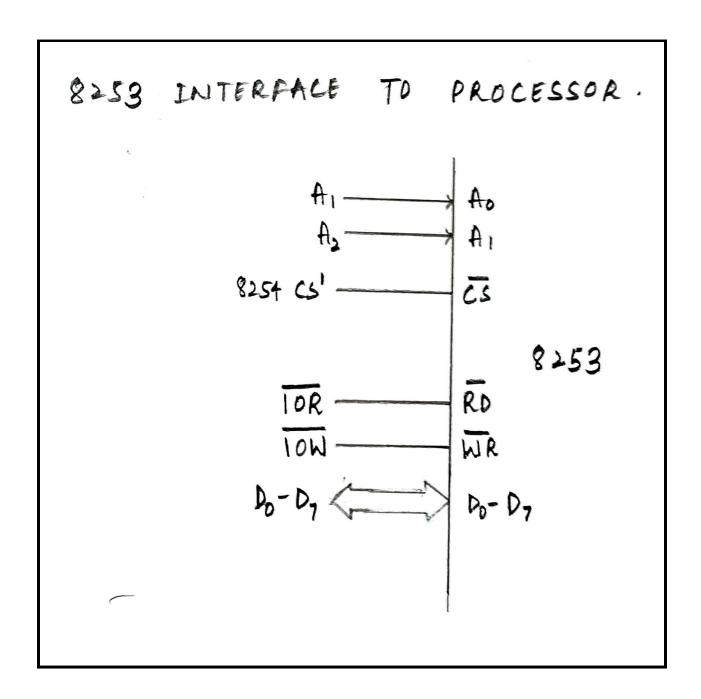


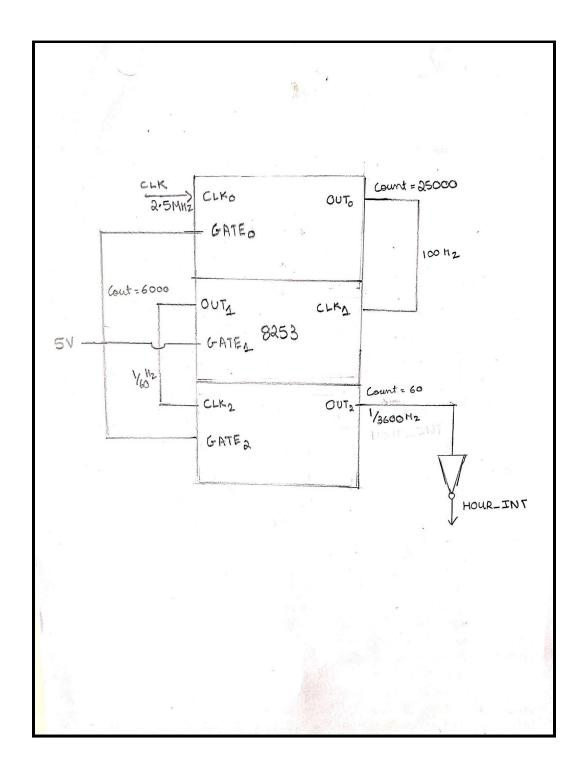
I/O Decoder

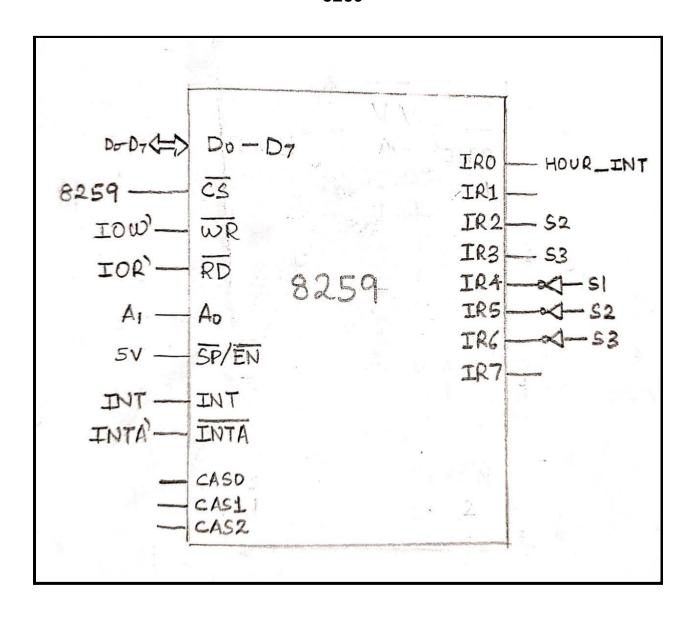




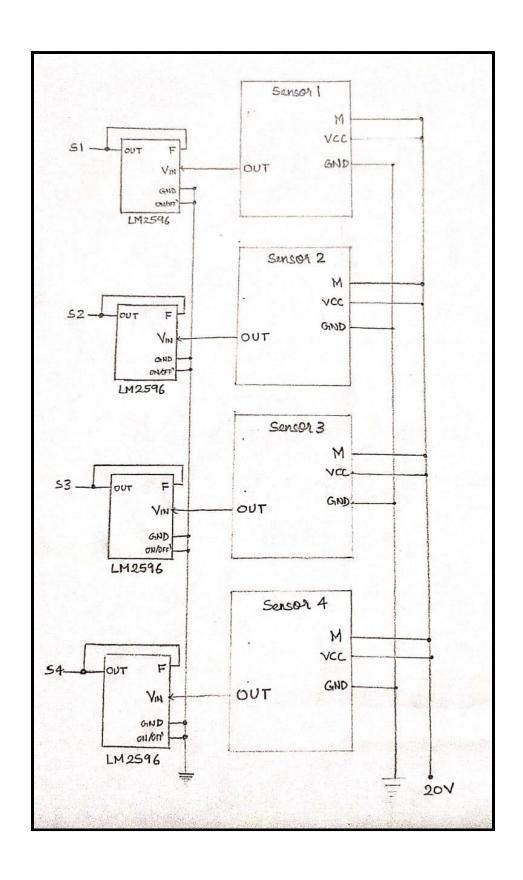
8253 Interface to the Processor







Sensors + LM2596



Motors + Valve + Relays

