**PRIYANKA VEPURI**

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DOB: 24th June 1998

**CAREER OBJECTIVE:**

To work for an organization which provides me the opportunity to improve my skills and knowledge to growth along with the organization objective.

**EDUCATIONAL QUALIFICATION:**

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| --- | --- | --- | --- |
| **Qualification** | **Name of Institute** | **Year of Passing** | **% or CGPA** |
| B.Tech in Electronics and Communication Engineering | Velagapudi Ramakrishna Siddhartha Engineering College | 2019 | CGPA: 6.18 |
| Intermediate | Sri Chaitanya Junior College | 2015 | 89.8% |
| Secondary | Veeramachaneni Paddayya Siddhartha Public School | 2013 | CGPA: 7.2 |

**SEMINARS:**

* Presented a seminar on **Gadget talk** in my college to my fellow students in Nov’18.
* Often volunteered to handle seminars being a member of **IEEE**.

**WORKSHOPS:**

Attended a workshop on “Ethical Hacking and Cyber Security” organized by **Bytecode** Cyber Security at **JNTUK, Kakinada**.

**INDUSTRIAL TRAINING:**

* Internship at Dr**. Narla Tatarao Thermal Power Station**, Ibrahimpatnam, Vijayawada, Andhra Pradesh from 1st May to 30th May 2018.
* Got trained in NI Lab View – graphical programming language that uses icons instead of lines of text to create applications.
* Visited one of the most advanced steel plant in India i.e. Vizag Steel Plant where I got direct exposure to today’s technology present in my country.

**ACADEMIC PROJECT:**

**Surface Wave Suppression In Patch Antennas Using EBG Structures**

This focuses on an antenna array structure that is designed and evaluates the mutual coupling between antenna elements. Based on the evaluation, an EBG structure is designed using Dispersion diagram and identifies the stop band frequencies for the designed EBG, and finally the mutual coupling is reduced compared with the non-EBG patch array. By introducing these structures into the patch array various parameters like gain, directivity, radiation pattern in an antenna can be enhanced. These antennas and EBG structures are designed using **ANSOFT High Frequency Structural Simulator (HFSS).**

**SKILLS:**

* Committed and Goal oriented with positive attitude.
* Good communication skills and flexible mentality.
* Self-motivated & Team player.
* Technical:
* Programming Languages: C, Data Structures, Operating Systems, Verilog HDL, Python and basics of Database Management Systems
* OS: Windows XP/7/8/10, ios, Mac OS, Android, Linux
* Microsoft Office
* Software and Simulation Tools: Matlab, LabVIEW, Xilinx, Cadence tool, Multisim, Keil uvision, Flash magic, Code Composer Studio, ANSOFT High Frequency Structural Simulator(HFSS)

**ACHIEVEMENTS:**

* First prize in Technical Quiz conducted in **Annual** **Festival Of Siddhartha Engineering College**-2018.
* Completed the Abacus Mental Arithmetic Training Program Grade-1 on 25.07.2004.

**CO-CURRICULAR ACTIVITIES:**

* Participated in the “WAVE your FLAG” Quiz Competition conducted by ING Life Insurance at **VPSPS** in 2013.
* Participated in Paper Presentation conducted in **ZEITGEIST** 2017 at **JNTUK, Kakinada.**

**EXTRA CURRICULAR ACTIVITIES:**

* Participated in the badminton tournament representing the Electronics department held by Siddhartha Academy in the year 2015.
* Participated in **World’s Space Week** organized by **SDSC SHAR, ISRO** at **VRSEC** in 2016.
* Acted as a student coordinator in organizing **Annual Festival Of Siddhartha Engineering College** for two years2017 and 2018.
* Participated in a 5K JIO AMARAVATI MARATHON run in Jan 2017.
* Enjoy modeling.
* Actively participated in various social activities conducted by my college.

**LANGUAGES KNOWN:**

Fluent in English, Telugu and Hindi

**INTERESTS:**

* Sketching and Painting
* Photography
* Craft
* Gardening
* Playing Badminton

**STRENGTHS:**

* Ability to Work under Pressure
* Time Management
* Conflict Resolution
* Team Work