



### 8 KB EPROM

A12:0: address input  
 D7:0: data output  
 CEb: chip enable  
 OEb: output enable  
 PGMb: program  
 VPP: program voltage  
 NC: no connection

```

assign D = (~CEb & ~OEb) ? ROM[A]
          : 8'bZ;
  
```