Assembly	**Field Values**					**Machine Code**				
	$imm_{11:0}$	rs1	funct3	rd	op	$imm_{11:0}$	rs1	funct3	rd	op
`cssrw x9, mscratch, x8`	0x340	8	1	9	115	0011 0100 0000	01000	001	01001	111 0011
	12 bits	5 bits	3 bits	5 bits	7 bits	12 bits	5 bits	3 bits	5 bits	7 bits

op

111 0011 (0x340414F3)

7 bits