

Assembly

`csrrs x7, mcause, x3`

imm _{11:0}	rs1	funct3	rd	op
0x342	3	2	7	115
12 bits	5 bits	3 bits	5 bits	7 bits

imm _{11:0}	rs1	funct3	rd	op
0011 0100 0010	00011	010	00111	111 0011
12 bits	5 bits	3 bits	5 bits	7 bits

op
111 0011 (0x3421A3F3)
7 bits