```
# Address              RISC-V Assembly
0x0000540C            jal ra, func1
0x00005410            add s1, s2, s3
...                   ...

0x000ABC04  func1: add s4, s5, s8
...                   ...
```

func1 is **0xA67F8 bytes** past `jal`

imm = 0xA67F8	0	1 0 1 0	0 1 1 0	0 1 1 1	1 1 1 1	1 0 0 0
bit number	20	19 18 17 16	15 14 13 12	11 10 9 8	7 6 5 4	3 2 1 0

Assembly   **Field Values**   **Machine Code**

imm$_{20,10:1,11,19:12}$ rd   op   imm$_{20,10:1,11,19:12}$ rd   op

`jal ra, func1`
`jal x1, 0xA67F8`

0111 1111 1000 1010 0110	1	111		0111 1111 1000 1010 0110	00001	110 1111	(0x7F8A60EF)
20 bits	5 bits	7 bits		20 bits	5 bits	7 bits	