













































































































































```

lw  s8, 40(s0)
sub r0, t2, t3
and s10, s4, r0
or  s11, t5, t6
add s9, s8, t1
sw  s7, 80(s11)

```

2-cycle RAW (from `s8` in `add` to `s8` in `lw`)

RAW (from `r0` in `and` to `r0` in `sub`)

RAW (from `s8` in `add` to `s8` in `DM` stage of `lw`)









































