Memory

Byte Address	D3	D2	D1	D0
Data	F7	8C	42	03

Registers

s1	00	00	00	8C	lbu s1, 2(s4)
s2	FF	FF	FF	F7	lb s2, 3(s4)
s3	xx	xx	xx	9B	sb s3, 1(s4)