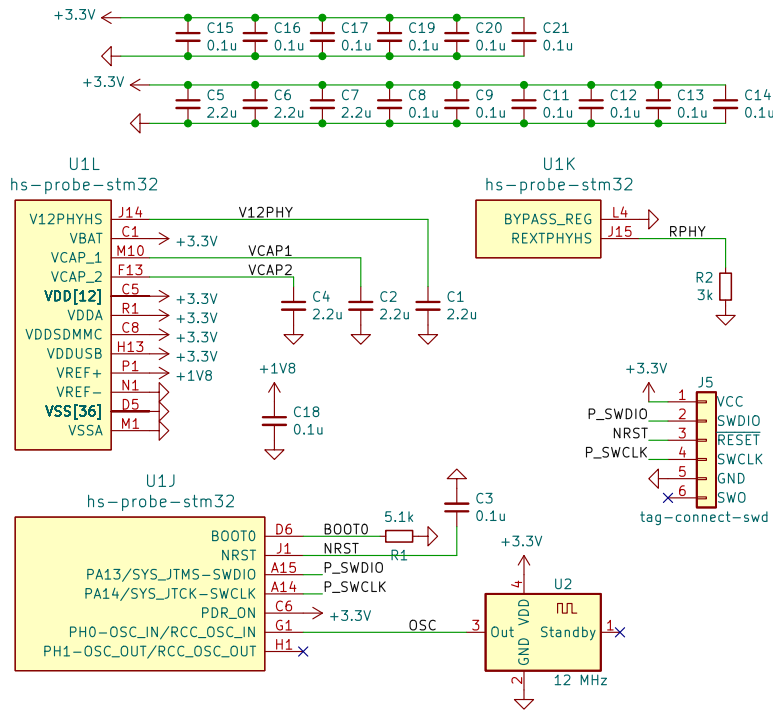
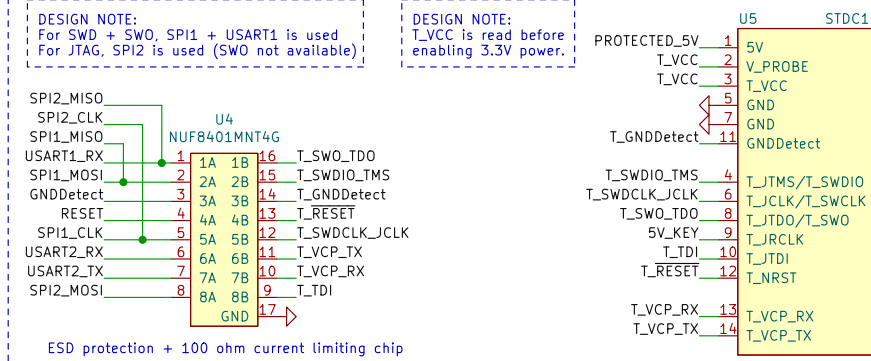


MCU Power, Config and Clock



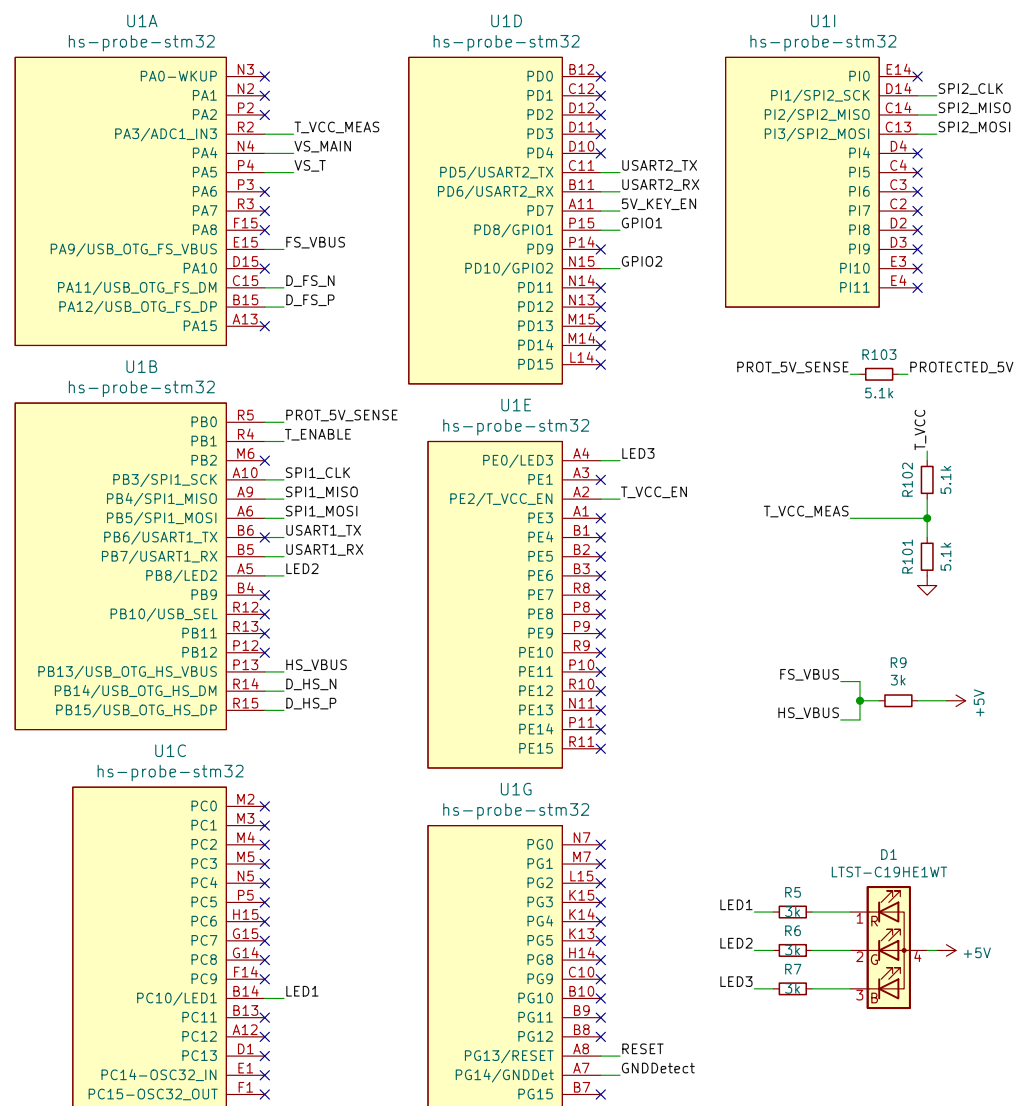
- Target SWD/JTAG port with ESD protection



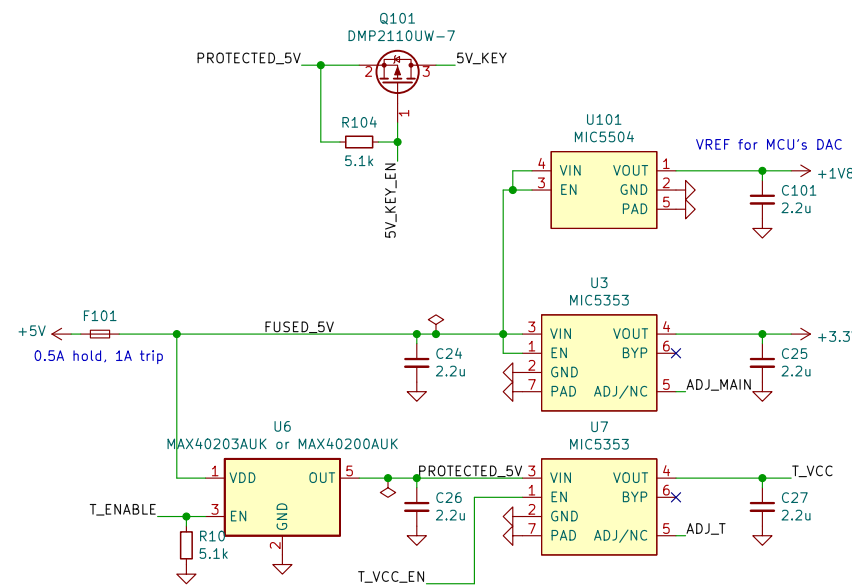
- Board-edge castellated vias



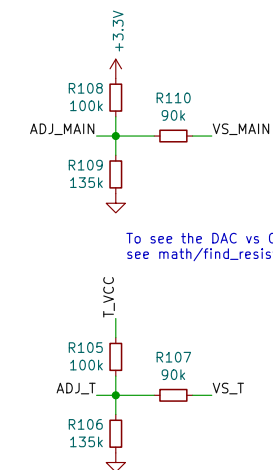
MCU I/O



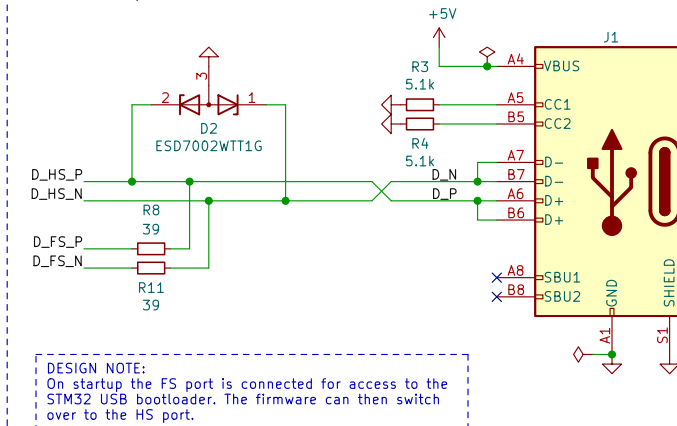
Probe power and optional target power



Probe and target voltage
feedback with DAC control



USB-C port with USB MUX



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Designed by: Emil Fresk
Twitter / Github: @korken89
Link: <https://github.com/probe-rs/hs-probe>

Sheet: /
File: hs-probe.kicad_sch

Title: Rusty High-Speed Probe

Size: A3

Date: 2022-01-07

Rev: v2.0

Size: A5	Date:
KiCad E.D.A.	kicad 6.0.0

Id: 1/1