Static Factorisation of Probabilistic Programs With User-Labelled Sample Statements and While Loops

MARKUS BÖCK, TU Wien, Austria JÜRGEN CITO, TU Wien, Austria

It is commonly known that any Bayesian network can be implemented as a probabilistic program, but the reverse direction is not so clear. In this work, we address the open question to what extent a probabilistic program with user-labelled sample statements and while loops can be represented as a graphical model. To this end, we introduce operational semantics for probabilistic programming languages (PPLs) with user-labelled sample statements that is modelled after real-world PPLs inhabiting this language feature like Gen, Turing, or Pyro. Furthermore, by formally translating a program to its control-flow graph, we define a sound static analysis that approximates the dependency structure of the random variables in the program. As a result, we obtain a factorisation of the implicitly defined program density, which allows us to show the equivalence of programs to either Bayesian or Markov networks. The factorisation result holds even for programs that define an unbounded number of random variables. In a case study, we use the statically obtained structure to speed up posterior inference, which demonstrates that our static analysis enables new opportunities for optimising inference algorithms.

CCS Concepts: • Theory of computation \rightarrow Operational semantics; Program analysis; • Mathematics of computing \rightarrow Bayesian computation; Bayesian networks; Markov networks.

Additional Key Words and Phrases: Probabilistic programming, operational semantics, static program analysis, factorisation, Bayesian networks, Markov networks

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1 Introduction

Probabilistic programming provides an intuitive means to specify probabilistic models as programs. Typically, probabilistic programming systems also come with machinery for automatic posterior inference. This enables practitioners without substantial knowledge in Bayesian inference techniques to work with complex probabilistic models.

Besides these practical aspects, there is also a long history of studying probabilistic programming languages (PPLs) from a mathematical perspective [Barthe et al. 2020; Kozen 1979]. It is commonly known that any Bayesian network [Koller and Friedman 2009] can be implemented as a probabilistic program [Gordon et al. 2014], but the reverse direction is not so clear. The understanding is that first-order probabilistic programs without loops can be compiled to Bayesian networks, because the number of executed sample statements is fixed and finite, such that each sample statement

Authors' Contact Information: Markus Böck, markus.h.boeck@tuwien.ac.at, TU Wien, Vienna, Austria; Jürgen Cito, juergen. cito@tuwien.ac.at, TU Wien, Vienna, Austria.

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corresponds to exactly one random variable [Borgström et al. 2011; Sampson et al. 2014; van de Meent et al. 2018]. Generally, a graphical representation of a probabilistic program is desirable as it lends itself for optimising inference algorithms by exploiting the dependency structure between random variables. In fact, many probabilistic programming languages require the user to explicitly construct the model in a graphical structure [Lunn et al. 2000; McCallum et al. 2009; Milch et al. 2007; Minka 2012; Salvatier et al. 2016].

However, in the universal probabilistic programming paradigm probabilistic constructs are embedded in a Turing-complete language [Goodman et al. 2012]. While loops and recursive function calls easily lead to an unbounded number of executed sample statements which makes a direct compilation to a graphical representation impossible. Even more, many PPLs allow the user to label each sample statement with a dynamic address. This further complicates matters, because in this case even a single sample statement may correspond to a potentially infinite number of random variables. As a consequence, many approaches resort to dynamic methods to leverage the dependency structure of this kind of probabilistic programs for efficient inference [Chen et al. 2014; Wu et al. 2016; Yang et al. 2014].

In this work, we are expanding the theoretical knowledge about the connection between probabilistic programs and graphical models by proving that the density of a probabilistic program with K user-labelled sample statements decomposes into K factors. This is true even if the program contains while loops. Moreover, the dependency structure implied by the factorisation can be computed *statically* which enables new opportunities for the optimisation of inference algorithms.

To this end, we present density-based operational semantics for PPLs that make use of user-defined addresses in sample statements, like Gen [Cusumano-Towner et al. 2019], Turing [Ge et al. 2018], or Pyro [Bingham et al. 2019]. Addresses are dynamically computed identifiers for random variables, which can be different from the name of the program variable they are assigned to. Below are examples of such address-based sample statements in several PPLs, where the address expressions are highlighted with red:

We interpret a probabilistic program as a function p, called program density, that maps a trace tr to its (unnormalised) density $p(tr) \in \mathbb{R}_{\geq 0}$. In this context, a trace is simply a map from addresses to values, where we need not further specify how these values are set. In practice, the values are set depending on the used inference algorithm. For instance, for a Metropolis Hastings algorithm, one could sample these values from a proposal distribution [Wingate et al. 2011], whereas for the Hamiltonian Monte Carlo algorithm one would set these values by simulating a Hamiltonian physics system [Hoffman et al. 2014]. In variational inference methods, these values would be perturbed by gradient ascent [Kucukelbir et al. 2017].

Having described the syntax and semantics of our formal PPL in Section 2, we will prove a factorisation of the program density. Namely, if a program contains K sample statements, then the program density can be written as a product of K terms: $p(tr) = \prod_{k=1}^K p_k(tr)$. For each factor p_k , we can *statically* determine the set of addresses that contribute to it. To evaluate this factorisation from a practical perspective, we have implemented a static analysis that can generate sub-programs for each factor. In Section 5, we demonstrate that these sub-programs can be used to speed-up the runtime of a Metropolis-Hastings inference algorithm on a benchmark set of 15 probabilistic programs. The majority of these benchmark programs make use of while loops to define an unbounded number of random variables.

1.1 Overview

Section 2. Address-Based Semantics For Probabilistic Programs introduces our semantics for a formal probabilistic programming language. It is a generalisation of the operational semantics of Core Stan [Gorinova et al. 2019]. Instead of interpreting the program density p as a function over only a fixed set of finite variables, we extended this approach to interpret the density as function over traces. In contrast to Gorinova et al., this allows us to consider programs with dynamic user-defined addresses and while loops. These language features greatly expand the class of expressible probabilistic models, for instance, models with an infinite number of random variables.

Section 3. **Static Factorisation for Programs Without Loops** examines the factorisation of the program density for the simplified case where programs do not contain while loops.

In Section 3.2, we present a static provenance analysis that finds the dependencies of a program variable x at any program state σ . The dependencies are described by specifying for which addresses a trace tr needs to be known to also know the value of x in σ . This analysis forms the basis for finding the factorisation over addresses and operates on the control-flow graph (CFG) of a program.

Formally mapping the operational semantics onto the CFG allows us to prove soundness of the provenance analysis in Section 3.2. This is achieved by constructing evaluation functions that map traces directly to the values of variables at program states. These functions require the traces to be only evaluated at the addresses the variable statically depends on.

In Section 3.3, we prove $p(\mathsf{tr}) = \prod_{k=1}^K p_k(\mathsf{tr})$ for programs without loops by explicitly constructing one factor per sample statement p_k from the evaluation functions. If we further restrict the program to static addresses, we arrive at the language of Gorinova et al., where our factorisation theorem implies equivalence to Bayesian networks. We show that already in the case of dynamic addresses, programs are only equivalent to the more general Markov networks, because of the possibility of cyclic dependencies and an infinite number of random variables.

Section 4. Static Factorisation for Programs With Loops presents a way to generalise the factorisation result of Section 3 to the much more challenging case where programs may contain while loops. The idea is to mathematically unroll while loops to obtain a directed acyclic graph that essentially represents all possible program paths. This *unrolled CFG* preserves the program semantics and allows us to effectively reuse the proofs of Section 3. In this case the density still factorises into K terms – one factor per sample statement. Like dynamic addresses, while loops may also be used to model an infinite number of random variables and thus, the resulting factorisation in general implies the equivalence of programs to Markov networks.

Section 5. Case Study: Speeding-Up Posterior Inference demonstrates how the statically obtained factorisation of the program density is non-trivial such that it can be used to significantly decrease the runtime of a Metropolis Hastings inference algorithm. By generating sub-programs for each factor, our static analysis enables the optimisation of the inference algorithms in a way that is typically reserved for PPLs that require the explicit construction of a graphical model. Such a static approach was previously not possible for programs with dynamic addresses or while loops. Several examples of such programs are included in our benchmark set.

2 Address-Based Semantics For Probabilistic Programs

First, we introduce the syntax of our formal probabilistic programming language in terms of expressions and statements similar to Gorinova et al. [2019] and Hur et al. [2015]. We assume that the constants of this language range over a set of values including booleans, integers, real numbers, immutable real-valued vectors, finite-length strings, and a special null value. We define $\mathcal V$ to be the set of all values. A program contains a finite set of program variables denoted by x, x_i , y, or z. We further assume a set of built-in functions g. We assume that functions g are total and return

null for erroneous inputs. The only non-standard construct in the language are sample statements. For sample statements, the expression E_0 corresponds to the user-defined sample address, which is assumed to evaluate to a string. The symbol f ranges over a set of built-in distributions which are parameterised by arguments E_1, \ldots, E_n .

Syntax of Expressions:

Syntax of Statements:

E ::=	expression	S ::=	statement
c	constant	x = E	assignment
x	variable	$S_1; S_2$	sequence
$g(E_1,\ldots,E_n)$	function call	if E then S_1 else S_2	if statement
		skip	skip
		while E do S	while loop
		$x = sample(E_0, f(E_1, \dots, E_n))$	sample

2.1 Operational Semantics

The meaning of a program is the density implicitly defined by its sample statements. This density is evaluated for a *program trace* and returns a real number. A program trace is a mapping from finite-length addresses to numeric values, $tr: Strings \to V$. We denote the set of all traces with T. How the value at an address is determined is not important for this work and in practice typically depends on the algorithm that is used to perform posterior inference for the program. For instance, if an address α corresponds to an observed variable, the value $tr(\alpha)$ is fixed to the data. If an address α does not appear in a program, then its value can be assumed to be $tr(\alpha) = null$.

For each trace tr, the operational semantics of our language are defined by the big-step relation $(\sigma, S) \downarrow^{\text{tr}} \sigma'$, where S is a statement and $\sigma \in \Sigma$ is a program state – a finite map from program variables to values

$$\sigma ::= x_1 \mapsto V_1, \dots, x_n \mapsto V_n, \quad x_i \text{ distinct}, \ V_i \in \mathcal{V}.$$

A state can be naturally lifted to a map from expressions to values $\sigma \colon \mathbf{Exprs} \to \mathcal{V}$:

$$\sigma(x_i) = V_i, \quad \sigma(c) = c, \quad \sigma(g(E_1, \dots, E_n)) = g(\sigma(E_1), \dots, \sigma(E_n)).$$

The operational semantics for the non-probabilistic part of our language are standard:

Above, $\sigma[x \mapsto V]$ denotes the updated program state σ' , where $\sigma'(x) = V$ and $\sigma'(y) = \sigma(y)$ for all other variables $y \neq x$. The inference rules are to be interpreted inductively such that the semantics of a while loop can be rewritten as

$$(\sigma, \mathsf{while} \ E \ \mathsf{do} \ S) \ {\downarrow^\mathsf{tr}} \ \sigma' \iff \quad \exists n \in \mathbb{N}_0 \colon \left((\sigma, \mathsf{repeat}_n(S)) \ {\downarrow^\mathsf{tr}} \ \sigma' \wedge \sigma'(E) = \mathsf{false} \right) \wedge \\ \forall m < n \colon \left((\sigma, \mathsf{repeat}_m(S)) \ {\downarrow^\mathsf{tr}} \ \sigma'_m \wedge \sigma'_m(E) = \mathsf{true} \right),$$

where $\operatorname{repeat}_n(S) = (S; ...; S)$ repeated *n*-times. In particular, if a while loop does not terminate for state σ then $\nexists \sigma' : (\sigma, \text{while } E \text{ do } S) \downarrow^{\operatorname{tr}} \sigma'$. This makes the presented operational semantics partial,

where errors and non-termination are modelled implicitly. Formally, \parallel^{tr} is the least-relation closed under the stated rules.

At sample statements, we inject the value of the trace at address V_0 . We multiply the density, represented by the reserved variable \mathbf{p} , with the value of function pdf_f evaluated at $\mathrm{tr}(V_0)$. Note that the function pdf_f may be an arbitrary function to real values. However, in the context of probabilistic programming, we interpret pdf_f as the density function of distribution f with values in $\mathbb{R}_{>0}$. Lastly, the value $\operatorname{tr}(V_0)$ is stored in the program state at variable x. The semantics of sample statements are summarised in the following rule:

$$\frac{\forall i \colon \sigma(E_i) = V_i \land V_i \neq \text{null} \qquad V_0 \in \mathbf{Strings} \qquad V = \mathsf{tr}(V_0) \land V \neq \mathsf{null}}{(\sigma, x = \mathsf{sample}(E_0, f(E_1, \dots, E_n))) \ \Downarrow^{\mathsf{tr}} \ \sigma[x \mapsto V, \mathbf{p} \mapsto \sigma(\mathbf{p}) \times \mathsf{pdf}_f(V; V_1, \dots, V_n)]}$$

Finally, the meaning of a program S is defined as the mapping of trace to density. This definition is well-defined, because the semantics are deterministic for each trace. If a trace leads to errors or non-termination, then the density is undefined.

Definition 1. The density of a program S is the function $p_S \colon \mathcal{T} \to \mathbb{R}_{\geq 0}$ given by

$$p_S(\mathsf{tr}) \coloneqq \begin{cases} \sigma(\boldsymbol{p}) & \text{if } \exists \, \sigma \colon ((x_i \mapsto \mathsf{null}, \boldsymbol{p} \mapsto 1), S) \, \Downarrow^{\mathsf{tr}} \, \sigma \\ \text{undefined} & \text{otherwise} \end{cases}$$

Listing 1. Simple probabilistic program with stochastic branching.

y = sample("y", Bernoulli(0.25))

z = sample("z", Bernoulli(0.75))

if x == 1 then

else

```
p = sample("p", Uniform(0,1))
x = sample("x", Bernoulli(p))
```

Listing 2. Program of Listing 1 rewritten with dynamic addresses.

```
p = sample("p", Uniform(0,1))
x = sample("x", Bernoulli(p))
if x == 1 then
    addr = "y"; prob = 0.25;
    addr = "z"; prob = 0.75;
r = sample(addr, Bernoulli(prob))
```

Consider the simple probabilistic program in Listing 1. The density defined by our operational semantics is given below, where δ_v denotes the delta function whose value is 1 if v equals true and 0 otherwise.

```
p(\mathsf{tr}) = \mathrm{pdf}_{\mathrm{Uniform}}(\mathsf{tr}("p"); 0, 1)
              \times pdf_{Bernoulli}(tr("x"); tr("p"))
              \times \left(\delta_{\mathsf{tr}("x")} pdf_{\mathsf{Bernoulli}}(\mathsf{tr}("y"); 0.25) + (1 - \delta_{\mathsf{tr}("x")})\right)
              \times \left(\delta_{\mathsf{tr}("x")} + (1 - \delta_{\mathsf{tr}("x")}) pdf_{\mathsf{Bernoulli}}(\mathsf{tr}("z"); 0.75)\right).
```

As the addresses in Listing 1 are static and identical to the program variables, the program is well-described by many existing PPL semantics. However, in Listing 2, we rewrite the program with dynamic addresses which results in the same above density. Most formal PPLs studied in prior work do not support user-defined addresses for sample statements and cannot interpret a program with three sample statements as a four-dimensional joint density. In contrast, our presented semantics correctly interprets the program with dynamic addresses. This interpretation accurately describes the density of programs written in PPLs that lazily evaluate if statements like Gen or Pyro.

To the best of our knowledge, there are only two existing semantic models that support userdefined addresses [Lee et al. 2019; Lew et al. 2023]. These approaches are denotational and samplingbased as opposed to our density-based operational semantics. As sampling-based semantics they

```
Listing 3. Program with duplicate address.
```

```
x = sample("x", Normal(0.,1.))
x = sample("x", Normal(2*x+1,1.))

Listing 4. Program with random address.
n = sample("n", Poisson(5))
x = sample("x_"+str(n), Normal(0.,1.))
```

Listing 5. Program with while loop implementing a Geometric distribution.

```
b = true; i = 0;
while b do
    i = i + 1
    b = sample("b_"+i, Bernoulli(0.25))
```

require that an address appears only once during the execution of a program. Our semantics do not rely on this assumption and can explain programs like the one shown in Listing 3. Even though the program does not correspond to a probability distribution, it can in fact be translated to density-based PPLs like Stan or Turing. Lastly, the presented semantics correctly describe programs with while loops and random addresses. Such programs are significantly harder to describe, because both cases easily lead to an unbounded number of addresses as can be seen in Listing 4 and Listing 5.

2.2 Remark Regarding a Measure-theoretic Interpretation

Even though it suffices to consider the explicit computation of the density of a probabilistic program in this work, a measure-theoretic interpretation of the presented operational semantics is still worth exploring. The density of a measure μ with respect to some reference measure ν is formally given by the Radon-Nikodym derivative p such that $\mu(A) = \int_A p \ d\nu$. Thus, the measure-theoretic interpretation of the operational semantics is described by a construction of a measure space of traces $(\mathcal{T}, \Sigma, \nu)$ such that the density p_S is measurable. Then, $\mu_S(A) = \int_A p_S \ d\nu$ is the measure denoted by the program S. See Appendix C for a possible construction of this measure space.

3 Static Factorisation for Programs Without Loops

The goal of this work is to statically find a factorisation of the density implicitly defined by a probabilistic program in terms of addresses. This static analysis will operate on the control-flow graph (CFG) of a program. We begin by considering programs without loops first and describe how we can formally translate such a program to its corresponding CFG. We will equip this CFG with semantics equivalent to the operational semantics of the original program. The following construction is fairly standard but necessary for formally verifying the correctness of the factorisation.

3.1 Control-Flow Graph

A CFG has five types of nodes: start, end, assign, branch, and join nodes. Branch nodes have two successor nodes, end nodes have no successor, all other nodes have one. Only join nodes have multiple predecessors. The CFG contains exactly one start node from which every other node is reachable. Further, it contains exactly one end node that is reachable from any other node. We describe the recursive translation rules for program S as diagrams, which serve to convey the general idea of the construction. For a formal description see Appendix A. Sub-graphs are drawn by circular nodes. Branch and join nodes are drawn as diamond nodes, the latter filled black. Assign, start, and end nodes have rectangular shape. In text, branch nodes are written as Branch(E). Assign nodes are denoted with Assign(x = E) and $Assign(x = sample(E_0, ...))$.

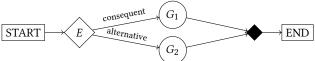
- The CFG of a skip-statement, S = skip, consists of only start and end node: $\boxed{\text{START} \longrightarrow \text{END}}$
- The CFG of an assignment, x = E, or sample statement $x = \text{sample}(E_0, f(E_1, ..., E_n))$, is a sequence of start, assign, and end node:

```
\boxed{\text{START} \longrightarrow x = E} \longrightarrow \boxed{\text{END}} \qquad \boxed{\text{START} \longrightarrow x = \text{sample}(E_0, f(E_1, \dots, E_n))} \longrightarrow \boxed{\text{END}}
```

• The CFG for a sequence of statements, $S = S_1$; S_2 , is recursively defined by "stitching together" the CFGs G_1 and G_2 of S_1 and S_2 :

$$\boxed{\text{START}} \longrightarrow G_1 \longrightarrow G_2 \longrightarrow \boxed{\text{END}}$$

• The CFG of an if statement, $S = (if E then S_1 else S_2)$, is also defined in terms of the CFGs of S_1 and S_2 together with a branch node B = Branch(E) and a join node J:



We denote the two successors of the branch node B as consequent(B) and alternative(B). Furthermore, we say the tuple (B, D) is a brain-join pair and define BranchJoin(D) to be the set of all branch-join pairs of D.

3.1.1 CFG Semantics. For a control-flow graph G we define the small-step semantics for a trace tr as a relation that models the transition from node to node depending on the current program state:

$$(\sigma, N) \xrightarrow{\mathsf{tr}} (\sigma', N')$$

The semantics for the CFG of program *S* are set-up precisely such that they are equivalent to the operational semantics of program *S*. The transition rules depending on node type and program state are given below:

$$\frac{N = \text{START} \quad N' = \text{successor}(N)}{(\sigma, N) \xrightarrow{\text{tr}} (\sigma, N')} \qquad \frac{N = \text{Assign}(x = E) \quad N' = \text{successor}(N)}{(\sigma, N) \xrightarrow{\text{tr}} (\sigma[x \mapsto \sigma(E)], N')}$$

Finally, the meaning of a CFG is a mapping from trace to density. It is defined for traces tr if there exists a path of node transitions from start to end node.

DEFINITION 2. Let $\sigma_0 = (x_i \mapsto \text{null}, \boldsymbol{p} \mapsto 1)$ be the initial program state. The density of a CFG G is the function $p_G \colon \mathcal{T} \to \mathbb{R}_{\geq 0}$ given by

$$p_G(\mathsf{tr}) \coloneqq \begin{cases} \sigma(\boldsymbol{p}) & \text{if } \exists \ \sigma \in \Sigma, n \in \mathbb{N} \colon \forall i = 1, \dots, n \colon \exists \ \sigma_i, N_i \colon \\ (\sigma_0, \mathit{START}) \xrightarrow{\mathsf{tr}} \dots \xrightarrow{\mathsf{tr}} (\sigma_i, N_i) \xrightarrow{\mathsf{tr}} \dots \xrightarrow{\mathsf{tr}} (\sigma, \mathit{END}) \\ \text{undefined} & \textit{otherwise}. \end{cases}$$

Proposition 1. For all programs S without while loops and corresponding control-flow graph G, it holds that for all traces tr

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}).$$

PROOF. By structural induction. See Appendix B.1.

3.2 Static Provenance Analysis

The provenance of variable x in CFG node N are all the addresses \subseteq Strings that contribute to the computation of its value in node N. In this section, we define an algorithm that operates on the CFG and statically determines the provenance for any variable x at any node N. As this is a static algorithm, it is an over-approximation of the true provenance. In Section 3.2.1, we will prove soundness of the presented algorithm. We show that indeed the value of variable x at node N can be computed from the values in the trace t t at the addresses which make up the provenance. But first, we introduce some useful concepts which are similar to definitions found in data-flow analysis [Cooper and Torczon 2011; Hennessy and Patterson 2011].

DEFINITION 3. For a CFG G, let AssignNodes(G, x) be all nodes N' in G that assign variable x, N' = Assign(x = ...). The set of reaching definitions for variable x in node N is defined as

$$RD(N, x) = \{N' \in AssignNodes(G, x) : \exists path(N', ..., N_i, ..., N), N_i \notin AssignNodes(G, x)\}.$$

Intuitively, the set of reaching definitions are all nodes that could have written the value of x in the program state before executing node N.

DEFINITION 4. For CFG G and node N the set of branch parents is defined as

$$BP(N) = \{B : (B, J) \in BranchJoin(G) \land \exists \ path \ (B, ..., N, ..., J)\}.$$

A branch node B and join node J are a branch-join pair $(B, J) \in BranchJoin(G)$ if they belong to the same if-statement (see the translation rule for if-statements to CFGs).

The set of branch parents are all parent branch nodes of N that determine if the program branch that N belongs to is executed.

Lastly, we need the set of all strings that the address expression of a sample statement may evaluate to.

Definition 5. For a sample CFG node $N = Assign(x = sample(E_0, f(E_1, ..., E_n)))$, we define the set of all possible addresses as

addresses(
$$N$$
) = { $\sigma(E_0) : \sigma \in \Sigma$ } \subseteq Strings.

Now we are able to define the algorithm to statically approximate the provenance of any variable x at any node N denoted by $\operatorname{prov}(N,x)$ in Algorithm 1. This algorithm is inspired by standard methods for dependence analysis. In the algorithm we use $\operatorname{vars}(E)$, which is defined as the set of all program variables in expression E. It is often useful to determine the provenance of an entire expression instead of only a single variable. For expressions E, we lift the definition of prov

$$\operatorname{prov}(N, E) = \bigcup_{y \in \operatorname{vars}(E)} \operatorname{prov}(N, y).$$
 (1)

The algorithm works as follows. If we want to find $\operatorname{prov}(N,x)$, we first have to find all reaching definitions for x. If the reaching definition N' is a sample node, $x = \operatorname{sample}(E_0, \ldots)$, then the value of x can only dependent on the addresses generated by E_0 , addresses (N'), and the provenance of E_0 . If N' is an assignment node, x = E, we have to recursively find the provenance of the expression E. Lastly, there can be multiple reaching definitions for x in different branches and the value of x also depends on the branching condition. Thus, we also find all branch parents and recursively determine their provenance and add it to the final result.

In practice the potentially infinite set addresses (N) can be represented by the CFG node N itself. An implementation of Algorithm 1 would return a set of CFG sample nodes rather than a potentially infinite set of strings.

Algorithm 1 Computing the provenance set prov(N, x) statically from the CFG.

```
1: Input CFG node N, variable x
 2: prov ← Ø
 3: queue \leftarrow [(N, x)]
 4: while queue is not empty do
       (N, x) \leftarrow \text{queue.pop}()
 5:
       for N' \in RD(N, x) do
 6:
          if N' = Assign(x = sample(E_0, f(E_1, ..., E_n))) then
 7:
             prov \leftarrow prov \cup addresses(N')
 8:
             E' \leftarrow E_0
                                                                        \triangleright E_0 comes from pattern-matching N'
 9:
          else if N' = Assign(x = E) then
10:
             E' \leftarrow E
                                                                         \triangleright E comes from pattern-matching N'
11:
          for y \in vars(E') do
12:
             if is_unmarked((N', y)) then
13:
                mark((N', y))
14:
                queue.push((N', y))
15:
          for N_{bp} \in BP(N')
                                                                                       \triangleright N_{\rm bp} = {\rm Branch}(E_{N_{\rm bp}}) \, {\bf do}
16:
             for y \in vars(E_{N_{bn}}) do
17:
                if is_unmarked((N_{bp}, y)) then
18:
                   mark((N_{bp}, y))
19.
                   queue.push((N_{bp}, y))
20:
21: return prov
```

We get a clearer sense of the introduced definitions and Algorithm 1 by considering a simple program in Fig. 1. On the right you can see examples for reaching definitions and branch parents for several nodes drawn as arrows. These arrows illustrate how Algorithm 1 computes the provenance of variables m and s at the sample node for s. Note that if we were to change the if statement to (if (b == 1) then (m = 1) else (m = 1)), then the variable "b" would still be in the provenance set of m. This illustrates that the presented algorithm can only over-approximate the provenance and not compute it exactly.

```
b = sample("b", Bernoulli(0.5))
s = sample("s", InverseGamma(1.,1.))
if b == 1 then
m = sample("mu", Normal(0.,1.))
else
m = 1
x = sample("x", Normal(m, s))
N = Assign(x = ...)
V_s^N(tr) = tr("s")
V_m^N(tr) = ife(tr("b"), tr("mu"), 1)
END
Prov(m) = ("b", "mu")
```

Fig. 1. Reaching definitions, branch parents, and the provenance set for a simple example. The evaluation functions introduced in Section 3.2.1 are also given for node N.

3.2.1 Soundness. Recall that the purpose of finding the provenance of variable x at node N is to determine which addresses contribute to the computation of its value. To prove that Algorithm 1 indeed finds the correct set of addresses, first, we have to precisely define what we mean that a set of addresses contribute to the computation. Suppose that there is a function f that maps a trace f to a value f. The set of addresses f contribute to the computation of value f any other trace f that has the same values as f and f addresses f and f addresses f and f and f are value of f and f and f are value of f are value of f and f are value of f are value of f and f are value of f and f are value of f and f are v

DEFINITION 6. For a function from traces to an arbitrary set $B, f : \mathcal{T} \to B$, we write $f \in [\mathcal{T}|_A \to B]$ for a subset of addresses $A \subseteq$ Strings, if and only if

$$\forall \alpha \in A : \mathsf{tr}(\alpha) = \mathsf{tr}'(\alpha) \implies f(\mathsf{tr}) = f(\mathsf{tr}').$$

That is, changing the values of tr at addresses $\beta \in Strings \setminus A$ does not change the value f(tr).

The next lemma states that we can always over-approximate the set of addresses that contribute to the computation of the values f(tr).

LEMMA 1. If
$$A \subseteq A'$$
 and $f \in [\mathcal{T}|_A \to B]$, then $f \in [\mathcal{T}|_{A'} \to B]$. In other words,

$$A \subseteq A' \implies [\mathcal{T}|_A \to B] \subseteq [\mathcal{T}|_{A'} \to B]. \tag{2}$$

If we combine two functions with provenance A_1 and A_2 respectively, then the resulting function has provenance $A_1 \cup A_2$. This is shown in the following lemma.

Lemma 2. Let
$$f_1 \in [\mathcal{T}|_{A_1} \to B_1]$$
, $f_2 \in [\mathcal{T}|_{A_2} \to B_2]$, $h: B_1 \times B_2 \to C$. Then
$$\mathsf{tr} \mapsto h(f_1(\mathsf{tr}), f_2(\mathsf{tr})) \in [\mathcal{T}|_{A_1 \cup A_2} \to C].$$

This brings us to the main soundness proof of Algorithm 1. Proposition 2 states that we can equip each CFG node N with evaluation functions that depend on the addresses determined by prov in the sense of Definition 6. The evaluation functions directly map the trace tr to the values of variables at node N such that the values agree with the small-step CFG semantics relation $\stackrel{\mathsf{tr}}{\longrightarrow}$. Importantly, while the operational semantics are defined for each individual trace, the evaluation functions work for all traces. In Fig. 1, you can see concrete examples of these evaluation functions for a simple program.

PROPOSITION 2. For each node N in the CFG G and variable x, there exists an evaluation function

$$V_x^N \in [\mathcal{T}|_{\text{prov}(N,x)} \to \mathcal{V}],$$

such that for all traces tr with $\sigma_0 = (x_i \mapsto \text{null}, \boldsymbol{p} \mapsto 1)$ and execution sequence

$$(\sigma_0, START) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_i, N_i) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_l, END)$$

we have

$$\sigma_i(x) = V_x^{N_i}(\mathsf{tr}).$$

Proof. The CFG G is a directed acyclic graph and the statement is proven by induction on G. See Appendix B.2.

Intuitively, V_x^N computes the value of x before executing N. These evaluation functions V_x^N can be lifted to evaluation functions for expressions V_E^N :

$$V_c^N(\mathrm{tr}) = c, \quad V_{g(E_1,\dots,E_n)}^N(\mathrm{tr}) = g(V_{E_1}^N(\mathrm{tr}),\dots,V_{E_n}^N(\mathrm{tr})).$$

By Lemma 2, if $V_u^N \in [\mathcal{T}|_{\text{prov}(N,y)} \to \mathcal{V}]$ for all $y \in \text{vars}(E)$, then $V_E^N \in [\mathcal{T}|_{\text{prov}(N,E)} \to \mathcal{V}]$.

3.3 Static Factorisation Theorem for Programs Without Loops

With Proposition 2, we can prove the first factorisation theorem by combining the evaluation functions V_x^N to express the program density p in a factorised form.

THEOREM 1. Let G be the CFG for a program S without while loop statements. Let N_1, \ldots, N_K be all sample nodes in G. For each sample node $N_k = \text{Assign}(x_k = \text{sample}(E_0^k, f^k(E_1^k, \ldots, E_{n_k}^k)))$, let

$$A_k = \operatorname{addresses}(N_k) \cup \bigcup_{i=0}^{n_k} \operatorname{prov}(N_k, E_i^k) \cup \bigcup_{N' \in \operatorname{BP}(N_k)} \operatorname{prov}(N', E_{N'}).$$

Then, there exist functions $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ such that if $p_S(\mathsf{tr}) \neq \mathsf{undefined}$, then

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}) = \prod_{k=1}^K p_k(\mathsf{tr}).$$

PROOF. For each sample node N_k , define $b_k(\mathsf{tr}) \coloneqq \bigwedge_{N' \in \mathrm{BP}(N_k)} t_{N'}^k(V_{E_{N'}}^{N'}(\mathsf{tr}))$ where $t_{N'}^k(v) = v$ if N_k is in the consequent branch of N' and $t_{N'}^k(v) = \neg v$ if N_k is in the alternate branch. By Lemma 2, we have

$$b_k \in [\mathcal{T}|_{\bigcup_{N' \in \mathrm{BP}(N_k)} \mathrm{prov}(N', E_{N'})} \to \{\mathrm{true}, \mathrm{false}\}].$$

Above, $V_{E_{N'}}^{N'}(\mathsf{tr})$ are precisely the evaluations of the branch conditions for node N_k . Thus, $b_k(\mathsf{tr}) = \mathsf{true}$ if N_k is in the execution sequence for tr else false. Define the factor p_k as

$$p_k(\mathsf{tr}) \coloneqq \delta_{b_k(\mathsf{tr})} \mathsf{pdf}_{f^k} \left(\mathsf{tr}(V_{E_0^k}^{N_k}(\mathsf{tr})); V_{E_1^k}^{N_k}(\mathsf{tr}), \dots, V_{E_{n_k}^k}^{N_k}(\mathsf{tr}) \right) + (1 - \delta_{b_k(\mathsf{tr})}).$$

By Proposition 2, we have $V_{E_i^k}^{N_k} \in [\mathcal{T}|_{\operatorname{prov}(N_k, E_i^k)} \to \mathcal{V}]$ and $V_{E_0^k}^{N_k} \in [\mathcal{T}|_{\operatorname{prov}(N_k, E_0^k)} \to \operatorname{addresses}(N_k)]$. Thus, it follows from Lemma 2 that $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ by construction.

Again, consider the example program in Fig. 1. The density of this model factorises like below:

$$\begin{split} p(\mathsf{tr}) &= \mathsf{pdf}_{\mathsf{Bernoulli}}(\mathsf{tr}("\mathsf{b"}); 0.5) &\in [\mathcal{T}|_{\{"\mathsf{b"}\}} \to \mathbb{R}_{\geq 0}] \\ &\times \mathsf{pdf}_{\mathsf{InverseGamma}}(\mathsf{tr}("\mathsf{s"}); 1, 1) &\in [\mathcal{T}|_{\{"\mathsf{s"}\}} \to \mathbb{R}_{\geq 0}] \\ &\times \left(\delta_{\mathsf{tr}("\mathsf{b"})} \mathsf{pdf}_{\mathsf{Normal}}(\mathsf{tr}("\mathsf{mu"}); 0, 1) + (1 - \delta_{\mathsf{tr}("\mathsf{b"})})\right) &\in [\mathcal{T}|_{\{"\mathsf{b"}, "\mathsf{mu"}\}} \to \mathbb{R}_{\geq 0}] \\ &\times \mathsf{pdf}_{\mathsf{Normal}}(\mathsf{tr}("\mathsf{x"}); V_{\mathsf{m}}^{N}(\mathsf{tr}), V_{\mathsf{s}}^{N}(\mathsf{tr})) &\in [\mathcal{T}|_{\{"\mathsf{x"}, "\mathsf{b"}, "\mathsf{mu"}, "\mathsf{s"}\}} \to \mathbb{R}_{\geq 0}] \end{split}$$

This is exactly the factorisation obtained from Theorem 1. However, if we would replace the if-statement with a nonsensical one, (if (b == 1) then (m = 1) else (m = 1)), then the fourth factor is in $[\mathcal{T}|_{\{\text{"x","b","s"}\}} \to \mathbb{R}_{\geq 0}]$ with $V_{\text{m}}^{N}(\mathsf{tr}) = \mathrm{ife}(\mathsf{tr}(\text{"b"}), 1, 1) = 1$. In this case, the static analysis over-approximates the true provenance with the spurious dependency on "b".

3.3.1 Equivalence to Bayesian Networks. If we restrict the way sample statements are used in our PPL, we can establish an equivalence to Bayesian networks. A Bayesian network [Koller and Friedman 2009] is a directed acyclic graph \mathcal{G} over a finite set of nodes X_1, \ldots, X_n which represent random variables, such that the joint distribution factorises according to the graph \mathcal{G} ,

$$P(X_1,\ldots,X_n) = \prod_{i=1}^n P(X_i \mid \text{parents}_{\mathcal{G}}(X_i)).$$

If we assume that all sample statements of a program have a constant unique address α_k , $N_k = \operatorname{Assign}(x_k = \operatorname{sample}(\alpha_k, f^k(E_1^k, \dots, E_{n_k}^k)))$, then we can identify each factor $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ of Theorem 1 with the unique address $\alpha_k \in \operatorname{Strings}$. We construct the Bayesian network \mathcal{G} by

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mapping each address to a random variable X_{α_k} . Since α_k is unique, p_k is the only relevant factor for X_{α_k} and can be interpreted as conditional density function of X_{α_k} . To see this, rewrite

$$p_k(\mathsf{tr}) = \delta_{b_k(\mathsf{tr})} \mathrm{pdf}_{f^k} \left(\mathsf{tr}(\alpha_k); V_{E_1^k}^{N_k}(\mathsf{tr}), \dots, V_{E_{n_k}^k}^{N_k}(\mathsf{tr}) \right) + (1 - \delta_{b_k(\mathsf{tr})}) \mathbf{1}_{\mathsf{null}} \left(\mathsf{tr}(\alpha_k) \right),$$

where the function $\mathbf{1}_{\text{null}}(v)$ equals 1 if v = null else 0. If the node N_k is not executed, then the value of X_{α_k} can be assumed to be null. As pdf_{f_k} is a probability density function and $\mathbf{1}_{\text{null}}$ is the density of a Dirac distribution centered at null, the factor p_k is also a density function for each choice of values in tr at addresses $\alpha \in A_k \setminus \{\alpha_k\}$. Thus, we introduce the edge $\alpha_j \to \alpha_k$ if $\alpha_j \in A_k \setminus \{\alpha_k\}$, such that parents $G(X_{\alpha_k}) = A_k \setminus \{\alpha_k\}$ and

$$P(X_{\alpha_k} | \text{parents}_G(X_{\alpha_k})) = p_k(\{\alpha_j \mapsto X_j : \alpha_j \in A_k\}).$$

In the above construction, it is important that each sample statement has an *unique* address. This guarantees no cyclic dependencies and a well-defined conditional probability distributions.

Below you can see the Bayesian network equivalent to the example program of Fig. 1. Since the value of the program variable m depends on which program branch is taken during execution, the random variable $X_{"x"}$ not only depends on $X_{"mu"}$, but also on $X_{"b"}$.

$$P(X_{"b"}) = \operatorname{pdf}_{\operatorname{Bernoulli}}(X_{"b"}; 0.5)$$

$$P(X_{"s"}) = \operatorname{pdf}_{\operatorname{InverseGamma}}(X_{"s"}; 1, 1)$$

$$P(X_{"mu"}|X_{"b"}) = \delta_{X_{"b"}}\operatorname{pdf}_{\operatorname{Normal}}(X_{"mu"}; 0, 1) + (1 - \delta_{X_{"b"}})\mathbf{1}_{\operatorname{null}}(X_{"mu"})$$

$$P(X_{"x"}|X_{"b"}, X_{"mu"}, X_{"s"}) = \operatorname{pdf}_{\operatorname{Normal}}(X_{"x"}; \operatorname{ife}(\delta_{X_{"b"}}, X_{"mu"}, 1), X_{"s"})$$

3.3.2 Equivalence to Markov Networks. If the sample addresses are not unique or not constant, then the factorisation is in general not a Bayesian network. However, we can show equivalence to the more general Markov networks. A Markov network [Koller and Friedman 2009] is an undirected graph \mathcal{H} over random variables X_i that represents their dependencies. We consider Markov networks where the joint distribution of X_i factorises over a set of cliques $\mathcal{D} \subseteq \text{cliques}(\mathcal{H})$:

$$P(\vec{X}) = \prod_{D \in \mathcal{D}} \phi_D(D).$$

We again identify a random variable X_{α} for each address $\alpha \in \bigcup_{k=1}^{K} A_k$ and construct a Markov network \mathcal{H} by connecting node X_{α} to X_{β} if $\alpha \in A_k \land \beta \in A_k$ for any k. Thus, $D_k = \{X_{\alpha} : \alpha \in A_k\}$ forms a clique and p_S factorises over \mathcal{H} with $\phi_{D_k}(D_k) = p_k(\{\alpha_j \mapsto X_j : \alpha_j \in A_k\})$.

Consider the program with constant but non-unique sample addresses in Listing 6. With nine sample statements we get a Markov network over $\{X_{"F"}, X_{"P0"}, X_{"P1"}, X_{"D0"}, X_{"D1"}\}$. The nine factors correspond to the address sets $\{"F"\}$, $\{"F", "P0"\}$, $\{"F", "P1"\}$, $\{"F", "D0", "P1"\}$, $\{"F", "D1", "D1", "P0"\}$, $2 \times \{"F", "P0"\}$, and $2 \times \{"F", "P1", "D1"\}$. In the consequent branch of the program the distribution in the sample statement with address "P1" depends on "D0", while in the alternative branch, the distribution of "P0" depends on "D1". Thus, there is the dependency cycle "P0" \rightarrow "D0" \rightarrow "P1" \rightarrow "D1" \rightarrow "P0", which means the obtained factorisation is not a Bayesian network representation for the program. Note that the program can also be rewritten with dynamic addresses to eliminate the if statement. In this case, the factorisation still admits cyclic dependencies.

Lastly, while the programs considered up until this point were equivalent to finite Bayesian networks or finite Markov networks, note that the small program in Listing 4 is equivalent to a Markov network with an infinite number of nodes $\{X_{n^*}\} \cup \{X_{n^*} : i \in \mathbb{N}\}$.

Listing 6. Probabilisitic program for the hurricane example of [Milch et al. 2005].

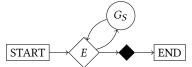
```
first_city_ixs = sample("F", Bernoulli(0.5))
if first_city_ixs == 0 then
    prep_0 = sample("P0", Bernoulli(0.5))
    damage_0 = sample("D0", Bernoulli(prep_0 == 1 ? 0.20 : 0.80))
    prep_1 = sample("P1", Bernoulli(damage_0 == 1 ? 0.75 : 0.50))
    damage_1 = sample("D1", Bernoulli(prep_1 == 1 ? 0.20 : 0.80))
else
    prep_1 = sample("P1", Bernoulli(0.5))
    damage_1 = sample("D1", Bernoulli(prep_1 == 1 ? 0.20 : 0.80))
    prep_0 = sample("P0", Bernoulli(damage_1 == 1 ? 0.75 : 0.50))
    damage_0 = sample("D0", Bernoulli(prep_0 == 1 ? 0.20 : 0.80))
```

4 Static Factorisation for Programs With Loops

In this section, we describe how the argument for proving the factorisation theorem for programs without loops, can be extended to generalise the result to programs with loops.

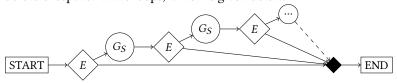
4.1 Unrolled Control-Flow Graph

The translation rules for programs to their CFG can be extended to support while loops as follows. Let G_S be the sub-CFG of statement S. The CFG of (while E do S) is given by



For this CFG, prov(N, x) is still well-defined by Algorithm 1, but the proof of Proposition 2 does not work anymore since it requires a directed acyclic graph.

To establish Proposition 2 for programs with while loops, we introduce a second type of control-flow graph, the *unrolled CFG*, which again is a directed acyclic graph. All translation rules are as before except for while loops, which is given below.



The resulting graph G contains for every $i \in \mathbb{N}$ branch nodes $B_i = \operatorname{Branch}(E)$ and copies of the sub-CFG G_i of statement S. It contains a single join node J for which we have $(B_i, J) \in \operatorname{BranchJoin}(G)$ for all i. Let $N^i_{\operatorname{first}}$ be successor of the start node in G_i and $N^i_{\operatorname{last}}$ the predecessor of the end node in G_i . In the new graph G, the first branch node B_1 is successor of the start node and the end node is the successor of the join node G_i . The branch nodes G_i have two successors G_i and G_i and G_i have the last node of sub-graph G_i , resumes in the next branch node G_i .

This definition implies that the number of nodes of G is countable. Furthermore, since the unrolled CFG consists only of start, end, branch, join, and assign nodes, we can equip it with the same semantics as the standard CFG. As before, the semantics are equivalent.

Proposition 3. For all program S with CFG G and unrolled CFG H, it holds that for all traces tr

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}) = p_H(\mathsf{tr}).$$

PROOF. By structural induction. See Appendix B.3.

As mentioned, Algorithm 1 still works for the unrolled CFG. However, since the unrolled CFG contains an infinite number of nodes, in practice, we run this algorithm on the standard CFG with a finite number of nodes. We will show that the computations on the standard CFG produce an over-approximation of the provenance set in the unrolled CFG. But first, we define the connection between the two graph types.

DEFINITION 7. Let G be the CFG and H the unrolled CFG of program S. For each node $M \in H$, we define $\iota_{\text{cfg}}(M) \in G$ as the node from which M was copied in the construction of H.

The definition of $\iota_{\rm cfg}$ is best understood when considering the example in Fig. 2, where you can see the CFG and unrolled CFG of a program with the mapping $\iota_{\rm cfg}$.

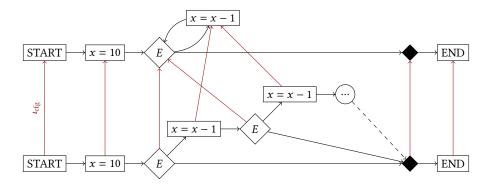


Fig. 2. CFG and unrolled CFG for program (x = 10; while (x < 10) do (x = x - 1)) and their connection via the map ι_{cfg} shown with red edges.

The following lemma states that we can over-approximate the provenance set for variable x at node M in the unrolled CFG by applying Algorithm 1 to the corresponding node $\iota_{\rm cfg}(M)$ in the standard CFG.

LEMMA 3. Let G be the CFG and H the unrolled CFG of program S. For each node $M \in H$ following statements hold:

$$\{\iota_{\mathrm{cfg}}(M') \colon M' \in \mathrm{RD}(M, x)\} \subseteq \mathrm{RD}(\iota_{\mathrm{cfg}}(M), x) \tag{3}$$

$$\{\iota_{\mathrm{cfg}}(M') \colon M' \in \mathrm{BP}(M)\} = \mathrm{BP}(\iota_{\mathrm{cfg}}(M)) \tag{4}$$

$$\operatorname{prov}(M, x) \subseteq \operatorname{prov}(\iota_{\operatorname{cfg}}(M), x)$$
 (5)

PROOF. This follows from the fact that the standard CFG and unrolled CFG of while loops produce paths that are compatible regarding reaching definitions and branch parents. The former produces paths of form START, B, $\overrightarrow{S_1}$, B, $\overrightarrow{S_2}$, ..., ..., $\overrightarrow{S_n}$, B, J, END, while the latter produces paths of form START, B_1 , $\overrightarrow{S_1}$, B_2 , $\overrightarrow{S_2}$, ..., ..., $\overrightarrow{S_n}$, B_{n+1} , J, END, where $\overrightarrow{S_i}$ are paths in the sub-CFGs. The full proof is given in Appendix B.4.

4.2 Static Factorisation Theorem for Programs With Loops

The unrolled CFG has the important property that it is a directed acyclic graph with a potentially infinite number of nodes, but with a single root node. Further, each node $N \neq \text{START}$ still has exactly one predecessor except for join nodes. Therefore, Proposition 2 can be generalised to unrolled CFGs with only a slight modification to the proof.

Proposition 4. For each node M in the unrolled CFG H and variable x, there exists an evaluation function

$$V_x^M \in [\mathcal{T}|_{\text{prov}(M,x)} \to \mathcal{V}],$$

such that for all traces tr with $\sigma_0 = (x_i \mapsto \text{null}, \boldsymbol{p} \mapsto 1)$ and execution sequence

$$(\sigma_0, START) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_i, M_i) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_l, END)$$

we have

$$\sigma_i(x) = V_x^{M_i}(\mathsf{tr}).$$

PROOF. The unrolled CFG H is constructed in a way such that we can prove the statement by well-founded induction on the relation $M_1 \prec M_2 \Leftrightarrow M_1$ is parent of M_2 (i.e. there is an edge from M_1 to M_2 in H) similar to the proof of Proposition 2. The full proof is given in Appendix B.5. \square

As before, Proposition 4 allows us to explicitly construct the factorisation of the program density.

THEOREM 2. Let G be the CFG for a program S. Let N_1, \ldots, N_K be all sample nodes in G. For each sample node $N_k = \text{Assign}(x_k = \text{sample}(E_0^k, f^k(E_1^k, \ldots, E_{n_k}^k)))$, let

$$A_k = \operatorname{addresses}(N_k) \cup \bigcup_{i=0}^{n_k} \operatorname{prov}(N_k, E_i^k) \cup \bigcup_{N' \in \operatorname{BP}(N_k)} \operatorname{prov}(N', E_{N'}).$$

Then, there exist functions $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ such that if $p_S(\mathsf{tr}) \neq \mathsf{undefined}$, then

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}) = \prod_{k=1}^K p_k(\mathsf{tr}).$$

PROOF. We give a proof sketch and refer to Appendix B.6 for the full proof. For each sample node M_j in the *unrolled* CFG H, we construct $\tilde{p}_j \in [\mathcal{T}|_{\tilde{A}_j} \to \mathbb{R}_{\geq 0}]$ as in the proof of Theorem 1. Next, we group the nodes M_j by their corresponding CFG node:

$$p_k = \prod_{j: \ \iota_{\mathrm{cfg}}(M_j) = N_k} \tilde{p}_j$$

From Lemma 3, we have that if $\iota_{\mathrm{cfg}}(M_j) = N_k$, then $\tilde{A}_j = A_k$. Thus, $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$

4.2.1 Equivalence to Markov Networks. As in Section 3.3.2, Theorem 2 implies that a program S with K sample statements is equivalent to a Markov network \mathcal{H} with nodes $\{X_{\alpha} : \alpha \in \bigcup_{k=1}^{K} A_k\}$ even if it contains loops. Again, the sets $D_k = \{X_{\alpha} : \alpha \in A_k\}$ form cliques in \mathcal{H} and p_S factorises over \mathcal{H} with $\phi_{D_k}(D_k) = p_k(\{\alpha_j \mapsto X_j : \alpha_j \in A_k\})$:

$$P(\vec{X}) = \prod_{k=1}^{K} \phi_{D_k}(D_k).$$

5 Case Study: Speeding-up Posterior Inference

While the statically obtained factorisation is provably correct, it is natural to ask whether the quality of the factorisation is good enough to be of practical use. To answer this question, we have implemented a program transformation based on the static factorisation that generates subprograms for each factor (described in Section 5.1). This program transformation operates on probabilistic programs that are implemented in our research PPL which models the formal language introduced in Section 2. In Section 5.2, we demonstrate that if the change to the current program trace is small, then we can leverage the sub-programs to compute the density of the updated trace faster. The employed optimisation trick is well-known but typically restricted to programs with

a finite number of random variables and PPLs that require the user to explicitly construct the graphical structure. In contrast, our transformation operates statically on the source code and results in improved computation time even if a program contains while loops. We evaluate our approach on 15 probabilistic programs, the majority of which make use of while loops to declare an unbounded number of random variables. The implementation and the benchmark programs can be found in the openly-available replication package [Anonymous 2024].

5.1 Generating Sub-programs from the Static Factorisation

We generate a sub-program for each sample statement (and thus for each factor) by deriving a program slicing method from the static provenance analysis. For sample node N_k , we want to generate a sub-program that

- continues execution directly from N_k ,
- stops execution once all sample nodes that depend on N_k have been reached,
- accumulates the density for all dependencies and for N_k itself.

The first requirement is achieved by storing not only the value of a random variable in the trace when executing a sample statement, but also storing the state of the program σ — the values of all program variables. For the other two requirements, we need to collect all sample nodes that depend on N_k . Recall that a sample node N_j depends on N_k if the factor $p_j \in [\mathcal{T}|_{A_j} \to \mathbb{R}_{\geq 0}]$ depends on addresses $\alpha \in \operatorname{addresses}(N_k) \subseteq A_j$. As explained in Section 3.2, these dependencies can be found by modifying Algorithm 1 to collect the sample nodes instead of their addresses.

Original program

Sub-program for factor

```
# factor for "B"
# Program 1
A = sample("A", Normal(0.0, 1.0))
                                            A = \sigma("A")
B = sample("B", Normal(A, 1.0))
                                            B = score("B", Normal(A, 1.0))
C = sample("C", Normal(A, 1.0))
                                            C = read_trace("C")
D = sample("D", Normal(B+C, 1.0))
                                            D = score("D", Normal(B+C, 1.0))
E = sample("E", Normal(A, 1.0))
                                            # factor for "b"
# Program 2
i = 0; b = 1;
                                            i = \sigma("i"); b = \sigma("b");
                                            b = score("b"+str(i), Bernoulli(0.5))
while b do
  b = sample("b"+str(i), Bernoulli(0.5))
                                            i = i + 1
  i = i + 1
                                            while b do
                                              b = score("b"+str(i), Bernoulli(0.5))
                                              i = i + 1
                                            # factor for "z"
# Program 3
i = 0;
                                            i = \sigma("i");
                                            z = score("z"+str(i), Bernoulli(0.5))
while i < N do
  z = sample("z"+str(i), Bernoulli(0.5))
                                            m = (z == 1) ? -2.0 : 2.0
  m = (z == 1) ? -2.0 : 2.0
                                            x = score("x"+str(i), Normal(m, 1.0))
  x = sample("x"+str(i), Normal(m,1.0))
  i = i + 1
```

Fig. 3. Example sub-programs generated from the static factorisation alongside the original program.

To generate the sub-programs, we perform a type of reachability analysis to find all CFG nodes that lie on a path from N_k to N_j and slice the program according to this set of nodes. We will illustrate this process with three examples and refer to our implementation for further details [Anonymous 2024]. Consider program 1 in Figure 3. To generate a sub-program for the factor of sample statement "B", we omit the sample statements "A" and "E" as they do not lie on a path from "B" to "D" — the only sample statement that depends on "B". However, we still need to read the value of program variable A from the cached program state σ and we read the value for random variable "C" from the trace. Continuing executing directly from a sample statement by caching the program state means that we can continue a loop from a specific iteration as exemplified by program 2 in Figure 3. This is particularly powerful, if the only dependency of a sample statement belongs to the same loop iteration. In this case, an entire while loop can be replaced by a single iteration which is shown for program 3 in Figure 3.

5.2 Speeding-up Density Computation for Single-Site Trace Updates

Many posterior inference algorithms can be improved by exploiting the factorisation of the model density. For instance, in sequential Monte Carlo methods probabilistic programs are executed one factor at a time, after which resampling is performed to discard low quality samples [Meent et al. 2015]. Furthermore, variational inference methods may use the factorisation to reduce the variance in gradient estimation [Ranganath et al. 2014]. However, in this section we focus on the single-site Metropolis Hastings inference (MH) algorithm [Wingate et al. 2011]. The single-site MH algorithm works by selecting a single address α in each iteration at random and proposing a new value v. Even though we select a single address for the update, the proposal may have to produce additional values for sample statements that were not executed for trace tr, but due to stochastic branching or dynamic addresses are now executed for the new trace tr'. For instance in Figure 3 program 2, if we change the value of the last random variable "b_i" from 0 to 1, then we may have to sample values for multiple "b_j" where j > i until we get $tr'("b_j") = 0$.

The proposed trace tr' is then accepted with probability $A = \min\left(1, \frac{p(\operatorname{tr}') \times Q_{\alpha}(\operatorname{tr}')}{p(\operatorname{tr}) \times Q_{\alpha}(\operatorname{tr}')}\right)$, where Q_{α} captures the probability of the proposal process (sampling a value for α and all additional values as explained above). If the address comes from sample node N_k , then we can optimise the runtime of this algorithm by noting that $\log p(\operatorname{tr}') - \log p(\operatorname{tr}) = \log p_k(\operatorname{tr}') - \log p_k(\operatorname{tr})$. Thus, by being able to compute the factors $p_k(\operatorname{tr}')$ and $p_k(\operatorname{tr})$ separately, we can more efficiently compute the ratio of densities $p(\operatorname{tr}')/p(\operatorname{tr})$.

As mentioned earlier, this optimisation trick is well-known and easily implemented for PPLs that require the user to explicitly construct the probabilistic model graphically with one node per random variable. By generating a sub-program to compute p_k we enable this optimisation for programs with an unbounded number of random variables and dynamic dependencies.

5.3 Experiment Results

To evaluate the speed-up made possible by our static factorisation, we gathered a benchmark set of 15 probabilistic programs. We specifically sampled probabilistic programs that make use of while loops to define an unbounded number of random variables. For comparison, we also included models that define only a finite number of random variables with fixed dependency structure For these programs, we implemented a factorisation that is representative of PPLs that explicitly construct a probabilistic graphical model. For each model, we generated N traces from the prior and randomly selected an address α for each trace tr. Then, we perform a resample update for this address to get a new trace tr'. In this update, we sample a new value for α from the prior and also sample values for any new sample statements that are encountered due to stochastic branching, as

Table 1. Average runtime of computing p(tr') for traces tr' that are generated by resampling traces tr at a single site in microseconds (see Section 5.2). The column "None" reports the runtime of computing p(tr') from scratch. The column "Static Analysis" reports the runtime of updating the old density by computing $\log p_k(tr') - \log p_k(tr)$ with the generated sub-programs (see Section 5.1). The column "Fixed-Finite" reports the runtime of updating the density with a hand-written factorisation that is representative of PPLs that explicitly construct a probabilistic graphical model. The time in relation to computing from scratch is given in parenthesis. Lastly, the number of random variables defined in each model can be found in column "# RVs".

		Factorisation		
Model	# RVs	None	Static Analysis	Fixed-Finite
Aircraft	∞	8.303	4.217 (0.51)	-
Captcha	∞	948.582	335.915 (0.35)	-
Dirichlet process	∞	70.388	35.223 (0.50)	-
Geometric	∞	0.665	0.531 (0.80)	-
GMM (fixed number of clusters)	209	45.703	3.589 (0.08)	3.061 (0.07)
GMM (variable number of clusters)	∞	40.798	5.008 (0.12)	-
HMM (fixed sequence length)	21	4.777	4.714 (0.99)	1.837 (0.38)
HMM (variable sequence length)	∞	6.600	6.110 (0.93)	-
Hurricane	5	0.893	0.677 (0.76)	-
LDA (fixed number of topics)	551	165.376	25.385 (0.15)	12.503 (0.08)
LDA (variable number of topics)	∞	155.461	15.390 (0.10)	-
Linear regression	102	3.634	6.949 (1.91)	6.811 (1.87)
Marsaglia	∞	0.495	1.199 (2.42)	-
Pedestrian	∞	4.532	3.986 (0.88)	-
Urn	∞	8.945	5.961 (0.67)	-

explained in Section 5.2. We measure the runtime of computing $\log p_k(\mathsf{tr}') - \log p_k(\mathsf{tr})$ with the generated sub-programs and compare it against computing $p(\mathsf{tr}')$ from scratch. In Table 1, you can see the results of the experiment run on a M2 Pro CPU. We averaged the measured times over all runs N. Note that we selected N for each model such that the benchmark time is approximately 1-2 seconds. N is greater than 10000 except for the Captcha model where N=1000.

The first observation that we want to highlight is that computing $\log p_k(\mathsf{tr}') - \log p_k(\mathsf{tr})$ can lead to longer runtime if there is no structure to exploit in the model, $p_k \approx p$. Consider the linear regression model in which the 100 observations depend on the slope and intercept variables. When changing the value of one of these variables, we have to subtract the log-probability of observing the 100 data points according to the old value, $\log p_k(\mathsf{tr}')$, and add the log-probability of observing the data points according to the new value $\log p_k(\mathsf{tr}')$. This effectively doubles the runtime, because when computing $p(\mathsf{tr}')$ from scratch, we only have to add up the log-probability according to the new value. The same is true for the Marsaglia model which implements a rejection sampling method for the Normal distribution such that all variables depend on each other.

The factorisation approach shines when there is a lot of structure in the models like for the Aircraft, Captcha, and Dirichlet Process models, where the runtime is more than halved. It is particularly powerful, when there are latent variables that only affect independent data variables. This is the case for the Gaussian mixture model (GMM) where there is one cluster allocation variable per data point and for the Latent Dirichlet Allocation model (LDA) where there is similar dependency structure. For these models we observe 6-12x speed-ups. These kind of speed-ups are also achievable with existing methods provided that we only have a finite number of random variables. With our proposed method, we can transfer these speed-ups to the case where the GMM

has an unknown number of clusters and to the case where the LDA has an unknown number of topics. In both of these cases the models instantiate an unbounded number of random variables. Note that the handwritten "fixed-finite" factorisation of the LDA model is twice as fast as the "static analysis" factorisation, because it was optimised to do less read-from-trace operations rather than leveraging the dependency structure more effectively. Also note that there are model specific improvements for the factorisation of GMM and LDA to only update the log-probability of data points that belong to a certain cluster / allocation which further reduces the runtime to 2.41 and 2.46 microseconds, respectively. However, these improvements are beyond the scope of automated static methods.

The only model where our approach fails to effectively leverage the full dependency structure is the Hidden Markov Model (HMM). If the sequence length is fixed then the current state only affects the next state. Our static analysis over-approximates this structure and outputs that the current state affects all future states. This is the reason why the "fixed-finite" factorisation outperforms the static one. Now, if we modify the program by unrolling the while loop, i.e. repeating its body ten times, our factorisation approach results in an improved runtime of 2.261 μ s, because in this case the dependency structure is not over-approximated anymore. This suggests that our analysis may be improved by detecting loops for which the number of iterations is fixed and statically identifiable. Note that if the sequence length is variable and a transition to a terminal state is possible at any point, indeed the current state affects all future states. In this case, our static analysis is exact, but no improvements in runtime are measurable.

Lastly, we want to note that the Hurricane model is the only model with a finite number of random variables but a dynamic dependency structure (see Section 3.3.2). Thus, for this model existing approaches are also not applicable, but our method can reduce the runtime by over 20%.

6 Related Work

6.1 Semantics of Probabilistic Programming Languages

One of the first works to formally define the semantics of a probabilistic programming language was Kozen [1979]. Derivatives of this semantics can be found throughout literature, for example. see Hur et al. [2015], Dahlqvist and Kozen [2019], or Barthe et al. [2020]. We give a brief (and incomplete) overview of existing semantic models for PPLs.

In recent years, a lambda calculus for probabilistic programming was presented by Borgström et al. [2016]. This work and a domain theoretic approach for probabilistic programming [Vákár et al. 2019] had an influence on the development of the statistical PCF [Mak et al. 2021] (programming computable functions), which was used to prove differentiability of terminating probabilistic programs. Probabilistic programming was also examined from a categorical perspective [Heunen et al. 2017] and the semantics of higher order probabilistic programs is also researched [Dahlqvist and Kozen 2019; Staton et al. 2016].

As already mentioned, the semantics presented in this work generalise those of Gorinova et al. [2019] which formalises the core language constructs of Stan by interpreting a program as a function over a fixed set of finite variables. In our semantics, we interpret programs as functions over traces which are mappings from addresses to values similar to dictionaries. Another dictionary based approach to probabilistic programming can be found in Cusumano-Towner et al. [2020] and denotational semantics based on trace types are introduced in Lew et al. [2019].

To the best of our knowledge there are only two existing semantic models that support user-defined addresses [Lee et al. 2019; Lew et al. 2023]. These approaches are denotational and sampling-based in contrast to our density-based operational semantics.

6.2 Graphical Representations and Dependency Analysis

The advantages of representing a probabilistic model graphically are well-understood [Koller and Friedman 2009]. For this reason, some PPLs like PyMC [Salvatier et al. 2016], Factorie [McCallum et al. 2009], or Infer.NET [Minka 2012] require the user to explicitly define their model as a graph. This is not possible if we want to support while loops in our programs. Modifying the Metropolis Hastings algorithm to exploit the structure of the model is also common practice, e.g. see Nori et al. [2014]; van de Meent et al. [2018]; Wu et al. [2016].

The connection of simple probabilistic programs to Bayesian networks is known [van de Meent et al. 2018]. Borgström et al. [2011] consider a small imperative language and give semantics in terms of factor graphs – a concept related to Bayesian networks. Sampson et al. [2014] compile probabilistic programs to Bayesian networks by treating loops as black-box functions in order to verify probabilistic assertion statements. Previous work extended Bayesian networks to so-called contingent Bayesian networks in order to support models with an infinite number of random variables or cyclic dependencies [Milch et al. 2005], but they cannot be straightforwardly applied to PPLs with user-labelled sample statements and while loops.

The probabilistic dependency structure of programs has also been exploited in previous work. Baah et al. [2008] use it for fault diagnosis, Hur et al. [2014] designed an algorithm for slicing probabilistic programs, and Bernstein et al. [2020] extract the factor graph of programs to automate model checking for models implemented in Stan. Castellan and Paquet [2019] enable incremental computation in the Metropolis Hastings algorithm by graphically modelling the data-dependencies between sample statements of first-order probabilistic programs.

In contrast to the methods above, in this work we statically determine a density factorisation for programs even if they contain while loops. This factorisation implies an equivalence to Markov networks and enables new opportunities for optimising inference. These opportunities are new, because in universal probabilistic programming the common approach is to track dependencies dynamically for optimised inference [Chen et al. 2014; Wu et al. 2016; Yang et al. 2014].

Lastly, it is worth mentioning that static analysis for probabilistic programming has also been explored for applications different from optimising inference. For instance, Lee et al. [2019] developed a static analysis to verify so-called guide programs for stochastic variational inference in Pyro. Another example is the extension of the Stan compiler with a pedantic mode that statically catches common pitfalls in probabilistic programming [Bernstein 2023]. For a a more complete survey on static analysis for probabilistic programming see Bernstein [2019].

7 Conclusion

In this work, we introduced address-based operational semantics for a formal probabilistic programming language to address the open question to what extent a probabilistic program with user-labelled sample statements can be represented graphically. We presented a sound static analysis that approximates the dependency structure of random variables in the program and used this structure to factorise the implicitly defined program density. The factorisation allowed us to show equivalence of probabilistic programs to either Bayesian or Markov networks. Lastly, we demonstrated that we can speed-up a single-site Metropolis Hastings algorithm by generating a sub-program for each factor based on the dependency structure and leveraging them to optimise program density computation. This illustrates that our static analysis has the potential to be used to optimise inference algorithms in a way that is typically reserved for PPLs which require the explicit construction of a graphical model.

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A Control-Flow Graph Construction

DEFINITION 8. A CFG is a tuple $(N_{\text{start}}, N_{\text{end}}, \mathcal{N}, \mathcal{E})$ comprised of start and end node, a set of branch, join, and assign nodes, \mathcal{N} , and a set of edges, $\mathcal{E} \subseteq \mathcal{N}^* \times \mathcal{N}^*$, where $\mathcal{N}^* = \mathcal{N} \cup \{N_{\text{start}}, N_{\text{end}}\}$.

ullet The CFG of a skip-statement, $S={\sf skip},$ consists of only start and end node:

$$START \longrightarrow END$$

$$G = (N_{start}, N_{end}, \emptyset, \{(N_{start}, N_{end})\})$$

• The CFG of an assignment, x = E, or sample statement $x = \text{sample}(E_0, f(E_1, ..., E_n))$, is a sequence of start, assign, and end node:

$$G = (N_{\text{start}}, N_{\text{end}}, \{A\}, \{(N_{\text{start}}, A), (A, N_{\text{end}})\}),$$

where A = Assign(x = ...) is the corresponding assign node.

• The CFG for a sequence of statements, $S = S_1$; S_2 , is recursively defined by "stitching together" the CFGs $G_1 = (N_{\text{start}}^1, N_{\text{end}}^1, \mathcal{N}^1, \mathcal{E}^1)$ and $G_2 = (N_{\text{start}}^2, N_{\text{end}}^2, \mathcal{N}^2, \mathcal{E}^2)$ of S_1 and S_2 :

$$\boxed{\texttt{START}} \longrightarrow \boxed{G_1} \longrightarrow \boxed{G_2} \longrightarrow \boxed{\texttt{END}}$$

$$G = (N_{\text{start}}^1, N_{\text{end}}^2, N^1 \cup N^2, \mathcal{E}^1 \setminus \{(N_e^1, N_{\text{end}}^1)\} \cup \mathcal{E}^2 \setminus \{(N_{\text{start}}^2, N_s^2)\} \cup \{(N_e^1, N_s^2)\}),$$

where N_e^1 is the predecessor node of N_{end}^1 in G_1 , $(N_e^1, N_{\text{end}}^1) \in \mathcal{E}^1$, and N_s^2 is the successor node of N_{start}^2 in G_2 , $(N_{\text{start}}^2, N_s^2) \in \mathcal{E}_2$.

• The CFG of an if statement, $S = (if E then S_1 else S_2)$, is also defined in terms of the CFGs of S_1 and S_2 together with a branch node B = Branch(E) and a join node J:

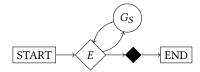
$$G = (N_{\text{start}}, N_{\text{end}}, N^{1} \cup N^{2} \cup \{B, J\},$$

$$\mathcal{E}^{1} \setminus \{(N_{\text{start}}^{1}, N_{s}^{1}), (N_{e}^{1}, N_{\text{end}}^{1})\} \cup \mathcal{E}^{2} \setminus \{(N_{\text{start}}^{2}, N_{s}^{2}), (N_{e}^{2}, N_{\text{end}}^{2})\} \cup$$

$$\{(N_{\text{start}}, B), (B, N_{s}^{1}), (B, N_{s}^{2}), (N_{e}^{1}, J), (N_{e}^{2}, J), (J, N_{\text{end}})\},$$

The nodes N_s^1 , N_e^1 , N_s^2 , N_e^2 are defined as in the last case. This construction assumes that $\mathcal{E}^1 \neq \emptyset$ and $\mathcal{E}^2 \neq \emptyset$. Otherwise, the branch would be a skip statement and we would connect B with J directly. The two predecessors of J are denoted with predcon(J) and predalt(J) depending on the path.

• The CFG of (while E do S) is given in terms of the sub-cfg G_S of statement S.

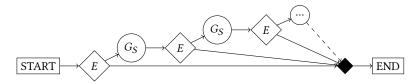


$$G = (N_{\text{start}}, N_{\text{end}}, \mathcal{N}^{S} \cup \{B, J\},$$

$$\mathcal{E}^{S} \setminus \{(N_{\text{start}}^{S}, N_{s}^{S}), (N_{e}^{S}, N_{\text{end}}^{S})\} \cup \{(N_{\text{start}}, B), (B, N_{s}^{S}), (B, J), (N_{e}^{S}, B), (J, N_{\text{end}})\},$$
where $G_{S} = (N_{\text{start}}^{S}, N_{\text{end}}^{S}, \mathcal{N}^{S}, \mathcal{E}^{S}).$

Again, if S = skip, then we would have the edge (B, B) instead of (B, N_s^S) and (N_e^S, B) .

• The unrolled CFG of (while *E* do *S*) is defined as follows:



$$G = (N_{\text{start}}, N_{\text{end}}, \\ \bigcup_{i \in \mathbb{N}} \mathcal{N}^{S_i} \cup \{B_i \colon i \in \mathbb{N}\} \cup \{J\}, \\ \bigcup_{i \in \mathbb{N}} \mathcal{E}^{S_i} \setminus \{(N_{\text{start}}^{S_i}, N_s^{S_i}), (N_e^{S_i}, N_{\text{end}}^{S_i})\} \cup \{(N_{\text{start}}, B_1), (B, J), (J, N_{\text{end}})\} \cup \{(B_i, N_s^{S_i}) \colon i \in \mathbb{N}\} \cup \{(N_e^{S_i}, B_{i+1}) \colon i \in \mathbb{N}\}),$$

where $G_{S_i} = (N_{\text{start}}^{S_i}, N_{\text{end}}^{S_i}, \mathcal{N}^{S_i}, \mathcal{E}^{S_i})$ is the *i*-th copy of sub-graph G_S . $N_S^{S_i}$ and $N_e^{S_i}$ are defined as in the cases before.

If S = skip, then we would have the edges (B_i, B_{i+1}) instead of the edges $(B_i, N_s^{S_i})$ and $(N_e^{S_i}, B_{i+1})$.

B Proofs

B.1 Proof of Proposition 1

STATEMENT. For all programs S with control-flow-graph G, it holds that for all traces tr

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}).$$

PROOF. With structural induction we prove the more general equivalence:

$$(\sigma, S) \Downarrow^{\mathsf{tr}} \sigma' \iff (\sigma, \mathsf{START}) \xrightarrow{\mathsf{tr}} \dots \xrightarrow{\mathsf{tr}} (\sigma', \mathsf{END}).$$

We write $\xrightarrow{\text{tr}}$ to indicate that the predecessor-successor relationship follow the CFG of program S.

For programs consisting of a single skip, assignment, or sample statement, the equality can be seen by directly comparing the operational semantic rules to the graph semantics.

For sequence $S = (S_1; S_2)$, by induction assumption we have

$$(\sigma, S_1) \Downarrow^{\mathsf{tr}} \sigma' \iff (\sigma, N_{\mathsf{start}}^1) \xrightarrow[S_1]{\mathsf{tr}} \dots \xrightarrow[S_1]{\mathsf{tr}} (\sigma'_1, N^1) \xrightarrow[S_2]{\mathsf{tr}} (\sigma', N_{\mathsf{end}}^1)$$

$$(\sigma', S_2) \Downarrow^{\mathsf{tr}} \sigma'' \iff (\sigma', N_{\mathsf{start}}^2) \xrightarrow[S_2]{\mathsf{tr}} (\sigma', N^2) \xrightarrow[S_2]{\mathsf{tr}} \dots \xrightarrow[S_2]{\mathsf{tr}} (\sigma'', N_{\mathsf{end}}^2)$$

By definition of the CFG of *S*, we see that

$$(\sigma, S) \Downarrow^{\mathsf{tr}} \sigma'' \iff (\sigma, N^1_{\mathsf{start}}) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma'_1, N^1) \xrightarrow{\mathsf{tr}} (\sigma', N^2) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma'', N^2_{\mathsf{end}})$$

In a similar way, we proof the result for if statements $S = (\text{if } E \text{ then } S_1 \text{ else } S_2)$ by considering the two cases $\sigma(E) = \text{true}$ and $\sigma(E) = \text{false}$.

$$(\sigma, S) \downarrow^{\mathsf{tr}} \sigma' \iff (\sigma, N_{\mathsf{start}}) \xrightarrow{\mathsf{tr}} (\sigma, \mathsf{Branch}(E)) \xrightarrow{\mathsf{tr}} \dots \xrightarrow{\mathsf{tr}} (\sigma', N_{\mathsf{join}}) \xrightarrow{\mathsf{tr}} (\sigma', N_{\mathsf{end}})$$

Depending on the case, we replace the dots with the path of S_i without the start and end nodes. Note that in our notation in both cases $(\sigma, S_i) \downarrow^{\mathsf{tr}} \sigma'$.

B.2 Proof of Proposition 2

To prove soundness of Algorithm 1, we list properties of the provenance set that will become useful in subsequent proofs.

LEMMA 4. Let N be a node and x a variable. prov has following properties:

• For all nodes N' it holds that

$$RD(N,x) = RD(N',x) \implies prov(N,x) = prov(N',x).$$
 (6)

• If $N' \in RD(N, x)$ is an assignment node, N' = Assign(x = E), then

$$\operatorname{prov}(N', E) = \bigcup_{u \in \operatorname{vars}(E)} \operatorname{prov}(N', u) \subseteq \operatorname{prov}(N, u). \tag{7}$$

• if $N' \in RD(N, x)$ is a sample node, $N' = Assign(x = sample(E_0, ...))$, then

$$\operatorname{prov}(N', E_0) \subseteq \operatorname{prov}(N, x)$$
 and $\operatorname{addresses}(N') \subseteq \operatorname{prov}(N, x)$. (8)

• For all $N' \in RD(N, x)$ it holds that for all branch parents $N_{bp} = Branch(E_{N_{bp}}) \in BP(N')$ we have $prov(N_{bp}, E_{N_{bp}}) \subseteq prov(N, x)$ and

$$\bigcup_{N_{bp} \in \mathrm{BP}(N')} \mathrm{prov}(N_{bp}, E_{N_{bp}}) = \bigcup_{N_{bp} \in \mathrm{BP}(N')} \bigcup_{y \in \mathrm{vars}(E_{N_{bp}})} \mathrm{prov}(N_{bp}, y) \subseteq \mathrm{prov}(N, x). \tag{9}$$

PROOF. If (N', y) is pushed into the queue, then $prov(N', y) \subseteq prov(N, x)$ as the algorithm for computing prov(N', y) starts with only (N', y) in the queue. With this fact the properties follow directly from the definition of the algorithm.

STATEMENT. For each node N in the CFG G and variable x, there exists an evaluation function

$$V_x^N \in [\mathcal{T}|_{\text{prov}(N,x)} \to \mathcal{V}],$$

such that for all traces tr with $\sigma_0 = (x_i \mapsto \text{null}, \boldsymbol{p} \mapsto 1)$ and execution sequence

$$(\sigma_0, START) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_i, N_i) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_l, END)$$

we have

$$\sigma_i(x) = V_x^{N_i}(\mathsf{tr}).$$

Proof.

Step 1. Defining V_x^N and proving $V_x^N \in [\mathcal{T}|_{\text{prov}(N,x)} \to \mathcal{V}]$. We begin by defining V_x^N inductively by noting that every node $N \neq \text{START}$ has exactly one predecessor except for join nodes which have two predecessors. Further, the CFG G is a finite directed acyclic graph with a single root node which makes mathematical induction possible.

Base case. For $((x_i \mapsto \text{null}, \mathbf{p} \mapsto 1), \text{START})$, let $V_{\mathbf{p}}^{\text{START}}(\text{tr}) = 1$ and $V_{x}^{\text{START}}(\text{tr}) = \text{null}$ for all variables x. We have $V_x^{\text{START}} \in [\mathcal{T}|_{\emptyset} \to \mathcal{V}]$.

Induction step.

Case 1. Let N be a node with single predecessor N' (N is not a join node). We define V_x^N based on the node type of N', for which we make the induction assumption that the evaluation function $V_x^{N'} \in [\mathcal{T}|_{\text{prov}(N',x)} \to \mathcal{V}]$ exists for all variables.

• Case 1.1. N' is an assignment node, N' = Assign(z = E). Define

$$V_y^N(\mathsf{tr}) = \begin{cases} V_E^{N'}(\mathsf{tr}) & \text{if } y = z \\ V_y^{N'}(\mathsf{tr}) & \text{otherwise.} \end{cases}$$

By assumption $V_y^{N'} \in [\mathcal{T}|_{\text{prov}(N',y)} \to \mathcal{V}]$ for all y and thus, $V_E^{N'} \in [\mathcal{T}|_{\text{prov}(N',E)} \to \mathcal{V}]$.

For $x \neq z$, RD(N, x) = RD(N', x) and by Eq. (6) we have prov(N, x) = prov(N', x), implying that $V_x^N \in [\mathcal{T}|_{\text{prov}(N,x)} \to \mathcal{V}].$

For x = z, RD $(N, x) = \{N'\}$ since N' is the single predecessor of N and assigns x. By Eq. (7) $\operatorname{prov}(N', E) \subseteq \operatorname{prov}(N, x)$, and by Eq. (2)

$$V_x^N = V_E^{N'} \in [\mathcal{T}|_{\operatorname{prov}(N',E)} \to \mathcal{V}] \subseteq [\mathcal{T}|_{\operatorname{prov}(N,x)} \to \mathcal{V}].$$

• Case 1.2. N' is a sample node, $N' = Assign(z = sample(E_0, f(E_1, ..., E_n)))$. Define

$$V_y^N(\mathsf{tr}) = \begin{cases} \mathsf{tr}(V_{E_0}^{N'}(\mathsf{tr})) & \text{if } y = z \\ V_y^{N'}(\mathsf{tr}) & \text{otherwise.} \end{cases}$$

As before, for $x \neq z, V_x^N \in [\mathcal{T}|_{\text{prov}(N,x)} \to \mathcal{V}]$. For x = z, by definition $V_{E_0}^{N'}(\text{tr}) \in \text{addresses}(N')$. Since $N' \in \text{RD}(N,x) = \{N'\}$ it follows from Eq. (8) that $\text{addresses}(N') \subseteq \text{prov}(N,x)$ and $\text{prov}(N',E_0) \subseteq \text{prov}(N,x)$. By Eq. (2)

$$V_x^N \in [\mathcal{T}|_{\operatorname{addresses}(N') \cup \operatorname{prov}(N', E_0)} \to \mathcal{V}] \subseteq [\mathcal{T}|_{\operatorname{prov}(N, x)} \to \mathcal{V}].$$

• Case 1.3. N' is a branch or join node. Define $V_u^N = V_u^{N'}$ for all variables y.

Case 2. Let *J* be a join node with two predecessor nodes, $N'_1 = \operatorname{predcons}(J)$, $N'_2 = \operatorname{predalt}(J)$. Let B = Branch(E) be the corresponding branching node, $(B, J) \in BranchJoin(G)$.

For variable *x*, there are two cases:

(1) There exists a reaching definition $N' \in RD(J, x)$ on a path (B, ..., N', ..., J). Define V_x^1 and V_x^2 depending on the node type of N_1' and N_2' as in step 1. The functions V_x^i compute the value of X after executing N_i' . As before one can see that $V_x^i \in [\mathcal{T}|_{\text{prov}(J,x)} \to \mathcal{V}]$. Let

$$V_x^J(\mathsf{tr}) \coloneqq \mathrm{ife}(V_E^B(\mathsf{tr}), V_x^1(\mathsf{tr}), V_x^2(\mathsf{tr})) = \begin{cases} V_x^1(\mathsf{tr}) & \text{if } V_E^B(\mathsf{tr}) = \mathsf{true}, \\ V_x^2(\mathsf{tr}) & \text{otherwise}. \end{cases}$$

Since $B \in \mathrm{BP}(N')$, by Eq. (9) $\mathrm{prov}(B, E) \subseteq \mathrm{prov}(J, x)$ and thus $V_x^J \in [\mathcal{T}|_{\mathrm{prov}(J, x)} \to \mathcal{V}]$.

(2) All reaching definitions of x (if there are any) are predecessors of B. Then, RD(J, x) = RD(B, x) and by Eq. (6) PO(J, x) = PO(B, x). Define $V_x^J := V_x^B$.

Step 2: Proving that $\sigma_i(x) = V_x^{N_i}(\mathsf{tr}).$

Having defined the evaluation functions, we now have to prove that they indeed compute the correct values. For trace tr let the corresponding execution sequence be

$$(\sigma_0, START) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_i, N_i) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_l, END).$$

We will prove that $\sigma_i(x) = V_x^{N_i}(\mathsf{tr})$ by induction.

Base case. The base case immediately follows from the definition $\sigma_0(x) = V_x^{\text{START}}(\text{tr})$.

Induction step. For transition $(\sigma_i, N_i) \xrightarrow{\text{tr}} (\sigma_{i+1}, N_{i+1})$ the assumption is that $\sigma_j(x) = V_x^{N_j}(\text{tr})$ holds for all variables x and $j \leq i$.

If N_{i+1} is not a join node, then $\sigma_{i+1}(x) = V_x^{N_{i+1}}(tr)$ follows directly from the CFG semantics and definition of $V_x^{N_{i+1}}$ in case 1 of step 1.

Lastly, we consider the case when N_{i+1} is a join node with branching node N_j = Branch(E), $(N_j, N_{i+1}) \in \text{BranchJoin}(G)$, for some $j \le i$. Let $N'_1 = \text{predcons}(N_{i+1})$, $N'_2 = \text{predalt}(N_{i+1})$.

By the semantics of if statements, $V_E^B(\mathsf{tr}) = \mathsf{true}$ iff $N_i = N_1'$ and $V_E^B(\mathsf{tr}) = \mathsf{false}$ iff $N_i = N_2'$. If there is a node N' between N_j and N_{i+1} in the execution sequence that assigns $x, N' \in \mathrm{RD}(N_{i+1}, x)$, then by definitions of V_x^1, V_x^2 , and $V_x^{N_{i+1}}$ we have

$$\sigma_{i+1}(x) = \mathrm{ife}(V_E^B(\mathsf{tr}), V_x^1(\mathsf{tr}), V_x^2(\mathsf{tr})) = V_x^{N_{i+1}}(\mathsf{tr}).$$

If there is no such N', then

$$\sigma_{i+1}(x)=\sigma_j(x)=V_x^{N_j}(\mathsf{tr})=V_x^{N_{i+1}}(\mathsf{tr}).$$

B.3 Proof of Proposition 3

STATEMENT. For all program S with CFG G and unrolled CFG H, it holds that for all traces tr

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}) = p_H(\mathsf{tr}).$$

PROOF. We continue the proof of Proposition 1 for a while statement. Note the well-known equivalence for while statements in terms of the operational semantics:

$$(\sigma, \mathsf{while}\ E\ \mathsf{do}\ S)\ \downarrow^\mathsf{tr}\ \sigma'\ \Longleftrightarrow\ (\sigma, \mathsf{if}\ E\ \mathsf{then}\ (S;\ \mathsf{while}\ E\ \mathsf{do}\ S)\ \mathsf{else}\ \mathsf{skip})\ \downarrow^\mathsf{tr}\ \sigma'$$

Thus, if the program terminates for tr, it is equivalent to $S; \ldots; S$ (S repeated n times), where $\sigma_0 = \sigma$, $(\sigma_i, S) \downarrow^{\text{tr}} \sigma_{i+1}, \sigma_n = \sigma'$, such that $\sigma_i(E) = \text{true for } i < n, \sigma_n(E) = \text{false}$.

In this case, it can be shown that the (unrolled) CFG of S; . . . ; S is also equivalent to the (unrolled) CFG of the while statement.

If the program does not terminate, then $p_S(tr) = p_G(tr) = p_H(tr) = undefined$.

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B.4 Proof of Lemma 3

Statement. Let G be the CFG and H the unrolled CFG of program S. For each node $M \in H$ following equation holds.

$$\{\iota_{\mathrm{cfg}}(M') \colon M' \in \mathrm{RD}(M, x)\} \subseteq \mathrm{RD}(\iota_{\mathrm{cfg}}(M), x) \tag{10}$$

PROOF. We will prove following Lemma by structural induction:

LEMMA. For every path $\overrightarrow{M} = (M', ..., M)$ in the unrolled CFG H, there is a path in the CFG G, $\overrightarrow{N} = (N', ..., N)$, such that

$$\iota_{\mathrm{cfg}}(M') = N', \quad \iota_{\mathrm{cfg}}(M) = N,$$

$$\operatorname{AssignNodes}(\overrightarrow{N},x) = \left\{ \iota_{\operatorname{cfg}}(M_i) \colon M_i \in \operatorname{AssignNodes}(\overrightarrow{M},x) \right\} = \operatorname{AssignNodes}_{\operatorname{cfg}}(\overrightarrow{M},x),$$

for all variables x, where $AssignNodes(\overrightarrow{N},x)$ is the set of all CFG nodes that assign x in path \overrightarrow{N} and $AssignNodes_{cfg}(\overrightarrow{M},x)$ is the set of all assign nodes in path \overrightarrow{M} mapped to G with ι_{cfg} .

With this lemma we can prove the statement:

Let $M' \in RD(M, x)$. Thus, there exists a path M = (M', ..., M), such that M' is the only assign node for x before M. By the lemma, there is a path in the CFG $\overrightarrow{N} = (N', ..., N)$, such that $\iota_{\mathrm{cfg}}(M') = N'$ and $\iota_{\mathrm{cfg}}(M) = N$.

Further, since $\operatorname{AssignNodes}(\overrightarrow{N}, x) = \operatorname{AssignNodes}_{\operatorname{cfg}}(\overrightarrow{M}, x) \subseteq \{\iota_{\operatorname{cfg}}(M'), \iota_{\operatorname{cfg}}(M)\} = \{N', N\}$, the node N' is also the only node in the path \overrightarrow{N} before N that assigns x.

Thus,
$$\iota_{\text{cfg}}(M') = N' \in \text{RD}(N, x) = \text{RD}(\iota_{\text{cfg}}(M), x)$$
.

PROOF OF LEMMA. By structural induction.

For CFGs consisting only of a single assign node, the unrolled CFGs are identical and the statement follows.

For a sequence of two statements $(S_1; S_2)$, let G be the CFG with sub-graphs G_1 and G_2 for statements S_1 and S_2 respectively. Let H the unrolled CFG with sub-graphs H_1 and H_2 . We consider the only interesting case where $M' \in H_1$ and $M \in H_2$:

$$\overrightarrow{M} = (\underbrace{M, \dots, M_i}_{\in H_1}, \underbrace{M_{i+1}, \dots, M}_{\in H_2}).$$

For sub-paths $\overrightarrow{M_1} := (M, \ldots, M_i, \text{END})$ and $\overrightarrow{M_2} := (\text{START}, M_{i+1}, \ldots, M)$ we apply the induction assumption to get two paths $\overrightarrow{N_1} = (N', \ldots, N_1, \text{END}), \overrightarrow{N_2} = (\text{START}, N_2, \ldots, N)$, with $\iota_{\text{cfg}}(M') = N'$ and $\iota_{\text{cfg}}(M) = N$. The corresponding CFG path in G is $\overrightarrow{N} := (N', \ldots, N_1, N_2, \ldots, N)$ as

$$\begin{aligned} \operatorname{AssignNodes}_{\operatorname{cfg}}(\overrightarrow{M},x) &= \operatorname{AssignNodes}_{\operatorname{cfg}}(\overrightarrow{M_1},x) \cup \operatorname{AssignNodes}_{\operatorname{cfg}}(\overrightarrow{M_2},x) \\ &= \operatorname{AssignNodes}(\overrightarrow{N_1},x) \cup \operatorname{AssignNodes}(\overrightarrow{N_2},x) = \operatorname{AssignNodes}(\overrightarrow{N},x). \end{aligned}$$

For an if statement, let G be the CFG with branch node \tilde{B} , join node \tilde{J} , and sub-graphs G_1 , G_2 . Let H be the unrolled CFG with corresponding B, J, H_1 , and H_2 . We only show the statement for the case

$$\overrightarrow{M} = (B, \underbrace{M', \dots, M}_{\in H_i}, J).$$

Again, we apply the induction assumption to $\overrightarrow{M_i} := (\operatorname{START}, M', \dots, M, \operatorname{END})$ and get a path in G_i : $\overline{N_i} = (\operatorname{START}, N', \dots, N, \operatorname{END})$. Since $\iota_{\operatorname{cfg}}(B) = \widetilde{B}$, $\iota_{\operatorname{cfg}}(J) = \widetilde{J}$, which are not assign nodes, and AssignNodes $(\overline{N_i}) = \operatorname{AssignNodes}_{\operatorname{cfg}}(\overline{M_i})$, the statement follows for the path $(\widetilde{B}, N', \dots, N, \widetilde{J})$.

Lastly, for a while loop, let G be the CFG with branch node \tilde{B} , join node \tilde{J} , and sub-graph G_S . Let H be the unrolled CFG with B_i branch nodes, J join node, and H_i sub-graphs. For the sake of brevity we consider three cases:

• For $M' \in H_i$ and $M \in H_i$

$$\overrightarrow{M} = (\underbrace{M', \dots, M_i}_{\in H_i}, B_{i+1}, \dots, B_j, \underbrace{M_j, \dots, M}_{\in H_j})$$

we apply the induction assumption to the sub-paths and construct the path

$$\vec{N} = (\underbrace{N', \dots, N_i}_{\in G_S}, \tilde{B}, \dots, \tilde{B}, \underbrace{N_j, \dots, N}_{\in G_S}))$$

where \tilde{B} and B_i are do not contribute to AssignNodes(\vec{N}) or AssignNodes(\vec{M}).

• For $M' \in H_i$ and J

$$\overrightarrow{M} = (\underbrace{M', \dots, M_i, B_{i+1}, \dots, B_l, J})$$

we construct the path

$$\overrightarrow{N} = (\underbrace{N', \dots, N_i}_{\in G_S}, \widetilde{B}, \dots, \widetilde{B}, \widetilde{J})$$

• For the path $\overrightarrow{M} = (B_l, J)$ the path in the CFG is $\overrightarrow{N} = (\widetilde{B}, \widetilde{J})$.

Statement. Let G be the CFG and H the unrolled CFG of program S. For each node $M \in H$ the following equation holds.

$$BP_{cfg}(M) := \{\iota_{cfg}(M') : M' \in BP(M)\} = BP(\iota_{cfg}(M))$$
(11)

PROOF. We prove this statement by structural induction.

For CFGs only consisting of a single assign node, the unrolled CFGs are identical, there are no branch parents, and the statement follows.

The sequencing of two statements $(S_1; S_2)$ does not change the branch parents of any node.

For an if statement, let G be the CFG with branch node \tilde{B} , join node \tilde{J} , and sub-graphs G_1 , G_2 . Let H be the unrolled CFG with corresponding B, J, H_1 , and H_2 . Every node M in H_i has branch parents (BP(M) \cap H_i) \cup {B}.

By induction assumption $\mathrm{BP}_{\mathrm{cfg}}(M) \cap G_i = \mathrm{BP}(\iota_{\mathrm{cfg}}(M)) \cap G_i$ and thus

$$\begin{aligned} \mathrm{BP}_{\mathrm{cfg}}(M) &= \left\{ \iota_{\mathrm{cfg}}(M') \colon M' \in \mathrm{BP}(M) \cap H_i \right\} \cup \left\{ \iota_{\mathrm{cfg}}(B) \right\} \\ &= \left(\mathrm{BP}(\iota_{\mathrm{cfg}}(M)) \cap G_i \right) \cup \left\{ \tilde{B} \right\} = \mathrm{BP}(\iota_{\mathrm{cfg}}(M)). \end{aligned}$$

Nodes B, \tilde{B} , J, and \tilde{J} do not have branch parents.

Lastly, for a while loop, let G be the CFG with branch node \tilde{B} , join node \tilde{J} , and sub-graph G_S . Let H be the unrolled CFG with B_i branch nodes, J join node, and H_i sub-graphs.

Every node M in H_i has branch parents $(BP(M) \cap H_i) \cup \{B_1, \dots, B_i\}$.

The CFG node $\iota_{\mathrm{cfg}}(M) \in G_S$ has branch parents $(\mathrm{BP}(\iota_{\mathrm{cfg}}(M)) \cap G_S) \cup \{\tilde{B}\}.$

Since $\iota_{\mathrm{cfg}}(B_j) = \tilde{B}$ and by induction assumption $\mathrm{BP}_{\mathrm{cfg}}(M) \cap G_S = \mathrm{BP}(\iota_{\mathrm{cfg}}(M)) \cap G_S$, we get

$$BP_{cfg}(M) = \{\iota_{cfg}(M') : M' \in BP(M) \cap H_i\} \cup \{\iota_{cfg}(B_1), \dots, \iota_{cfg}(B_i)\}$$
$$= (BP(\iota_{cfg}(M)) \cap G_S) \cup \{\tilde{B}\} = BP(\iota_{cfg}(M)).$$

STATEMENT. Let G be the CFG and H the unrolled CFG of program S. For each node $M \in H$ the following equation holds.

$$\operatorname{prov}(M, x) \subseteq \operatorname{prov}(\iota_{\operatorname{cfg}}(M), x)$$
 (12)

PROOF. Follows directly from the two equations $\{\iota_{\mathrm{cfg}}(M') : M' \in \mathrm{RD}(M,x)\} \subseteq \mathrm{RD}(\iota_{\mathrm{cfg}}(M),x)$ and $\{\iota_{\mathrm{cfg}}(M') : M' \in \mathrm{BP}(M)\} = \mathrm{BP}(\iota_{\mathrm{cfg}}(M))$, since $\mathrm{prov}(M,x)$ is defined in terms of RD and BP. \square

B.5 Proof of Proposition 4

Statement. For each node M in the unrolled CFG H and variable x, there exists an evaluation function

$$V_r^M \in [\mathcal{T}|_{\text{prov}(M,x)} \to \mathcal{V}],$$

such that for all traces tr with $\sigma_0 = (x_i \mapsto \text{null}, \boldsymbol{p} \mapsto 1)$ and execution sequence

$$(\sigma_0, START) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_i, M_i) \xrightarrow{\mathsf{tr}} \cdots \xrightarrow{\mathsf{tr}} (\sigma_l, END)$$

we have

$$\sigma_i(x) = V_x^{M_i}(\mathsf{tr}).$$

PROOF. First, we have to justify the use of mathematical induction to prove properties on a potentially infinite graph. For $M_1, M_2 \in H$ define the relation $M_1 \prec M_2 \Leftrightarrow M_1$ is parent of M_2 (i.e. there is an edge from M_1 to M_2 in H). By construction of H, there are no infinite descending chains, $M_i \in H$ for $i \in \mathbb{N}$ such that $M_{i+1} \prec M_i$. This makes \prec a well-founded relation, i.e. every non-empty subset has a minimal element. As a result, we may apply induction with respect to this relation (as has been done in the proof of Proposition 2) for graphs with infinite nodes. This proof principle is called well-founded or Noetherian induction.

To complete the proof, we only need to consider one additional case:

Case 3. Let J be a join node of a while loop with the parents $B_i = \operatorname{Branch}(E)$, $i \in \mathbb{N}$. The function $V_x^{B_i} \in [\mathcal{T}|_{\operatorname{prov}(B_i,x)} \to \mathcal{V}]$ exists by induction assumption and computes the value of x before executing the while loop body for the i-th time. If there is a reaching definition $N' \in \operatorname{RD}(J,x)$ on a path (B_i,\ldots,N',\ldots,J) , define

$$V_x^J(\mathrm{tr}) \coloneqq \begin{cases} V_x^{B_i}(\mathrm{tr}) & \text{if } \exists i \in \mathbb{N} \colon \forall j < i \colon V_E^{B_j}(\mathrm{tr}) = \mathrm{true} \land V_E^{B_i}(\mathrm{tr}) = \mathrm{false,} \\ \mathrm{null} & \text{otherwise.} \end{cases}$$

Further, as N' belongs to the sub-CFG of the while loop body, we have that for every B_i there is a $N_i' \in \mathrm{RD}(J,x)$ on the path from B_i to J. Since $B_i \in \mathrm{BP}(N')$, by Equation (9) $\mathrm{prov}(B_i,E) \subseteq \mathrm{prov}(J,x)$ and thus $V_x^J \in [\mathcal{T}|_{\mathrm{prov}(J,x)} \to \mathcal{V}]$. If there is no reaching definition in the while loop body sub-CFG, then $\mathrm{prov}(J,x) = \mathrm{prov}(B_1,x)$ and define $V_x^J = V_x^{B_1}$.

B.6 Proof of Theorem 2

STATEMENT. Let G be the CFG for a program S. Let N_1, \ldots, N_K be all sample nodes in G. For each sample node $N_k = \text{Assign}(x_k = \text{sample}(E_0^k, f^k(E_1^k, \ldots, E_{n_k}^k)))$, let

$$A_k = \operatorname{addresses}(N_k) \cup \bigcup_{i=0}^{n_k} \operatorname{prov}(N_k, E_i^k) \cup \bigcup_{N' \in \operatorname{BP}(N_k)} \operatorname{prov}(N', E_{N'}).$$

Then, there exist functions $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ such that if $p_S(\mathsf{tr}) \neq \mathsf{undefined}$, then

$$p_S(\mathsf{tr}) = p_G(\mathsf{tr}) = \prod_{k=1}^K p_k(\mathsf{tr}).$$

PROOF. Let H be the *unrolled* CFG of program S. Denote the (countably many) sample nodes in H with $M_j = \operatorname{Assign}(x^j = \operatorname{sample}(E_0^j, f^j(E_1^j, \dots, E_{n_j}^j)))$. Like in the proof of Theorem 1, define for each M_j , $b_j(\operatorname{tr}) := \bigwedge_{M' \in \operatorname{BP}(M_j)} t_{M'}^j(V_{EM'}^{M'}(\operatorname{tr}))$ where

$$b_j \in [\mathcal{T}|_{\bigcup_{M' \in \mathrm{BP}(M_i)} \mathrm{prov}(M', E_{M'})} \to \{\mathsf{true}, \mathsf{false}\}].$$

Again, $b_i(tr)$ = true if M_i is in the execution sequence for tr else false. Define

$$\tilde{p}_j(\mathsf{tr}) \coloneqq \delta_{b_j(\mathsf{tr})} \mathrm{pdf}_{f^j} \left(\mathrm{tr}(V_{E_0^j}^{M_j}(\mathsf{tr})); V_{E_1^j}^{M_j}(\mathsf{tr}), \dots, V_{E_{n_j}^j}^{M_j}(\mathsf{tr}) \right) + (1 - \delta_{b_j(\mathsf{tr})}).$$

We have $\tilde{p}_j \in [\mathcal{T}|_{\tilde{A}_i} \to \mathbb{R}_{\geq 0}]$, for

$$\tilde{A}_j = \operatorname{addresses}(M_j) \cup \bigcup_{i=0}^{n_j} \operatorname{prov}(M_j, E_i^j) \cup \bigcup_{M' \in \operatorname{BP}(M_i)} \operatorname{prov}(M', E_{M'}).$$

Finally, we group the nodes M_i by their corresponding CFG node.

$$p_k = \prod_{j: \ \iota_{\mathrm{cfg}}(M_j) = N_k} \tilde{p}_j$$

If $\iota_{\mathrm{cfg}}(M_j) = N_k$, then $\tilde{A}_j = A_k$, since for $i = 0, \dots, n$ we have $E_i^k = E_i^j$ and by Eq. (5)

$$\operatorname{prov}(M_i, E_i^j) \subseteq \operatorname{prov}(\iota_{\operatorname{cfg}}(M_i), E_i^j) = \operatorname{prov}(N_k, E_i^k)$$

and by Eq. (4) it holds that

$$\{\iota_{\mathrm{cfg}}(M') \colon M' \in \mathrm{BP}(M_j)\} = \mathrm{BP}(\iota_{\mathrm{cfg}}(M_j)) = \mathrm{BP}(N_k)$$

such that

$$\bigcup_{M' \in BP(M_j)} \operatorname{prov}(M', E_{M'}) \subseteq \bigcup_{M' \in BP(M_j)} \operatorname{prov}(\iota_{\operatorname{cfg}}(M'), E_{\iota_{\operatorname{cfg}}(M')})$$
$$= \bigcup_{N' \in BP(N_k)} \operatorname{prov}(N', E_{N'}).$$

Thus, for all j, $\iota_{\mathrm{cfg}}(M_j) = N_k$, we have $\tilde{p}_j \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$ which implies $p_k \in [\mathcal{T}|_{A_k} \to \mathbb{R}_{\geq 0}]$. \square

C Towards a Measure-theoretic Interpretation of the Operational Semantics

In this section, we will briefly discuss how one may construct a measure space on traces such that the function $tr \mapsto p_S(tr)$ is indeed a Radon-Nikodym derivative of a measure on this space. This construction is related to denotational semantics based on trace types [Lew et al. 2019, 2023]. We first define a measure space for each value type:

$$\tau ::= \{\mathsf{null}\}, \mathbb{Z}, \mathbb{R}, \mathbb{R}^2, \dots$$

$$\mathcal{M}_{\tau} = (M_{\tau}, \Sigma_{\tau}, \nu_{\tau}) ::= (\{\mathsf{null}\}, \mathcal{P}(\{\mathsf{null}\}), \delta_{\mathsf{null}}), \ (\mathbb{Z}, \mathcal{P}(\mathbb{Z}), \#), \ (\mathbb{R}, \mathcal{B}, \lambda), \ (\mathbb{R}^2, \mathcal{B}^2, \lambda^2), \ \dots$$

These measure spaces model the support of common distributions, but could also be extended by building sum and product spaces. They are comprised of the typical Dirac measure, power sets, counting measure, Borel sets, and Lebesgue measures.

Each trace tr can be assigned an unique type function τ_{tr} : Strings \to Types such that for all $\alpha \in$ Strings we have $tr(\alpha) \in \tau_{tr}(\alpha)$. The types are assumed to be disjoint (e.g. $\mathbb{Z} \cap \mathbb{R} = \emptyset$). Let

$$S_{<\infty} = \{s : \text{Strings} \to \text{Types} : |\{\alpha \in \text{Strings} : s(\alpha) \neq \{\text{null}\}\}| < \infty\}$$

be the set of all type functions that correspond to traces with a finite number of non-null values. We enumerate the addresses, Strings = $\{\alpha_i : i \in \mathbb{N}\}$, and define a measureable space for each type function, s :Strings \to Types, $\mathcal{M}_s = (M_s, \Sigma_s) = \bigotimes_{i \in \mathbb{N}: s(\alpha_i) \neq \{\text{null}\}} \mathcal{M}_{s(\alpha_i)}$. If $s \in S_{<\infty}$, then \mathcal{M}_s is a finite dimensional product space with product measure ν_s . For the set of traces \mathcal{T} , we define the σ -algebra Σ with $A \in \Sigma \Leftrightarrow \forall s :$ Strings \to Types: $\pi_s(\{\text{tr} \in A : \tau_{\text{tr}} = s\}) \in \Sigma_s$, where $\pi_s : \mathcal{T} \to M_s$ is the canonical projection. The reference measure on (\mathcal{T}, Σ) is

$$\nu(A) = \sum_{s \in \mathcal{S}_{<\infty}} \nu_s(\pi_s(\{\mathsf{tr} \in A \colon \tau_{\mathsf{tr}} = s\}))$$

which effectively is a measure of traces with a finite number of non-null values.

If a primitive distribution f with support $(f) \subseteq \tau_f$ and arguments v_1, \ldots, v_n corresponds to the measure μ_{f,v_1,\ldots,v_n} with Radon-Nikodym derivative $\mathrm{d}\mu_{f,v_1,\ldots,v_n}/\mathrm{d}v_{\tau_f}$, we define

$$\operatorname{pdf}_f(v; v_1, \dots, v_n) = \begin{cases} (\mathsf{d}\mu_{f, v_1, \dots, v_n} / \mathsf{d}v_{\tau_f})(v) & \text{if } v \in \tau_f \\ 0 & \text{otherwise.} \end{cases}$$

The measure-theoretic interpretation of a program is the measure $\mu_S(A) = \int_{\mathcal{T}_{valid} \cap A} p_S \, dv$, where

$$\mathcal{T}_{\text{valid}} \coloneqq \{ \text{tr} \colon p_S(\text{tr}) \neq \text{undefined} \land \forall \alpha \colon \text{tr}(\alpha) \neq \text{null} \Rightarrow p_S(\text{tr}[\alpha \mapsto \text{null}]) = \text{undefined} \}$$

is the set of all traces with well-defined density such that changing a value at any address to null leads to an undefined density. That is, for $tr \in \mathcal{T}_{valid}$ all addresses that are not used during the execution of the program are set to null and all address that are used are set to non-null values. Such a trace necessarily has a finite number of non-null values as the density for non-termination is undefined which is the only possibility for executing an infinite number of sample statements. This makes p_S the Radon-Nikodym derivative of μ_S with respect to v on \mathcal{T}_{valid} .

We emphasize that this interpretation demands a careful analysis of the measurability of $\mathcal{T}_{\text{valid}}$ and p_S which is beyond the scope of this work. This requires additional assumptions about the program, at the very least that the primitives g are measurable. Such measurability analysis may be done along the lines of Borgström et al. [2016] and Lee et al. [2019].

Note that this construction can handle programs where random variables are of mixed-type like the variable y in the example below:

```
x = sample("x", Bernoulli(p))
if x == 1 then
    y = sample("y", Bernoulli(0.5))
else
    y = sample("y", Normal(0.,1.))
```

We list the density according to the semantics for different example trace of different type:

```
\begin{split} p(\{x \mapsto 1 \colon \mathbb{Z}, y \mapsto 1 \colon \mathbb{Z}\}) &= \mathrm{pdf}_{\mathrm{Bernoulli}}(1; 0.5) \cdot \mathrm{pdf}_{\mathrm{Bernoulli}}(1; 0.5) = 0.5 \cdot 0.5 \\ p(\{x \mapsto 1 \colon \mathbb{Z}, y \mapsto 1.0 \colon \mathbb{R}\}) &= \mathrm{pdf}_{\mathrm{Bernoulli}}(1; 0.5) \cdot 0 = 0 \\ p(\{x \mapsto 0 \colon \mathbb{Z}, y \mapsto 1.0 \colon \mathbb{R}\}) &= \mathrm{pdf}_{\mathrm{Bernoulli}}(0; 0.5) \cdot \mathrm{pdf}_{\mathrm{Normal}}(1.0; 0.0, 1.0) \approx 0.5 \cdot 0.24197 \\ p(\{x \mapsto 0 \colon \mathbb{Z}, y \mapsto 1 \colon \mathbb{Z}\}) &= \mathrm{pdf}_{\mathrm{Bernoulli}}(0; 0.5) \cdot 0 = 0 \end{split}
```

C.1 Conditioning

Observed data is also modelled as a trace obs_tr, where addresses are mapped to the observed data points. All addresses that do not correspond to observed data map to null. The function $tr \mapsto p_S(tr \oplus obs_tr)$ then corresponds to the *unnormalised* posterior density, where we merge the trace tr, encapsulating latent variables, with the observed trace obs_tr like below

$$(A \oplus B)(\alpha) = \begin{cases} B(\alpha) & \text{if } B(\alpha) \neq \text{null,} \\ A(\alpha) & \text{otherwise.} \end{cases}$$