

ANDREAS PRODROMOU

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EDUCATION

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| Ph.D., Computer Science
<i>Department of Computer Science and Engineering</i>
University of California, San Diego | June 2013 – Present |
| M.Sc., Computer Engineering
<i>Department of Electrical and Computer Engineering</i>
University of Cyprus | 2011 – 2013 |
| B.Sc., Computer Engineering
<i>Department of Electrical and Computer Engineering</i>
University of Cyprus | 2007 – 2011 |

EXPERIENCE

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| University of California San Diego, CSE Department
<i>Graduate Student Researcher</i> | August 2013 – Present
<i>San Diego, CA</i> |
| <ul style="list-style-type: none">· Exploring the capabilities of dynamic dead code elimination.· Evaluating memory controller scheduling policies under emerging technological trends. | |
| University of Cyprus, CS Department
<i>Special Scientist</i> | June 2011 – June 2013
<i>Nicosia, Cyprus</i> |
| <ul style="list-style-type: none">· Part of Eurocloud FP7 Project (http://www.eurocloudserver.com/) – European research and development program for building a 3D server-on-chip concept integrating low power cores.· Research in Network-on-Chip Reliability.· Designed and evaluated circuit-level modules to detect hardware faults. (Published in MICRO'45 conference) | |
| University of Cyprus, ECE Department
<i>Special Scientist</i> | June 2010 – September 2010
<i>Nicosia, Cyprus</i> |
| <ul style="list-style-type: none">· Implementation of a parameterized cycle-accurate Network-on-Chip simulator as part of a Full-System simulator (extensive programming and simulations)· Later awarded as Best Senior Design Project | |
| KIOS Research Center for Intelligent Systems & Networks
<i>Undergraduate Research Scientist</i> | June 2009 – September 2009
<i>Nicosia, Cyprus</i> |
| <ul style="list-style-type: none">· Studied the infrastructure of a Network-on-Chip module· Implemented and assessed routing algorithms for such networks | |

RESEARCH INTERESTS

Memory scheduling policies, dynamic dead code elimination, multicore architectures, 3D microprocessor design, Network-on-Chip architectures.

SKILLS

Programming & Scripting	C, C++, Java, Javascript, VHDL, Perl, Python
Debugging	GDB
Simulators	Gem5 & Simics full-system simulators, Garnet NoC simulator, QEMU
Architectural Analysis	SPEC & PARSEC benchmarks, Pin, Simpoint
Version Control	Git, SVN
Languages	Greek, English

PUBLICATIONS

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| 2012 | NoCAAlert: An on-line and real-time fault detection mechanism for network-on-chip architectures, A. Prodromou , A.Panteli, C. Nicopoulos and Y. Sazeides |
| 2011 | Thermal characterization of cloud workloads on a power-efficient server-on-chip, D. Milojevic, S. Idgunji, D. Jevdjic, E. Ozer, P. Lotfi-Kamran, A. Panteli, A. Prodromou , C. Nicopoulos, D. Hardy, B. Falsafi and Y. Sazeides |

AWARDS AND ACCOMPLISHMENTS

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| 2013 | Powell Fellowship for academic years 2013–2016, UCSD Computer Science and Engineering. |
| 2012 | HiPEAC Paper Award for the paper titled “NoCAAlert: An on-line and real-time fault detection mechanism for network-on-chip architectures”. |
| 2011 | Best Senior Design Project Award in the Department of Electrical and Computer Engineering, University of Cyprus for academic year 2010-2011. |