

Challenge	Tradeoff	THM	HMA	CAMEO	MemPod
Page / line Relocation	<i>Flexibility / Time</i>	Only 1 Candidate <i>Min / Min</i>	No Restrictions <i>Max / High</i>	Only 1 Candidate <i>Min / Min</i>	Intra-Pod Migration <i>High / Medium</i>
Remap Table Size	<i>Flexibility / Area</i>	1 entry per fast page (1.5kB) <i>Min / Medium</i>	No remap table <i>Max / Min</i>	1 entry per fast line (72kB) <i>Min / Medium</i>	1 entry per page (2.8 MB / Pod) <i>High / Max</i>
Activity Tracking	<i>Accuracy / Area</i>	8 bits per fast page (512kB) <i>Medium / Low</i>	16 bits per page (9MB) <i>Medium / Max</i>	N/A <i>N/A / Min</i>	64 MEA entries (736 B) <i>Medium / Low</i>
Migration Trigger	<i>N/A</i>	Threshold	Interval	Event	Interval
Tracking Organization	<i>Simplicity / Parallelization</i>	Fully centralized (Serialized requests) <i>High / Min</i>	Fully distributed <i>High / Max</i>	Fully distributed <i>High / Max</i>	Semi-distributed (Pods operate independently) <i>High / High</i>
Migration Driver	<i>Latency</i>	CPU <i>High</i>	CPU (OS) <i>Max</i>	MCs <i>Medium</i>	Pod <i>Low</i>
Migration Cost	<i>Time</i>	HW cost + CPU time <i>Medium</i>	HW + SW + cold TLBs + CPU <i>Max</i>	HW Cost + Communication <i>Low</i>	HW <i>Min</i>