Memory

Address	Content
10	10
100	100
200	200

Swap
10↔100

Memory

Address	Content
10	100
100	10
200	200

Swap
10↔200

Memory

Address	Content
10	200
100	10
200	100

Remap Table

In	Relay
10	10
100	100
200	200

(i)

Swap
10↔100

Remap Table

In	Relay
10	100
100	10
200	200

(ii)

Swap
10↔200

Remap Table

In	Relay
10	200
100	10
200	10

Inconsistent State