Processor		
Cores	8 @ 3.2GHz	
Width	4 wide out-of-order	

Caches		
L1 I-Cache(private)	64KB, 2 way, 4 cycles	
L1 D-Cache (private)	16KB, 4 way, 4 cycles	
L2 Cache (shared)	8MB, 16 way, 11 cycles	

	HBM	DDR4-1600
Bus Frequency	1 GHz	800 MHz
Bus Width (bits)	128	64
Channels	8	4
Ranks	1	1
Banks	16	16
Row Buffer Size	8KB	8KB
tCAS-tRCD-tRP-tRAS	7-7-7-17	11-11-11-28