Tyler Creelan de Laguna

503-929-6233 (text/voice) delaguna.org tyler@delaguna.org

Software Engineer

EDUCATION

Bachelor of Science in Computer Science with Honors, Oregon State University 2003

Master of Science in Computer Science, Oregon State University 2004

EXPERIENCE

Google Developer Groups Portland 2022-Present

President

Relaunched Portland GDG chapter, sponsored by Google for Developers. Tripled membership and organized first DevFest. Hosted eleven events to connect 1,757+ programmers.

Microsoft TEALS 2023-Present

Computer Science Teacher

Taught Advanced Placement Computer Science course (Java based) at Lincoln High School - Portland Public Schools, via non-profit TEALS volunteer program sponsored by Microsoft.

Career Break 2017-2022

Pause to travel internationally and explore: sea kayaking, alpinism, museums, amateur botany, language.

Intel Corporation

Software Design Engineer - Mobile 2013-2017

Optimized design of the Intel SoC, focus on low power interactions:

- Added experimental Linux kernel driver support for low-power Display Engine and frame compression in drm/i915 module, extending battery life to attain a prospective Internet of Things customer.
- Expanded LLVM-derived C++ framework (Maestro) to stress interconnect hardware with machine code.
- Emulated tablet designs on Mentor Graphics Veloce platform, selectively dispatching Linux flows to Simics to achieve tractable regressions for pre-tapeout confidence.

Software Engineer - Modeling 2010–2013

Simulated circuits of Core i7 power controller (PCU) as extended tour of duty:

- Benchmarked memory power limits (RAPL) and Turbo2 algorithms in C++, comparing against actual microcode with Coco. Created model in Specimen 'e' to find 11 firmware path bugs before tapein.
- Resolved power spike with running power limits (RAPL); analyzed verilog to create experimental patch and satisfy blade server customers and EU regulations, receiving Division Recognition award.

Software Engineer - Post-Silicon Tools 2004-2010

Team lead for power analysis of new CPUs, creating strategy at tape-in to meet biannual product qual:

- Built new Win7 app in Qt C++ for wattage telemetry, heading off 400mW routing bug in Xeon boards.
- Created new C++ test harness on host/target coupled with JTAG probe to verify S3 using cacheline breakpoints, discovered new circuit bug fixed in first Pentium XD product.
- Discovered catastrophic protocol bug in QuickPath Interconnect (QPI): prototyped solution with Focused Ion Beam edits: inserted fix in time for Core i7 launch, receiving achievement award.

Oregon State University - Department of Computer Science

Research Assistant 2003-2004

- Designed novel system for usability studies under NSF grant, expanding 5k line Common Lisp (CLOS) engine connected to Java UI over tcp/ip. Added new lookahead LR parser with yacc.

- Designed machine learning network analyzer in Perl with decision trees to detect four intrusion attack types.

OPEN SOURCE

- Advanced Component Platform Architecture (acpi.sourceforge.io): C, Linux, System states, BIOS
- Gnumeric (gnumeric.org), Testmeric module: C++, gtk, glibc.
- Debian GNU/Linux, Sarge Release (debian.org): archives, Bash, dpkg, automation

PUBLICATIONS

1) "Power vs Debug: Solving IEEE JTAG Observability with Deep Powerdown™ (C6) active", Intel Design Technology and Test Conference (2007). T Creelan, N Ashraf and J Maxwell.

2) "Reporting CPU Frequency: The Challenge of Intel Turbo Boost™ Technology", Intel Design Technology and Test Conference (2007). T Creelan and T Baird.

3) "Scaling a Dataflow Testing Methodology", IEEE International Symposium on Software Reliability Engineering. (2006) p13-22. M Fisher, G Rothermel, T Creelan and M Burnett.

4) "Educators Have Hard Choices; Nationally". Science (Letters). Vol 289 (2000). Tyler Creelan.

CERTIFICATIONS

Google: Project Management | Linux Debug: Intel Open Source Technology Center
Win10 Kernel Debug | PCIE-3, USB-3 - MindShare | Google: Data Analytics

PROFESSIONAL ASSOCIATIONS

Google Developer Groups | ACM ICPC State Champion Team | Microsoft TEALS
ACM Student Chapter Coach | Portland Java User Group | PDX Women In Technology Mentor

TECHNICAL SKILLS

- **Software Programming Skills**: C/C++, Java, Bash, Posix, Sus, Qt, Linux modules, x86 asm
- **Hardware Design and Debug Skills**: Specman, Intel microarchitecture, Test Access Port (TAP)
- **Tools**: Github, Bitkeeper, Vim, clang, kernel debug, adb, WinDbg
- **Operating Systems**: Debian GNU/Linux, Android, MacOS X
- **Interface Protocols**: ACPI, Extensible Firmware Interface (EFI), JTAG