

## Sudheer Kumar

---

RESEARCH INTERESTS	High Performance Computing, Performance Optimization on Multi-cores and Accelerators
EDUCATION	<p>Ph.D., Computer Science, Sri Sathya Sai Institute of Higher Learning, April 2013</p> <ul style="list-style-type: none"><li>• Advisor: <i>Prof. Ashok Srinivasan, Florida State University</i></li><li>• Thesis: <i>Topology and Routing Aware Mapping on Parallel Processors</i></li></ul> <p>M.Tech., Sri Sathya Sai Institute of Higher Learning, March 2006</p> <ul style="list-style-type: none"><li>• Advisor: <i>Mr. Shakti Kapoor, STSM, IBM Austin</i></li><li>• Thesis: <i>Design and Implementation of Proc File System in MINIX Operating System</i></li></ul> <p>B.Tech., RVR&amp;JC College of Engineering, April 2004</p>
WORK EXPERIENCE	<b>Assistant Professor</b> , Department of Mathematics & Computer Science, July 2011 - Present
JOURNAL PUBLICATIONS	<i>Dynamic Load Balancing for Petascale Quantum Monte Carlo Applications: The Alias Method</i> , <b>C.D. Sudheer</b> , S. Krishnan, A. Srinivasan, and P. R. C. Kent. Computer Physics Communications, Feb 2013, Impact Factor : 3.268, (5-year Impact Factor: 2.812).
CONFERENCE PUBLICATIONS	<p><i>Optimization of the Hop-Byte Metric for Effective Topology Aware Mapping</i>, <b>C.D. Sudheer</b>, Ashok Srinivasan, Proceedings of the 19th IEEE International Conference on High Performance Computing (HiPC), 2012, (Acceptance rate: 25%).</p> <p><i>Optimizing Assignment of Threads to SPEs of the Cell BE Processor</i>, <b>C.D. Sudheer</b>, T. Nagaraju, P.K. Baruah, Ashok Srinivasan, 10th IEEE International Workshop on Parallel and Distributed Scientific and Engineering Computing (PDSEC), Proceedings of the 23rd International Parallel and Distributed Processing Symposium, IEEE, 2009, (Citations: SG - 5).</p> <p><i>High Throughput Compression of Floating point numbers in GPUs</i>, Ajith Padyana, <b>C.D Sudheer</b>, P.K. Baruah, Ashok Srinivasan, 2nd IEEE International Conference on Parallel, Distributed and Grid Computing - 2012 Himachal Pradesh, December 2012.</p> <p><i>Investigating Algorithmic Techniques for Enhancing Application Performance on Multicore Processors</i>, <b>C.D. Sudheer</b> (Advisor: Ashok Srinivasan), PhD Forum at IEEE International Parallel and Distributed Processing Symposium, 2009.</p> <p><i>A Communication Model for Determining Optimal Affinity on the Cell BE processor</i>, <b>C.D. Sudheer</b>, Sriram, S.: In: Proc. 16th IEEE International Conference on High Performance Computing (HiPC), Student Research Symposium, Dec 2009.</p>
INVITED PRESENTATIONS	<p><i>An Overview of the Global Arrays Toolkit</i>, Five-days Technology Workshop on Heterogeneous Computing - Many Core/ Multi GPU - Performance of Algorithms, Application Kernels (HeMPa), 2011, at CMSD, UoHYD by C-DAC Pune &amp; CMSD.</p> <p><i>Programming for Performance on Cell BE processor</i>, Performance Enhancement on Emerging Parallel Processing Platforms Workshop (PEEP), 2008, jointly organized by C-DAC and IUCAA.</p>
PROFESSIONAL SERVICE	Technical Program Committee member, International Conference on High Performance Computing and Communications (HPCC), 2011, 2012.

TPC member, The 11th IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA), 2013.

TPC member, The 12th International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP), 2012.

TPC member, Student Research Symposium, IEEE International Conference on High Performance Computing (HiPC), 2012.

COMPUTER ACCESS TIME GRANTS *XSEDE Research Allocation: Scaling Communication Performance for Massively Parallel Applications*, 800,000 SUs, PI: Prof. Ashok Srinivasan, Florida State University.

*Teragrid Startup Allocation*, PI: Prof. P. Sadayappan, Ohio State University.

*XSEDE Education Allocation: Programming for Performance on multicore and many-core processor*, PI: Prof. Ravi Mukkamala, Old Dominion University. Have used this allocation effectively for teaching graduate level courses. Had access to the following supercomputers: TACC systems Ranger, Lonestar and Longhorn, SDSC systems Gordon and Trestles, PSC Blacklight.

TEACHING High Performance Computing with Accelerators, Summer 2012.  
This course was highly successful resulting in 9 student papers, more than one third of the total papers, accepted for Student Research Symposium, at HiPC 2012. And also, 3 out of the 4 awards constituted for Best Presentation and Best Poster were secured by the students of this course. Course Webpage: <http://dmacssite.github.com>

Programming for Performance, Winter 2011, 2012 and 2013. <http://progforperf.github.com>

Parallel Computing, Winter 2013. <http://parallelcomp.github.com>

Computer Organization and Design, 2010, 2011 and 2012.

Processor Architecture and its Applications, 2008.

Operating Systems Design and Implementation, 2007.

Systems Programming using MINIX Operating System, 2006.

ACADEMIC ACHIEVEMENTS TCPP PhD Forum Travel Grant for attending the IPDPS 2009 conference in Italy.

Secured 96.84 percentile in Graduate Aptitude Test in Engineering (GATE-04) in Computer Science stream in 2004.

Achieved University first rank in C programming theory and laboratory exam in the 1st year of B.Tech.

REFERENCES Prof. Ashok Srinivasan, *Florida State University* ([asriniva@cs.fsu.edu](mailto:asriniva@cs.fsu.edu))

Prof. P. Sadayappan, *Ohio State University* ([saday@cse.ohio-state.edu](mailto:saday@cse.ohio-state.edu))

Mr. Shakti Kapoor, *STSM, IBM Austin* ([skapoor@us.ibm.com](mailto:skapoor@us.ibm.com))

Prof. Pallav Kumar Baruah, *Sri Sathya Sai Institute of Higher Learning* ([pkbaruah@sssihl.edu.in](mailto:pkbaruah@sssihl.edu.in))