# Rohit Kuppili

**education**  IIT Madras
CGPA: 7.91

**skills**
- Python
- System Verilog
- UVM

**Experience**  *Design Engineer Texas Instruments*  2015-2016

- Worked on DDC where I automated the test environment to run dynamic test cases manually
- Worked on NLC

**Achievements**

| 2012 | Best eater in the world | voila |
| 2012 | Best eater in the non existing pragmatic visionary bullshit world | voila |