

Workplan

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October 10, 2014

1 Due by Sunday 5/10

- ALU Design: 2-3 hours

2 Due by Tuesday 7/10

- ALU verilog: 2-3 hours
- Test bench verilog: 1-2 hours

3 Due by Wednesday 8/10

- Timing Analysis: 1 hour
- Workplan Analysis: 1 hour
- Writup: 2-3 hours