Green University of Bangladesh
Department of Computer Science and Engineering (CSE)
Faculty of Sciences and Engineering
Semester: Spring, Year: 2024, B.Sc. in CSE (Day)

LAB REPORT NO: 01
Course Title: Digital Logic Design Lab
Course Code: CSE 204   Section: 231
Lab Experiment Name: Verification of Basic Logic Gates

Student Details

Name	ID
Promod Chandra Das	**231002005**

Lab Date   : _
Submission Date   : _
Course Teacher's Name   :   Md. Shihab Hossain

[For Teachers use only: Don't Write Anything inside this box]

Lab Report Status	
Marks: ……………………………	Signature:.....................
Comments:..	Date:.............................