EHSAN (ERIC) QASEMI

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RESEARCH INTEREST

Machine Learning DataScience Bioinformatics High Perf. Computing

EDUCATION

MSC. IN COMPUTER SCIENCE(GPA 3.53/4): University of Wisconsin at Madison, (Graduate: May 2018)

MSC. IN COMPUTER ENGINEERING (GPA 3.53/4): University of Wisconsin at Madison, (Graduated: May 2017)

• CERTIFICATE IN ENTREPRENEURSHIP (GPA 3.5/4): University of Wisconsin Madison, Madison, WI, USA

BSC. IN ELECTRONICS ENGR. /DIGITAL SYS. (GPA 16.12/20): University of Tehran, (Graduated: February 2015)

Skills

Programming: C/C++, Python, Java, Scala, Matlab, Julia, Octave, Cuda, OpenCL, Assembly, VB
HPC frames: Hadoop (HDFS, MR, Tez), Spark (SQL, Streaming), Apache(Hive, storm, flink),
ML Libs: Tensorflow, Keras, Pandas, NumPy, SciPy, MatPlotLib, scikit-learn, PyQt, JuMP, D3

HDLs : Verilog, SystemVerilog, VHDL, Chisel HDL, System C/C-AMS

Databases : SQL, SQL-Lite, RocksDB

RESEARCH EXPERIENCE

PERSEPOLIS RESEARCH GROUP

Prof. Amir Assadi

University of Wisconsin-Madison 2016-Current

- Research and development in big data science methods: Manage and process **massive heterogeneous spatiotemporal data sets** and **big data** that includes time-series data, video, genome sequence etc.
- Research team member for the project: Novel clustering and data visualization methods for discovery of **Autism Spectrum Disorder (ASD)** personalized therapy.
- Propose an architecture based on **deep recurrent neural network** and its implementation for a solution of ASD classification.
- Research and development of physics-based algorithm and software: **Entropy methods** in ASD feature discovery, and pattern classification.
- Intellectual Property (IP) disclosure: Novel algorithms and methodology in medicine of ASD, disclosed to Wisconsin Alumni Research Foundation (WARF) for patent application.
- Research in Progress:
 - o Big data local-to-global methods in analysis and prediction of dynamics in **atmospheric chemistry spatiotemporal data**.
 - o Inverse problems in **brain activation dynamics** using multiple modalities (**fMRI**, **EEG**, and **DTI**)

CASES IN COMPENSATION

University of Wisconsin-Madison 2015-2016

Prof. Barry Gerhart

- Financial Data Analytics: Implement interactive application to manage and Analyse the Financial dataset
- Case In Compensation: Implement an integrated case on pay model and Compensation used in the HR Compensation textbook.

DESIGN, VERIF. & DEBUG OF EMBEDDED SYSTEMS (DVDES) LAB Prof. Bijan Alizadeh

University of Tehran 2014-15

- HW/SW co-design of highly parallel Blokus-Duo Solver based on Monte Carlo Tree Search (MCTS) Engine.
- Research on a Monte-Carlo Tree Search(MCTS) Based Scheduling algorithm.

SILICON INTELLIGENCE AND VLSI SIGNAL PROCESSING(SI) LAB Prof. Sayed Mehdi Fakhrae

University of Tehran Summer & Fall 2013

- Design state-of-the-art low power SRAM memory cells to operate in subthreshold voltages.
- Automated SRAM Memory Generator CAD tool compatible with low-power SRAMs in sub-threshold

COMPUTER-AIDED DESIGN(CAD) AND TLM LABS Prof. Zain Navabi

University of Tehran Summer & Fall 2012

- Research on formal verification methods using temporal logic
- HW/SW co-design of AES encryption/decryption algorithm on FPGA platform.

NOTABLE PUBLICATIONS

- ACCEPTED: Z. Liz Li, E. Qasemi, A. Ardalan, H. Gao, A. H. Assadi, "A Computational Model for Mental Face Spaces: **Deep Learning Empirical Space of Faces**", The 2017 International Conference on Computational Science and Computational Intelligence (CSCI 17), Dec 2017, Las Vegas, USA
- ACCEPTED: A. H. Assadi, P. Han, E. Qasemi, A. Ardalan, H. Gao, "**Deep Learning Empirical Topology** for Classical Music Style Decision Making", The 2017 International Conference on Computational Science and Computational Intelligence (CSCI 17), Dec 2017, Las Vegas, USA
- ACCEPTED: S. Yuchen, E. Qasemi, A. Ardalan, H. Gao, A. H. Assadi, "Deep Learning Art History from Data: Baroque Intellectual Influence on the Romantic Era Painting", The 2017 International Conference on Computational Science and Computational Intelligence (CSCI 17), Dec 2017, Las Vegas, USA
- M. Biglari, E. Qasemi, B. PourMohseni, "Maestro: A High-Performance **AES Encryption/Decryption** System", The 17th CSI International Symposium on Computer Architecture & Digital Systems (CADS 2013), October 2013, School of Computer Science, IPM, Tehran, Iran.
- E. Qasemi, Mohammad H. Shadmehr, Bardia Azizian, Amir Samadi, Sajjad Mozaffari, Amir Shirian and Bijan Alizadeh, "Highly Scalable, Shared Memory, **Monte Carlo Tree Search** based Blokus Duo Solver on FPGA", International Conference on Field-Programmable Technology (FPT), 2014.

PROFESSIONAL EXPERIENCE

LEAD EMBEDDED DESIGNER/SOFTWARE DEVELOPER/VERIFICATION ENGINEER IWIN CO.

AUGUST 2014-JULY 2015 Tehran, IRI

- FPGA-based Hardware Security Module (HSM) to provide a secure platform for bank applications.
- Manage a 4 member HW team to Design the FPGA-based HW implementation of cryptography cores.

SOFTWARE DEVELOPER/ EMBEDDED ARCHITECT PARDIS CO.

MAY 2016-AUGUST 2016 Tehran, IRI

• Embedded low cost web server to manage a cryptographic network node to secure media the communication.

SOFTWARE DEVELOPER/EMBEDDED ARCHITECT

JUN 2013-JAN 2014

S. T. FARABI CO.

Sanandaj, IRI

- High performance, low-cost **Genetic algorithm (GA)** based robotic arm controller on FPGA platform.
- GA based high throughput **Face Recognition** hardware on FPGA platform

TEACHING EXPERIENCE

UNIVERSITY OF WISCONSIN-MADISON:

CS 552: Introduction to Computer Architecture (Prof. Yu H. Hu)

ECE 344: Electrical Circuits (L. Shohet)

LCA 601,563: Advanced Persian Language (E. Barnard)

CS 352: Digital System Fundamentals (K. Morrow, X. Zhang)

CS 252: Introduction to Computer Engineering (K. Morrow, M. Morrow)

Fall 2016, 2017, Spring 2017,2018

Spring 2016

UNIVERSITY OF TEHRAN:

ECE 615: Electronic System Level Design (B. Alizadeh)

ICEEP: Embedded Linux Workshop (Z. Navabi)

ECE 367: Digital Logic Design lab (Z. Navabi)

Spring 2013,2014, Fall 2013, Summer 2014

ECE 532: Object-Oriented Simulation of Electronic Systems (Z. Navabi)

ECE 642: FPGA Based Embedded System Design (B. Alizadeh)

ECE 423: Computer Architecture (S. Safari)

ECE 267: Introduction to Computer and Computing Systems (H. Moradi)

Fall 2012

TEST SCORES

TOEFL iBT: GRE General UW SPEAK TEST (Reading 28, Listening 30, Speaking 26, Writing 25) **109/120**Quant: 164, Verbal: 149, Analytics: 3.5/6 **55/60**

2015-2017

NOTABLE ACADEMIC PROJECTS

University of Wisconsin-Madison:

Study on Low-Precision SGD for Big Data analysis in Spark	CS744
Study on Security analysis of Split Manufacturing method in Chip manufacturing	CS756
XSS, XSFR, SQLI, and Phishing attacks and their countermeasures in websites (HTML, JS)	CS642
Low-level OS exploits using Aleph One's code (Stack Smashing, Double free, Format String)	CS642
Study on Monte-Carlo Tree Search Algorithms for Parallel Platforms in CUDA, OpenMP, and MPI	CS759

University of Tehran:

Automated adaptive voice recorder tool based on PESQ speech quality evaluation method	ECE403
Harmonic Synthesizer with Gender Identification Based on AC Pitch Estimation Method.	ECE991
Real-time, RLS based, Adaptive noise cancellation FIR filter on DSK6700, DSP boards.	ECE991

VOLUNTARY AND LEADERSHIP EXPERIENCE

President at Persian Student's Society of UW Madison (PS	S)	
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Organize and Lead group of 7 undergraduate students to participate in:

ICFPT Design Competition

FPGASoC Design competition

Embedded Linux workshop instructor at ICEEP Group

Summer and Fall 2014-15

Winter 2014

Summer 2014

INTERESTS

Piano, and Setar (Persian Traditional instrument),

Archery, Hiking, Biking, and Hitchhiking (I have really missed this last one)

Languages: Persian(MT), Kurdish(MT), Azeri(MT), Arabic (reading knowledge), English

Research/Academic Collaborators

University of Wisconsin-Madison:

Prof. A. Assadi
Prof. Yu H. Hu
hu@engr.wisc.edu
Prof. L. Laurent
laurent.lessard@wisc.edu

University of Tehran:

Prof. B. Alizadeh
Prof. M. A. Akhaee
Prof. S. M. Fakhrae
Prof. Z. Navabi

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