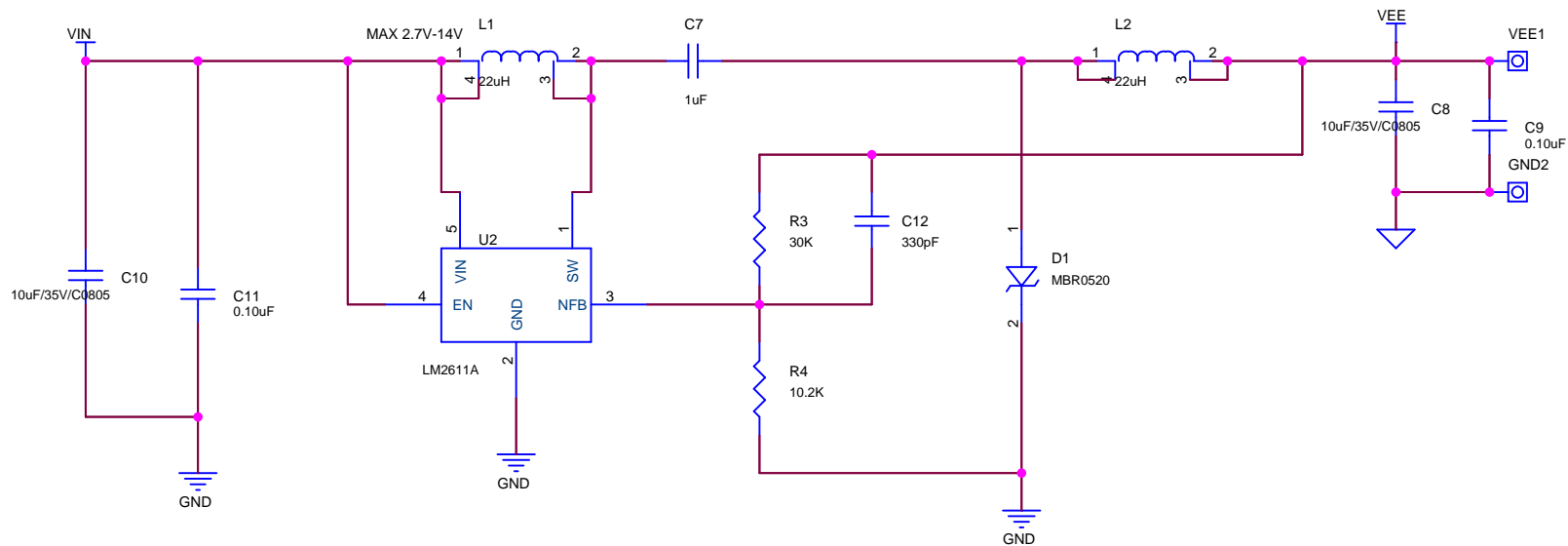
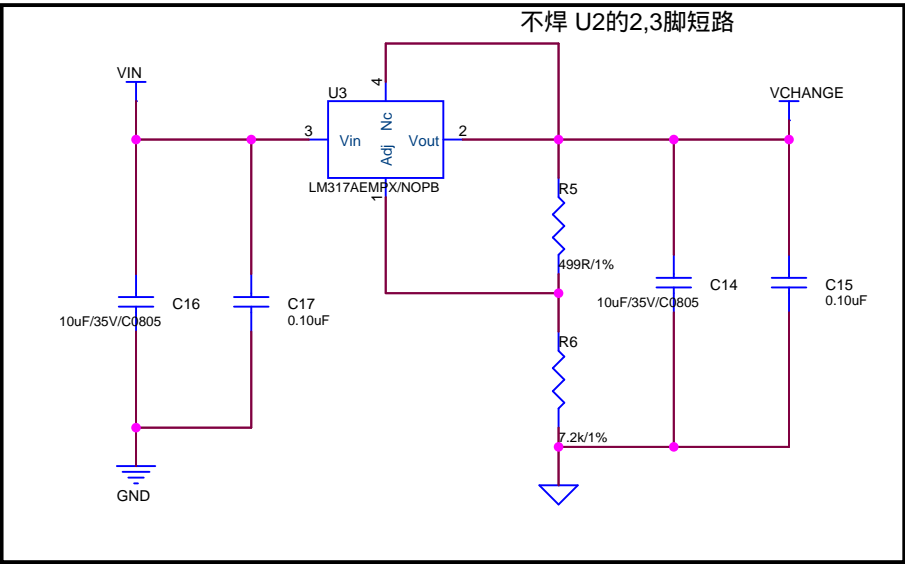


$$1.21 \times (23.3 / 3.6 + 1) + 3\mu A \times 23.2k = 9V$$

$$1.21 \times (10 / 3.6 + 1) + 3\mu A \times 23.2k = 4.57V$$



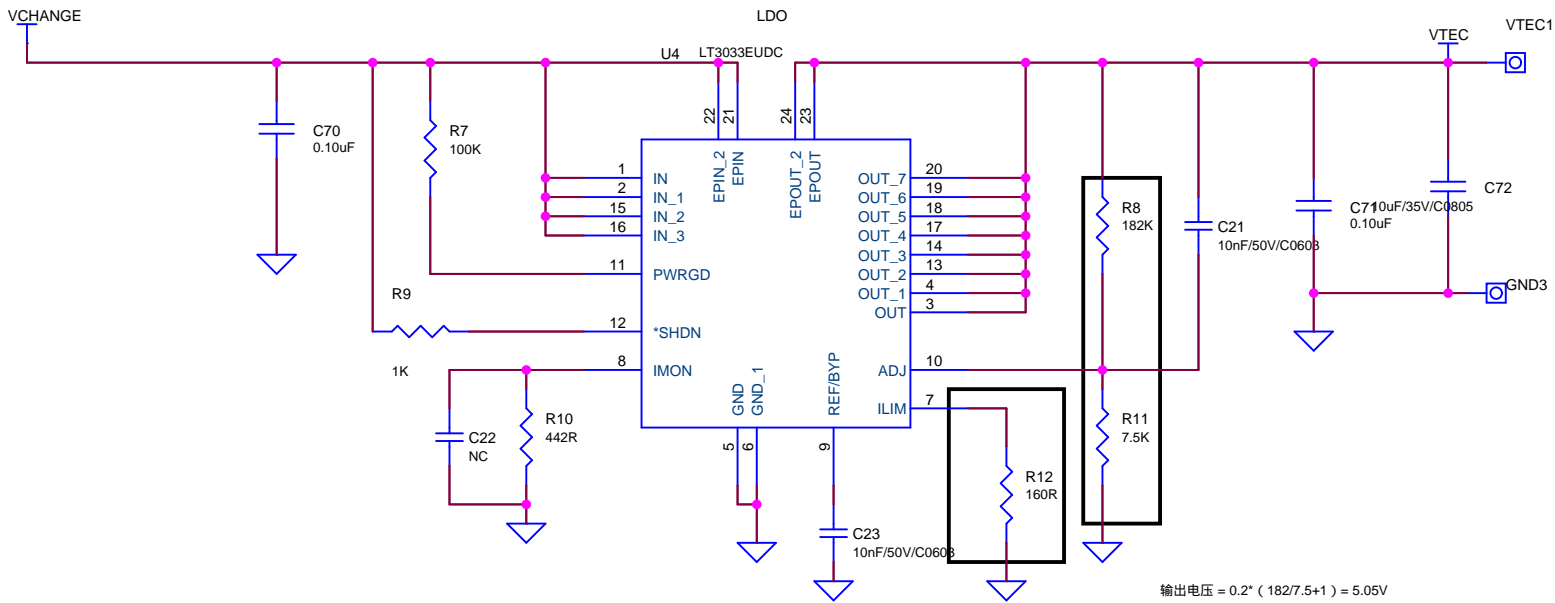
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5V系统时

U2不焊 R3,R4不焊 U2的3脚和2、4脚短接

R1修改为10k R2修改为3.6k
R5修改为30k R6修改为10.2k



R53修改为0R

该引脚电阻用于电流的限制
若不使用，设置为0R;

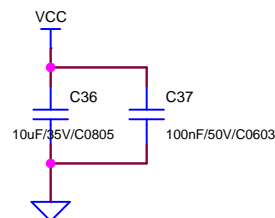
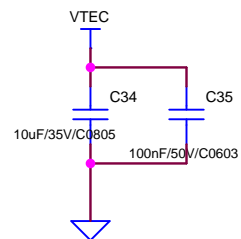
设置公示为：
 $2650 \times (0.198/Il_{lim}) - 14R$

经验值为：
510R 对应1A
160R 对应3A

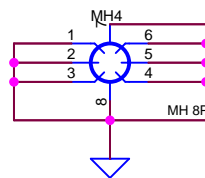
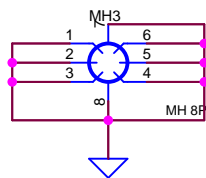
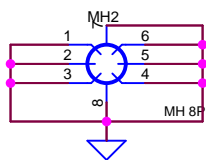
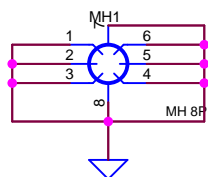
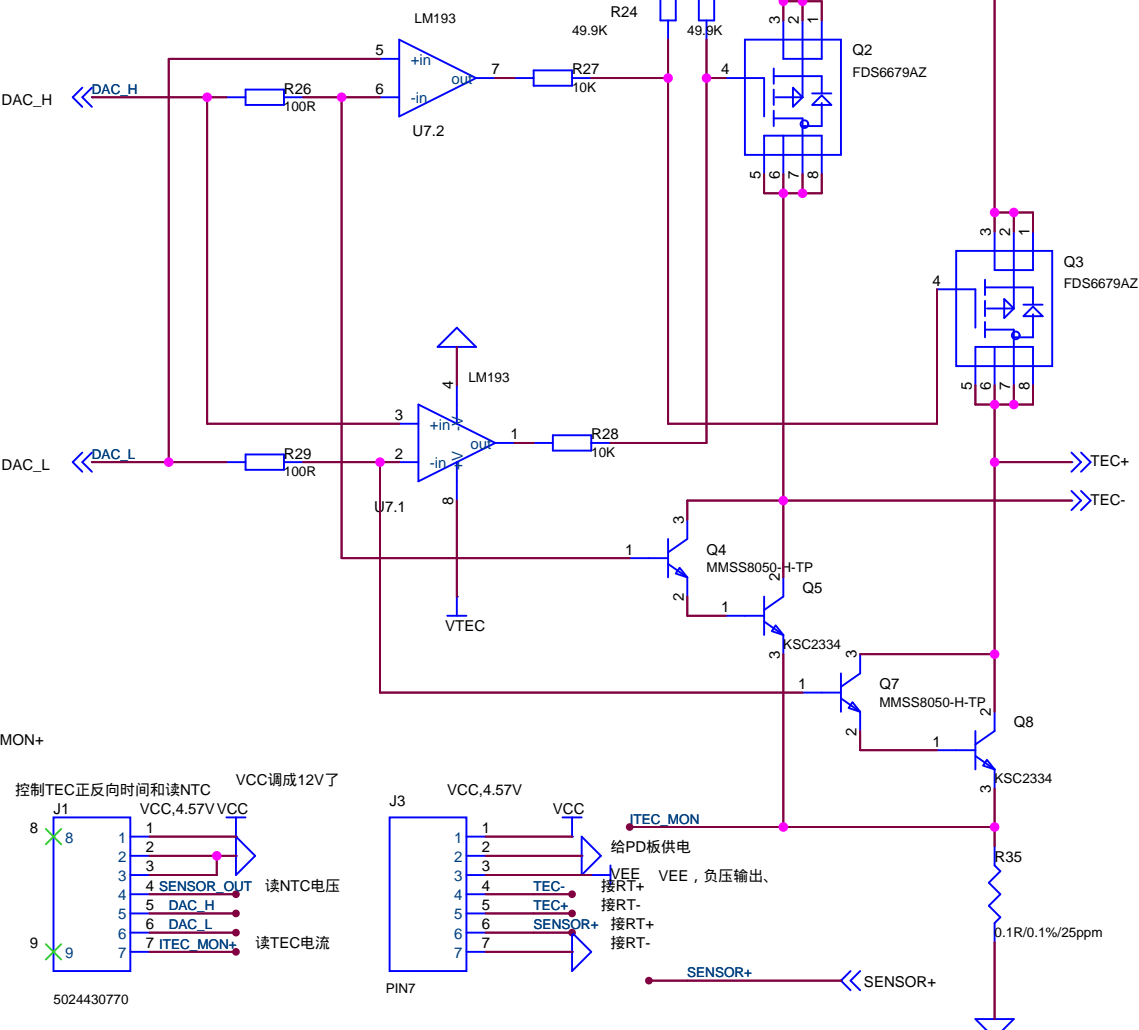
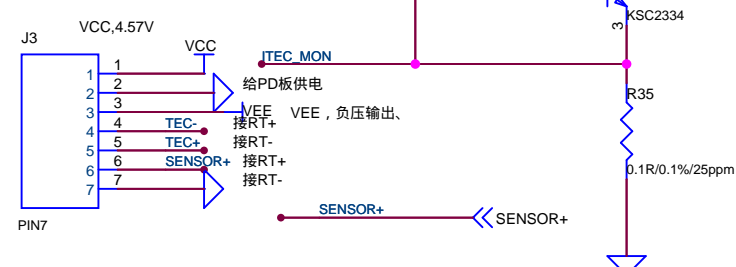
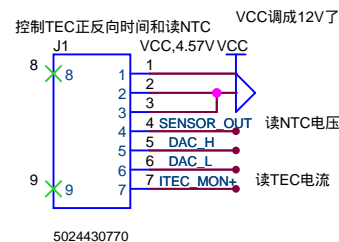
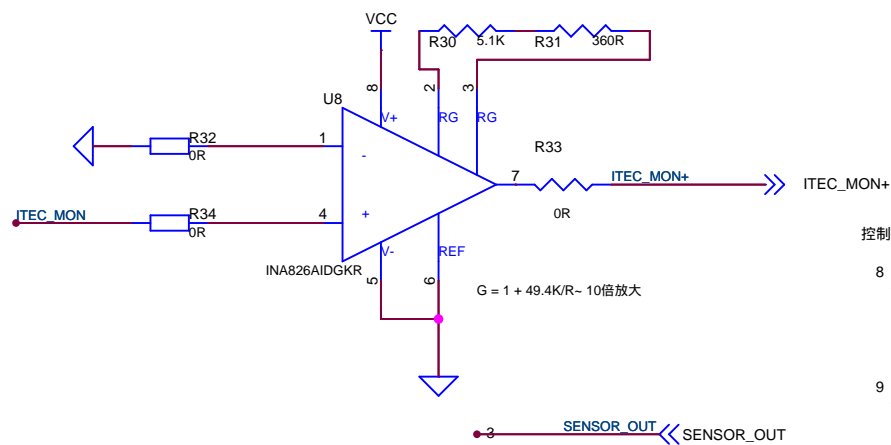
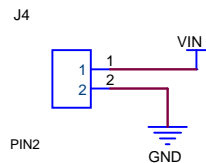
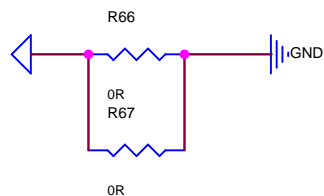
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DAC_H DAC_L
1 0 TEC+~TEC- 降温
0 1 TEC- ~TEC+ 升温
换向时，要先切换为0，0



假设放大倍数250*40
理论上DAC_L, H最大2.048V, 即可最大电流5A.

实际1.3V开启, 3.9V时, 对应4A

故DAC最大电压需要4.096V

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