

# Microcomputer Design

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## Design Report #7

1. Total time spent this week: 8 hours
2. Accomplishments
  - a. I finished soldering the board and started troubleshooting/testing continuity
  - b. I completed setup for a compiler to compile c to an srecord
3. Planned Work
  - a. This week I plan to continue continuity testing and start planning for the VHDL for the glue logic
  - b. Once the plan for VHDL is completed I plan to implement it and test the duart before spring break
4. Project Completion
  - a. Hardware: 90%
  - b. Software: 30%
5. Help from Lab Tech
  - a. 2/28/24 – Joao helped me with continuity testing and other board characteristics.