

Pratik Shrestha

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Education

2019 - **Ph.D. in Electrical Engineering.**

Present Drexel University, Philadelphia, Pennsylvania

Relevant Courseworks: VLSI Design, ASIC Design, Hardware Security, Machine Learning, Deep Learning

2012 - 2016 **Bachelors in Computer Engineering.**

Kathmandu University, Dhulikhel, Kavre, Nepal

Research Experience

Aug 2019 - **Graduate Researcher**, Integrated Circuits and Electronics Lab, Drexel University.

- Present
 - Graph representation learning guided electronic design automation.
 - Automated physical design (using DC/IC Compiler and PrimeTime) for large scale dataset generation at different design stages (logic synthesis, floorplan, placement, CTS, routing).
 - Designed spatial graph based neural networks to model timing and parasitic information.
 - Hardware security and sequential circuit obfuscation.
 - Developed synthesis algorithm and security metric for FSM locking achieving 8.53x increase in security.

Professional Experience

Jan 2018 - **Engineering Consultant**, Leapfrog Technologies.

- Jun 2019
 - Ed-Fi Alliance Analytics Middleware:** Rapid synthesis and reporting of Texas school district data across rosters, transcripts, assessments, college applications, etc.
 - Developed python modules for data extraction, parsing, pipeline, storage, report generation.
 - Created RESTful API for client-end applications and statistical analysis tool to monitor server traffic.

Jun 2017 - **Software Engineer**, Beta Analytics.

- Jun 2019
 - NEPSEguide:** Pricing and risk analytic tools to support financial risk management.
 - Designed data entry, normalization, and verification systems supporting 30+ remote data entry workers.
 - Designed data warehouse schema to support historical pricing data for Nepal stock exchange.
 - Implemented statistical metrics for predictive analyses. Developed stock screener and charting tools to analyze asset performance.

Nov 2016 - **Associate Developer**, Logic Information Systems.

- Jun 2017
 - Implemented Oracle Retail Analytics Data-warehousing architecture and develop BI solutions.
 - Provided 24-hr technical support and consulted offshore clients on ETL batches.

Jun 2016 - **Software Development Intern**, Logic Information Systems.

- Oct 2016
 - Migrated ETL tool Oracle Data Integrator's (ODI) knowledge modules from Oracle to Teradata.

Technical Proficiency

Languages Python, Verilog, VHDL, Bash, SQL (Expertise)

R, Matlab, Java, Javascript, VBScript, C++, C (Working Knowledge)

EDA Synopsys: DC Compiler, IC Compiler, PrimeTime; Cadence: Virtuoso

Data/ML Pandas, Tensorboard, Statsmodel, Scikit-learn, Torch, Torch-geometric, NLTK, Gensim

Databases PostgreSQL, MongoDB, MS-SQL, Oracle, Teradata

Frameworks Docker, SQLAlchemy, Scrapy, Airflow, Selenium, Flask, Django, Falcon

Awards

2014 **NCELL App Camp 2014 Category Winner (Business Category).**

- Presented Opinio mobile app (aggregated analysis and visualization of social media mentions).
- Nationwide competition with 150+ participating teams.

2014 **Startup Weekend Kathmandu 2014 Winner.**

- Presented Opinio search (social media mention search and natural language analysis).