ARJUN BHARADWAJ

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EDUCATION

University of California, Davis – Davis, CA
Master of Science in Computer Science (CS) (Sept 2015 – Jun 2017) (Enrolled)

University of California, Davis – Davis, CA
Bachelor of Science in Computer Science and Engineering (CSE) (Sept 2012 – Jun 2015)

SKILLS

Programming Languages: Ruby, Python, C/C++, Java
Web Dev. Languages: HTML, CSS, JS, JQuery (Client-Side), Ruby on Rails (Server-Side)
Networking Concepts: OSI Model, TCP/IP, Networking Protocols, REST
Version Control: Git
Operating Systems: Linux, Windows
Database Query Languages: MySQL, PostgreSQL, Oracle SQL
IDE: Eclipse, X-Code, Android Studio, Visual Studio

WORK HISTORY

- **Android Developer Intern, The Creative App Company, Davis, CA Apr 2013 – Jun 2013**
  - Implemented a professional application on the Android platform using Java and XML in a span of 2 months.
  - A large amount of JSON Data was successfully parsed for the functionality of the application.
  - Actively interacted with a team of five people to successfully develop the product.
- **IPhone Enterprise App Developer Intern, VSSOD Corporation, San Jose, CA Jun 2011 – Aug 2011**
  - Implemented a fully-fledged enterprise application on the I-Phone and I-Pad platforms in a period of 3 months.
  - Added functionality for the app to extract enterprise data from SAP servers and display the results in the form of analytical transaction reports such as pie charts and bar graphs.
  - Constantly communicated with a group of ten people to require ideas and implement a robust app.

RELEVANT PROJECTS

- **Bluetooth Connectivity Project (Mar 2015 – Jun 2015)**
  - Successfully wrote an embedded C program to connect one microcontroller board to another board using embedded C. This connection was established via Bluetooth (BT) and required the use of BT libraries provided by the WyzBee manufacturer.
- **CSMA/CA Simulation (Jan 2015 – Mar 2015)**
  - Programmed a collision simulation in Python. The simulation represented real-world scenarios with varying incoming and outgoing request rates.
- **Multi-Cycle CPU and Pipelined CPU Simulation (Jan 2014 – Mar 2014)**
  - Successfully simulated two central processing units (One multi-cycle and other pipelined) using the Logisim Software. The CPU's were then tested with small pieces of MIPS assembly code.
- **MINIX Operating Systems Project (Sept 2013 – Dec 2013)**
  - Tweaked the MINIX operating system to accomplish certain tasks. These tasks included changing the process scheduling algorithms, modifying the ttyl drivers, etc.