

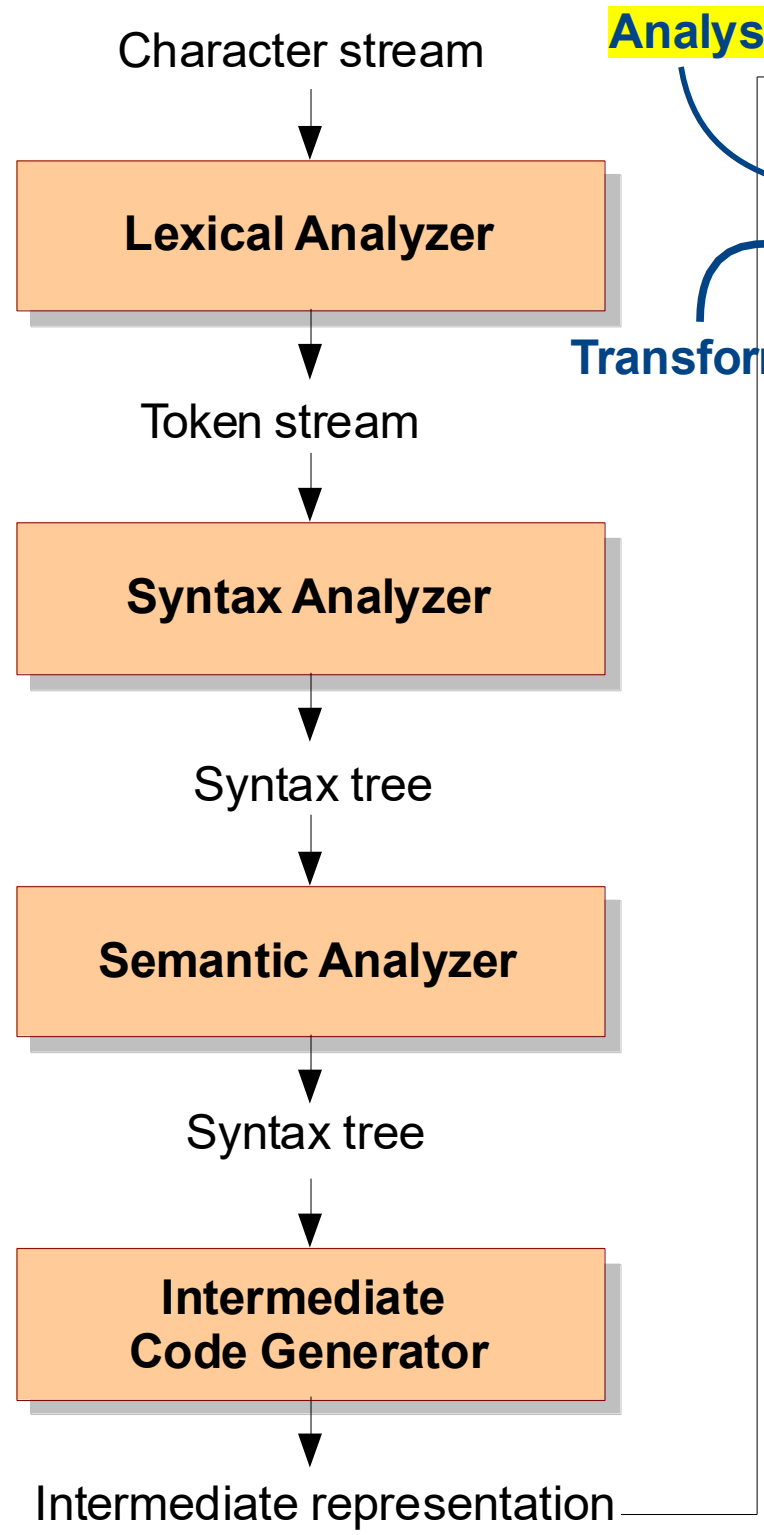
# CS202: Software Tools and Techniques for CSE

## Lecture 7

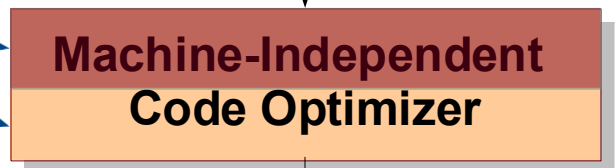
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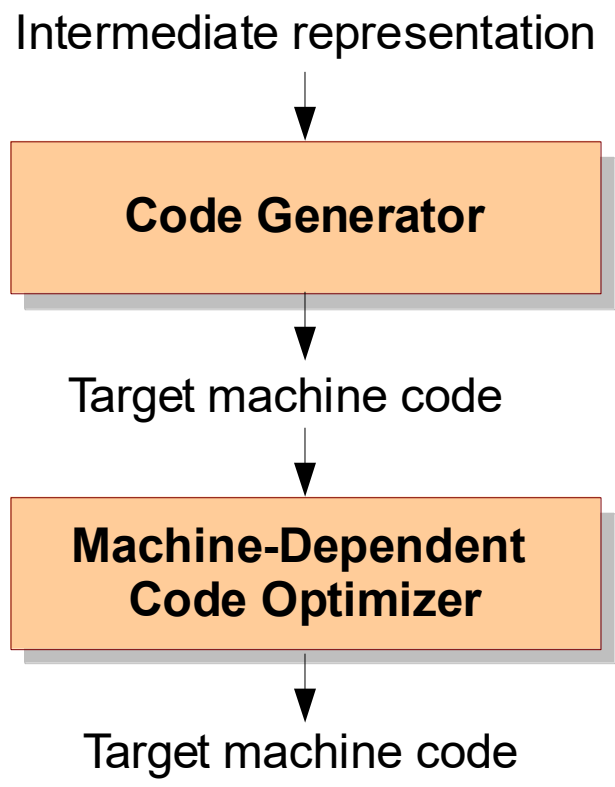
Frontend



Analysis



Transformation

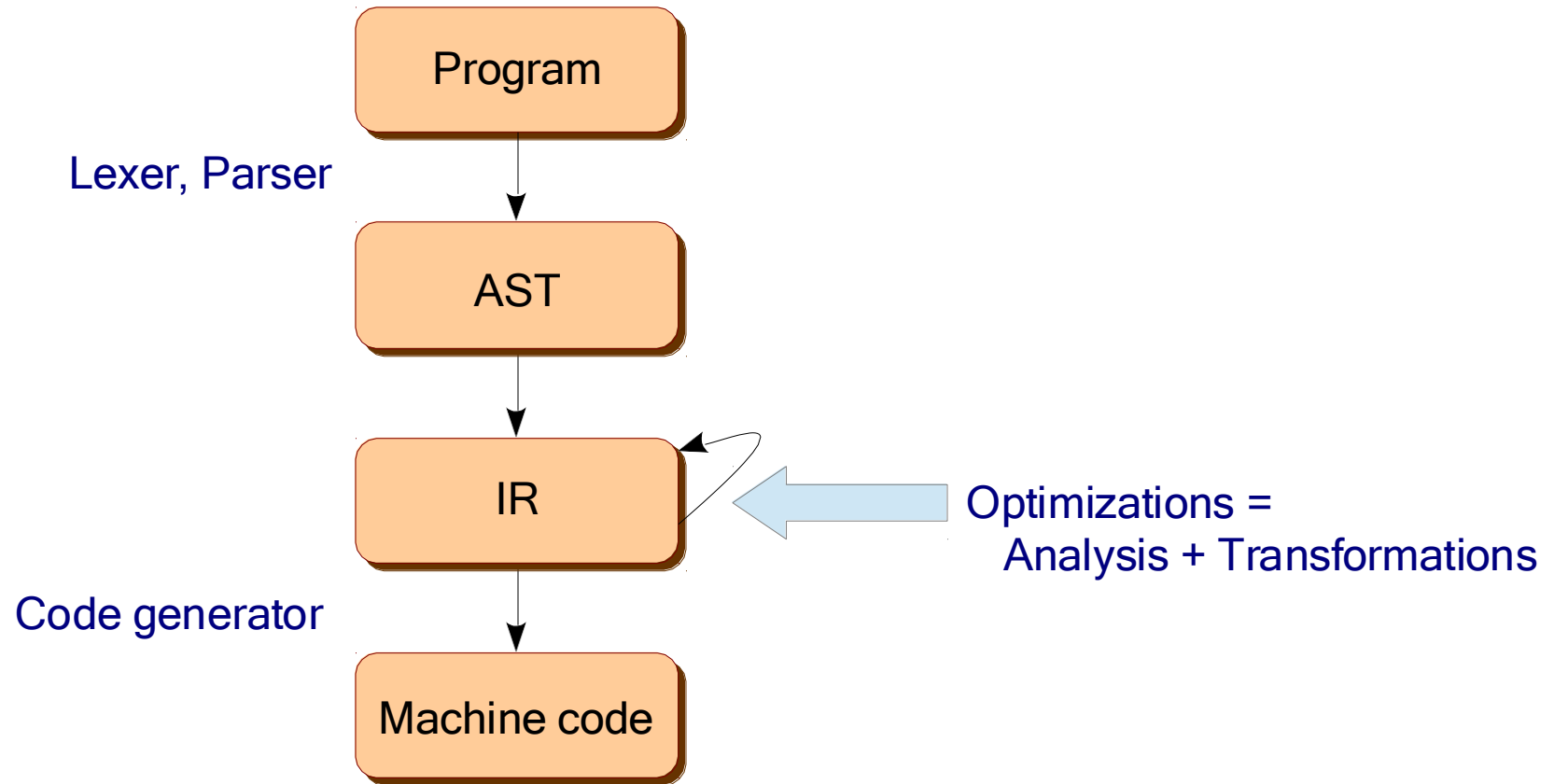


Backend

But remember that Analysis can be done on source, AST or machine code also.

Symbol Table

# Compiler Organization



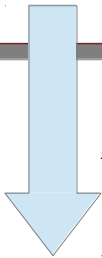
# Why are we doing this?

- To apply data-flow analysis and its variants on input programs and collect relevant information
  - reaching definitions, points-to information, etc.
- To design and implement analyses for new problems

# Example

```
void main() {  
    int a, b, c, d, *p;  
  
    p = &a;  
    c = a + b;  
    d = *p + b;  
}
```

Can this computation be avoided?  
(*common subexpression elimination*)



```
void main() {  
    int a, b, c, d, *p;  
  
    p = &a;  
    int t = a + b;  
    c = t;  
    d = t;  
}
```

This requires a program analysis  
called *pointer analysis*.

This requires another analysis  
called *type analysis*.

# What is Pointer Analysis?

```
a = &x;
```

```
b = a;
```

```
if (b == *p) {
```

```
    ...
```

```
} else {
```

```
    ...
```

```
}
```

# What is Points-to Analysis?

```
a = &x;
```



a points to x

```
b = a;
```

```
if (b == *p) {
```

```
...
```

```
} else {
```

```
...
```

```
}
```

# What is Points-to Analysis?

`a = &x;`

a points to x

`b = a;`

a and b are aliases

`if (b == *p) {`

`...`

`} else {`

`...`

`}`



# What is Points-to Analysis?

```
a = &x;
```

a points to x

```
b = a;
```

a and b are aliases

```
if (b == *p) {  
    ...  
} else {  
    ...  
}
```

Is this condition always satisfied?

Points-to information

```
a → {x, y}  
b → {y, z}  
c → {z}
```

Aliasing information

	a	b	c
a	--	Yes	No
b	--	--	Yes
c	--	--	--

Pointer Analysis is a mechanism to **statically** find out run-time values of a pointer.

# Why Points-to Analysis?

- for Parallelization
  - ♦ `fun(p) || fun(q)`
- for Optimization
  - ♦ `a = p + 2;`
  - ♦ `b = q + 2;`
- for Bug-Finding
- for Program Understanding
- ...



**Clients of  
Points-to Analysis**

# Compiler Basics

- Program as Data
- Control-Flow Graph (CFG)
- Basic Blocks
- Optimizations

- *gcc -O2 prog.c*

```
int main() {  
  int x = 1;  
  if (x > 0)  
    ++x;  
  else  
    x = 100;  
  printf("%d\n", x);  
}
```



```
int main() {  
  int x = 1;  
  if (1 > 0)  
    ++x;  
  else  
    x = 100;  
  printf("%d\n", x);  
}
```



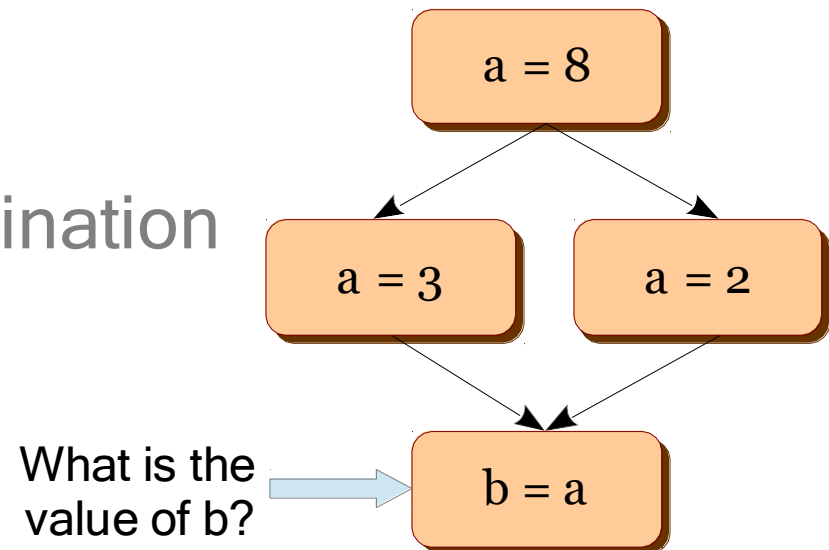
```
int main() {  
  int x = 1;  
  ++x;  
  printf("%d\n", x);  
}
```



```
int main() {  
  printf("%d\n", 2);  
}
```

# Data Flow Analysis

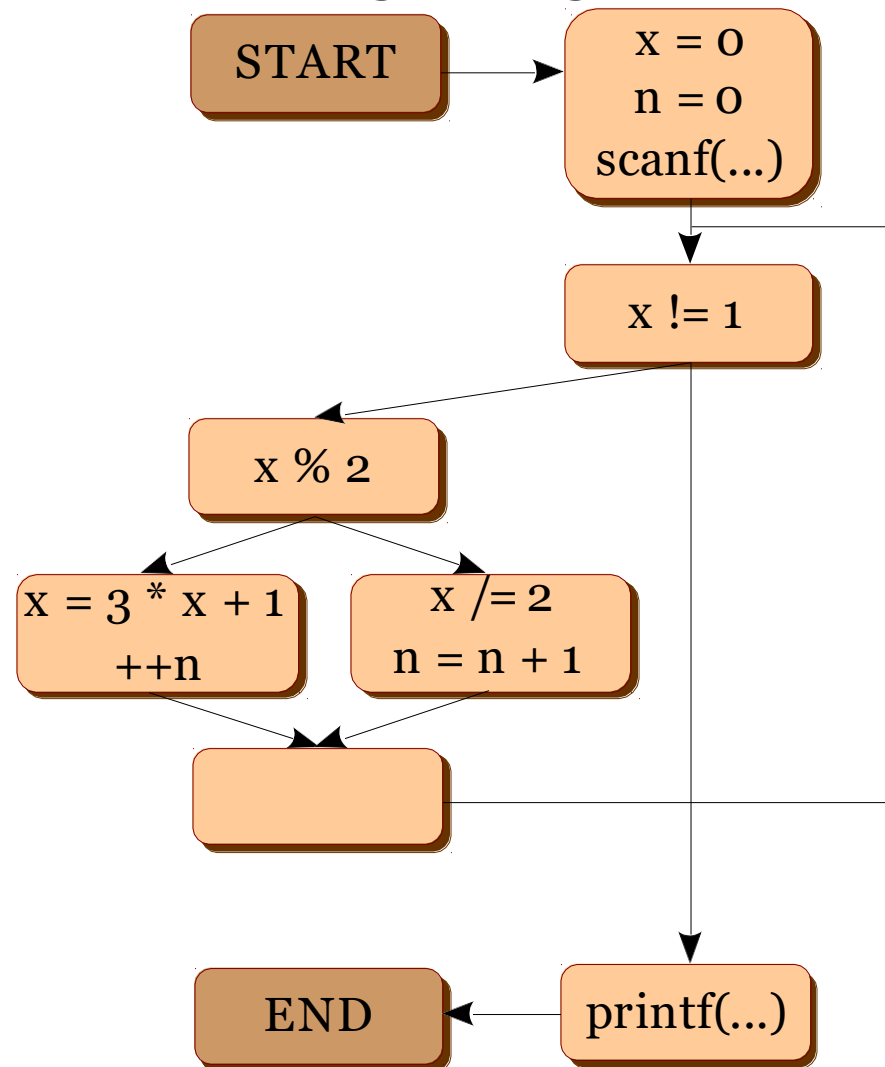
- Flow-sensitive: Considers the control-flow in a function
- Operates on a flow-graph with nodes as basic-blocks and edges as the control-flow
- Examples
  - Constant propagation
  - Common subexpression elimination
  - Dead code elimination



# Example: Control Flow Graph (CFG)

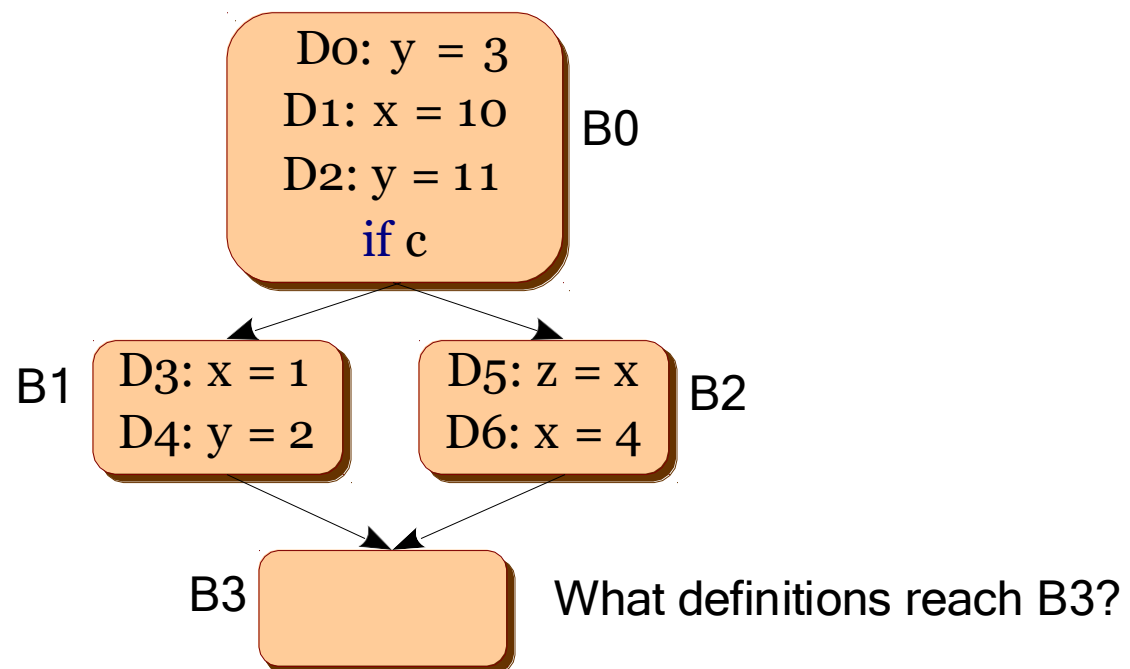
- Draw the CFG for the following program.

```
int main() {  
    int x = 0, n = 0;  
    scanf("%d", &x);  
  
    while (x != 1) {  
        if (x % 2) {  
            x = 3 * x + 1;  
            ++n;  
        } else {  
            x /= 2;  
            n = n + 1;  
        }  
    }  
    printf("%d\n", n);  
}
```



# Reaching Definitions

- Every assignment is a definition
- A **definition** *d* **reaches** a program point *p* if there exists a path from the point immediately following *d* to *p* such that *d* is **not killed** along the path.



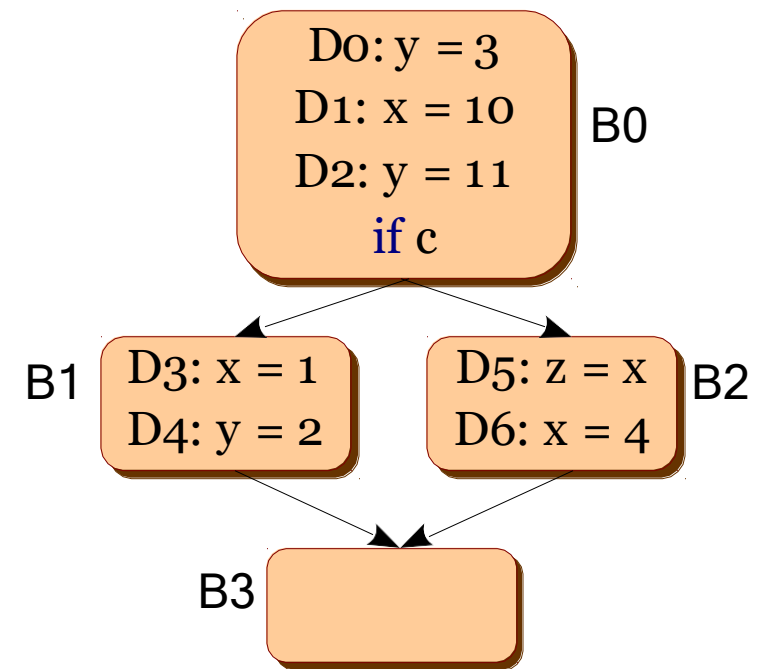
# DFA Equations

- $\text{in}(B)$  = set of data flow facts **entering** block  $B$
- $\text{out}(B) = \dots$
- $\text{gen}(B)$  = set of data flow facts **generated in**  $B$
- $\text{kill}(B)$  = set of data flow facts **from the other blocks** killed in  $B$

# DFA for Reaching Definitions

- $\text{in}(B) = \bigcup \text{out}(P)$  where  $P$  is a predecessor of  $B$
- $\text{out}(B) = \text{gen}(B) \cup (\text{in}(B) - \text{kill}(B))$
- Initially,  $\text{out}(B) = \{ \}$

$\text{gen}(B_0) = \{D_1, D_2\}$      $\text{kill}(B_0) = \{D_3, D_4, D_6\}$   
 $\text{gen}(B_1) = \{D_3, D_4\}$      $\text{kill}(B_1) = \{D_0, D_1, D_2, D_6\}$   
 $\text{gen}(B_2) = \{D_5, D_6\}$      $\text{kill}(B_2) = \{D_1, D_3\}$   
 $\text{gen}(B_3) = \{ \}$              $\text{kill}(B_3) = \{ \}$



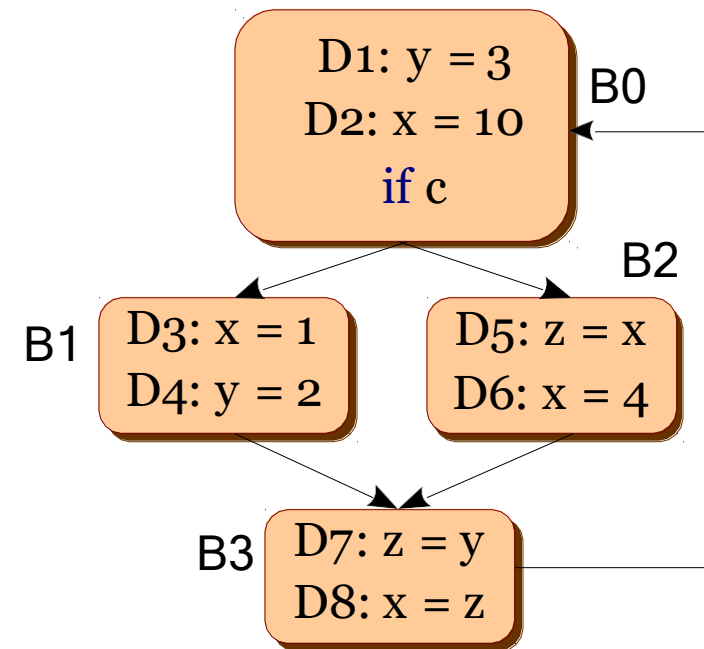
	in1	out1	in2	out2	in3	out3
B0	{ }	{D1, D2}	{ }	{D1, D2}	{ }	{D1, D2}
B1	{ }	{D3, D4}	{D1, D2}	{D3, D4}	{D1, D2}	{D3, D4}
B2	{ }	{D5, D6}	{D1, D2}	{D2, D5, D6}	{D1, D2}	{D2, D5, D6}
B3	{ }	{ }	{D3, D4, D5, D6}	{D3, D4, D5, D6}	{D2, D3, D4, D5, D6}	{D2, D3, D4, D5, D6}



# DFA for Reaching Definitions

- $\text{in}(B) = \bigcup \text{out}(P)$  where  $P$  is a predecessor of  $B$
- $\text{out}(B) = \text{gen}(B) \cup (\text{in}(B) - \text{kill}(B))$
- Initially,  $\text{out}(B) = \{ \}$

$\text{gen}(B_0) = \{D_1, D_2\}$      $\text{kill}(B_0) = \{D_3, D_4, D_6, D_8\}$   
 $\text{gen}(B_1) = \{D_3, D_4\}$      $\text{kill}(B_1) = \{D_1, D_2, D_6, D_8\}$   
 $\text{gen}(B_2) = \{D_5, D_6\}$      $\text{kill}(B_2) = \{D_2, D_3, D_7, D_8\}$   
 $\text{gen}(B_3) = \{D_7, D_8\}$      $\text{kill}(B_3) = \{D_2, D_3, D_5, D_6\}$



	in1	out1	in2	out2	in3	out3	in4	out4
B0	{ }	{D1, D2}	{D7, D8}	{D1, D2, D7}	{D4, D7, D8}	{D1, D2, D7}	{D1,4,7, 8}	{D1,2,7}
B1	{ }	{D3, D4}	{D1, D2}	{D3, D4}	{D1, D2, D7}	{D3, D4, D7}	{D1,2,7}	{D3,4,7}
B2	{ }	{D5, D6}	{D1, D2}	{D1, D5, D6}	{D1, D2, D7}	{D1, D5, D6}	{D1,2,7}	{D1,5,6}
B3	{ }	{D7, D8}	{D3,4,5,6}	{D4,7,8}	{D1, D3, D4, D5, D6}	{D1,4, 7, 8}	{D1,3,4,5,6,7}	{D1,4,7,8}

# Algorithm for Reaching Definitions

**for each** basic block  $B$

compute  $\text{gen}(B)$  and  $\text{kill}(B)$

$\text{out}(B) = \{ \}$

**do** {

**for each** basic block  $B$

$\text{in}(B) = \bigcup \text{out}(P)$  where  $P \in \text{pred}(B)$

$\text{out}(B) = \text{gen}(B) \cup (\text{in}(B) - \text{kill}(B))$

} **while**  $\text{in}(B)$  changes for any basic block  $B$

# DFA for Reaching Definitions

<b>Domain</b>	Sets of definitions
<b>Transfer function</b>	$\text{in}(B) = \bigcup \text{out}(P)$ $\text{out}(B) = \text{gen}(B) \cup (\text{in}(B) \setminus \text{kill}(B))$
<b>Direction</b>	Forward
<b>Meet / confluence operator</b>	$\cup$
<b>Initialization</b>	$\text{out}(B) = \{ \}$

# Memory Optimization

- Reuse memory / register wherever possible.
- z and y can reuse memory / register.

## Live Ranges

x : (0,1), (3,4,5), (0,1,6,7,8)

y : (4,5,10), (7,8,9,10)

z : (0,1,2), (0,1,6,7)

```
0 int x = 2, y = 3, z = 1;
1 if (x == 2) {
2     y = z;
3     x = 9;
4     y = 7;
5     x = x - y;
6 } else {
7     y = x + z;
8     ++x;
9 }
10 printf("%d", y);
```

**This optimization demands computation of live variables.**