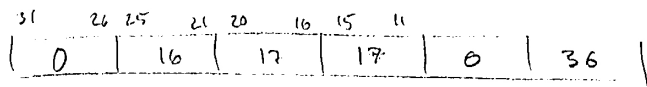
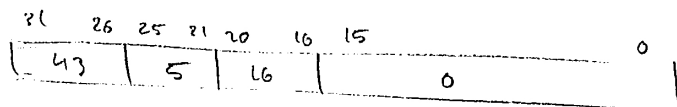


1. and \$s0, \$s0, \$s1, R-type: opcode = 0, funct = 36



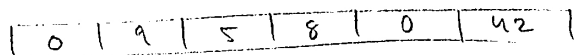
RegDst = 1 → A3 = 17

sw \$s0, 0(\$a1), I-type: opcode = 43, funct = 6

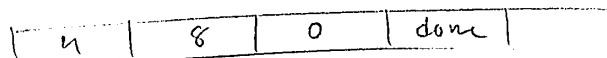


	RegDst	RegWrite	ALUSrc	Branch	MemWrite	MemToReg	Jump	ALUControl
and	1	1	0	0	0	0	0	000
sw	?	0	1	0	1	?	0	010
j	?	0	?	?	0	?	1	???

2. slt \$b0, \$b1, \$a0



beq \$b0, \$0, done, I-type: opcode = 4, funct = 6



1 2 3 4 5 6 7 8

slt unknown \$a0 0x0000402a \$b1 0x8 0x5 0x8 0x0

beq unknown 0x0 0x00000007 \$b0 0x0 0x0 0x7 0x1

