

Unit - I

3. Instruction Sets: Characteristics and Functions

Chapter 12

Sections

12.1 and 12.2 (Page 412 to 422)

12.4 (Page 425 to 438)

Addressing Modes and Formats

Chapter 13

Sections

13.1 (Page 456 to 463)

Unit - I

2. Memory

Chapter 4

Sections

4.1 and 4.3 (Page 120 to 146)

Chapter 5

Sections

5.1 (Page 165 to 172)

Input/Output

Chapter 7

Sections

7.1 to 7.3 (Page 228 to 236)

7.4 (Page 239 to 241)

7.5 (Page 248 to 251)

Computer Arithmetic

Chapter 10

Sections

10.1 and 10.2 (Page 328 to 333)

10.3 (Page 335 to 341)

10.4 (Page 350 to 353)

10.5 (Page 358 to 362)

Digital Logic

Chapter 11

Sections

11.1 to 11.4 (Pages 372 to 405)

Unit - I

1. Basic Concepts and Computer Evolution

Chapter 1 :

Sections

- 1.1 and 1.2 (Page 1 to 8),
- 1.3 (Pages 11 to 14)
- 1.5 (Pages 29 to 33)

Performance Issues

Chapter 2

Sections

- 2.1 and 2.5 (Page 45 to 63),
- 2.6 (Pages 67,68, and 71 to 74)

A Top-Level View of Computer Function and Interconnection

Chapter 3

Sections

- 3.1 and 3.5 (Page 81 to 102)
-