Parampreet

psbhatia@uwaterloo.ca
github.com/psbhatia
linkedin.com/psbhatia26

EDUCATION

University of Waterloo
Bachelor of Applied Sciences, Computer Engineering — Term 3B

EXPERIENCE

Buckzy Payments — Sep 2020 – Dec 2020
Backend Developer — Toronto, ON

- Developed two new Spring Boot Microservices from ground up to integrate partner payment gateways
- Set up and deployed Elasticsearch, Logstash and Kibana logging system for multiple services
- Responsible for triaging issues across multiple services and setting up a system to alert developers when these happen using Kafka messages
- Worked on a querying service to check the status of payments with partner payment gateways

Insticator — Jan 2020 – Apr 2020
Backend Developer — Waterloo, ON

- Developed tooling in the form of a maven repository to expedite reporting of business exceptions
- Integrated multiple third party RESTful APIs to our existing Play Framework Java Backend
- Developed proof of concept for processing of exceptionally large CSV files using streaming libraries to optimize run time by over 100%
- Regularly pushed new features and bug-fixes to production environment with tight deadlines

OpenText — May 2019 – Aug 2019
Software Developer — Waterloo, ON

- Modernized legacy APIs to RESTful ones so they could be integrated with our new web application
- Improved page render times by optimizing existing API calls
- Implemented complex SQL queries on very large datasets with a focus on efficiency
- Increased test coverage by 20% by structuring and implementing a new unit testing library

PROJECTS

Image Repository
Created a Spring Boot application to upload and query images hosted on AWS

Database Systems Project
Designed a MySQL database, interfaced with Python for an internet traffic dataset from Kaggle

Operating Systems Project
Designed and implemented a small Real-Time Executive for an ARM based Intel board (C and Assembly)

Conways Game of Life
Simple implementation of Conways Game of Life as an exercise to learn Javascript

Compilers Project
Produced the components of a rudimentary compiler for a subset of VHDL