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(54) ASYNCHRONOUS CONVERSION CIRCUITRY APPARATUS, SYSTEMS, AND METHODS

(75) Inventors: Rajit Manohar, Ithaca, NY (US);
Clinton W. Kelly, San Jose, CA (US);
Virantha Ekanayake, San Jose, CA
(US); Christopher LaFrieda, Brooklyn,
NY (US); Hong Tam, San Jose, CA
(US); Ilya Ganusov, San Jose, CA (US);
Raymond Nijssen, San Jose, CA (US);
Marcel Van der Goot, Pasadena, CA
(US)

(73) Assignee: Achronix Semiconductor Corporation, Santa Clara, CA (US)

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See application file for complete search history.

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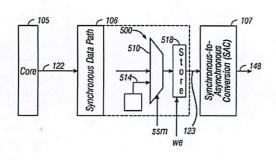
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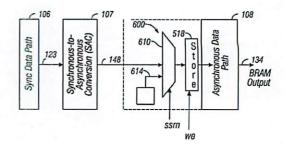
Primary Examiner — Vibol Tan (74) Attorney, Agent, or Firm — Schwegman, Lundberg & Woessner, P.A.

(57) ABSTRACT

Apparatus, systems, and methods operate to receive a sufficient number of asynchronous input tokens at the inputs of an asynchronous apparatus to conduct a specified processing operation, some of the tokens decoded to determine an operation type associated with the specified processing operation; to receive an indication that outputs of the asynchronous apparatus are ready to conduct the specified processing operation; to signal a synchronous circuit to process data included in the tokens according to the specified processing operation; and to convert synchronous outputs from the synchronous circuit into asynchronous output tokens to be provided to outputs of the asynchronous apparatus when the synchronous outputs result from the specified processing operation. Additional apparatus, systems, and methods are disclosed.

20 Claims, 9 Drawing Sheets





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