

Technology	Synopsys 32nm
Core Area	0. 898010mm × mm
Power	1171mW
Energy/Samp	1.951×10^8 pJ/Sample
ADC Range	95mV
ENOB	14bits

Design Features

- ◆ Minimized number of states and registers used
- ◆ Minimized number of latches by using “assign” statements

Technology	Synopsys 32nm
Core Area	0. 180184mm × mm
Power	232mW
Energy/Samp	3.8667×10^7 pJ/Sample
ADC Range	95mV
ENOB	14bits

Design Features

- ◆ 8-channel interleaving
- ◆ Minimized number of states and registers used
- ◆ Reduce power by more than 80% & Save area by 80%