Potential Faliure	Potential Faliure		
Mode	Effect	Severity	Potential Cause
Repeated values in	wrong data		buffer not cleared
Circular buffer	transmission	6	after transmission
Circular buffer stack	momory oxhoustion		
	memory exhaustion	0	
memory overflow	leading to hardfault	8	memory not freed
			buffer size less than
buffer overflow	part of the data lost	5	the TX RX data
	wrong data		interrupt occurs
Corrupt data	transmission	5	while in handler
UART transmission			voltage surge
faliure	Corrupted TX RX line	10	distroying the lines
	M/mama tima a atama m		into muunt diaabla
T'' F	Wrong timestamp	2	interrupt disable
Timing Error	for data received	3	stops systick timer
			Disconnection from
Countries Charles and	Data Last	7	
System Shutdown	Data Lost	/	power supply
	Uart while		Error in UART
	initialization gives		initialization
UART hardfault	hardfault	4	registers

_			Risk Priority
Occurence	Controls	Detection	Number
	clear buffer after		
9	transmission	6	324
	Destroy buffer after		
6	buffer use	4	192
	Create expandable		
4	buffer if it gets full	5	100
	Interrupt disabled in		
	critical Section of		
3	code	6	90
	Use proper		
2	electrical guidelines	3	60
	Disable interrupts in		
2	highly critical code	6	36
	keep the cables and		
	connections tight		
2	and clean	2	28
	Refer datasheet to		
	check if registers		
	have been set to		
2	right values	3	24