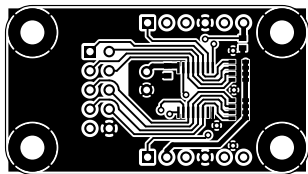
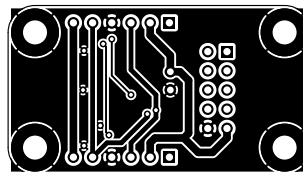


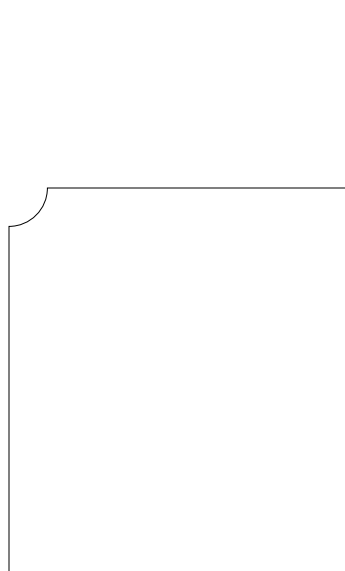
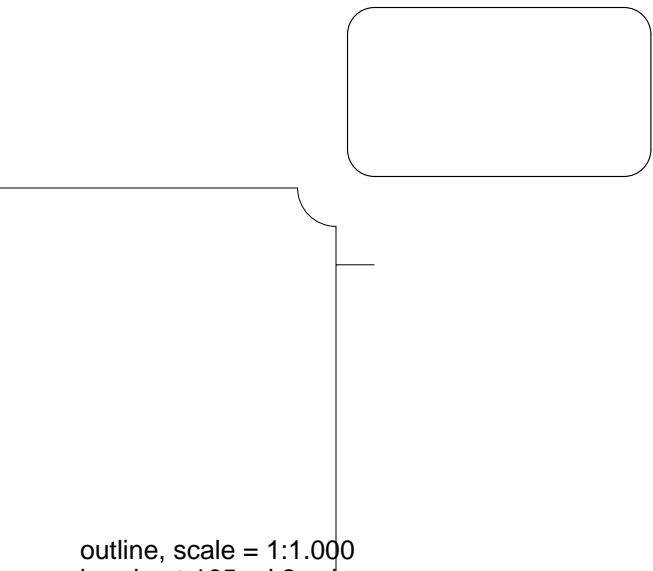
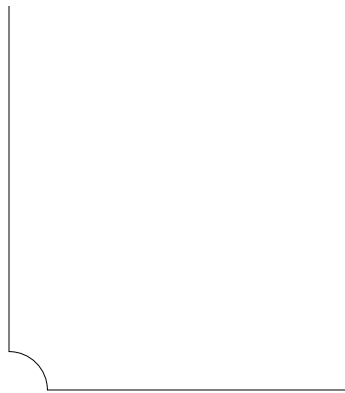
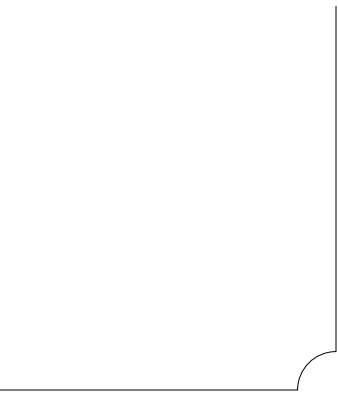
1. Table of Contents (This Page)
Created on Wed Aug 23 12:31:24 2017

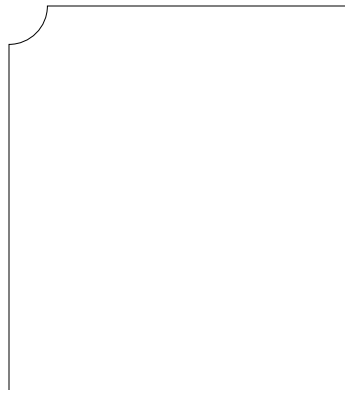
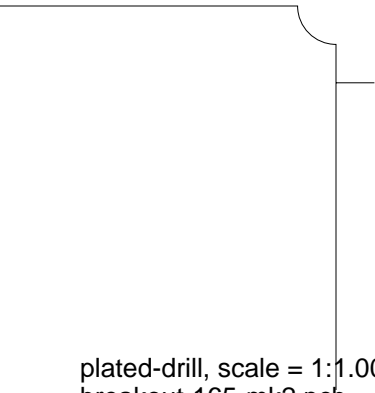
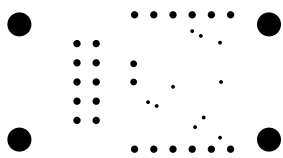
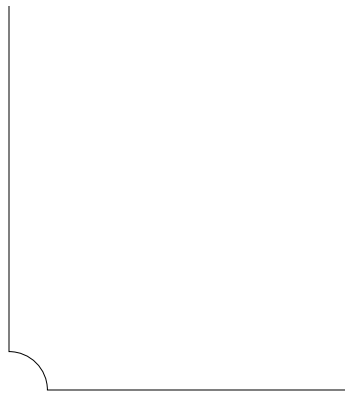
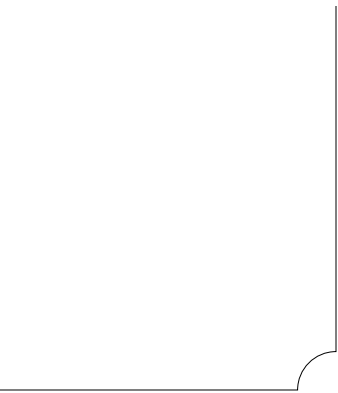
2. top
3. bottom
4. outline
5. plated-drill
6. componentmask
7. soldermask
8. topsilk
9. bottomsilk
10. toppaste
11. topassembly
12. bottomassembly
13. fab

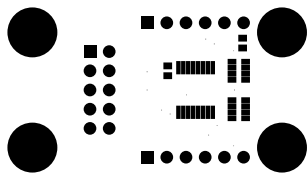


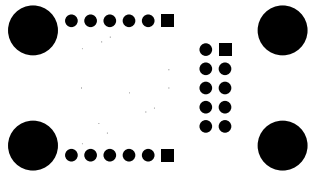
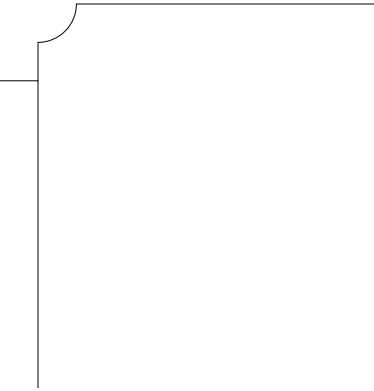
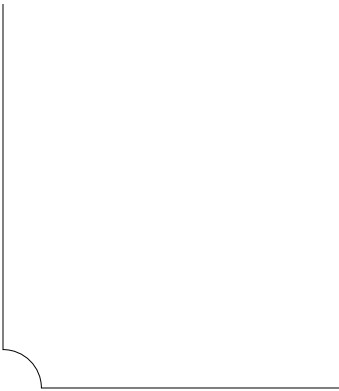
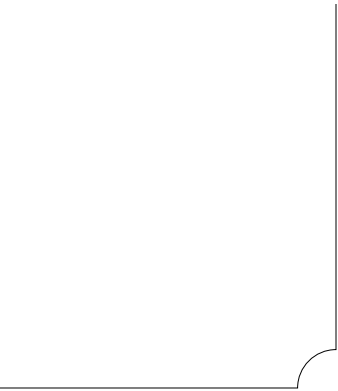


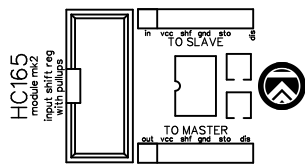
bottom (mirrored), scale = 1:1.000
breakout-165-mk2.pcb











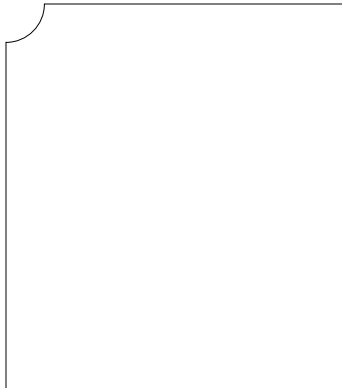
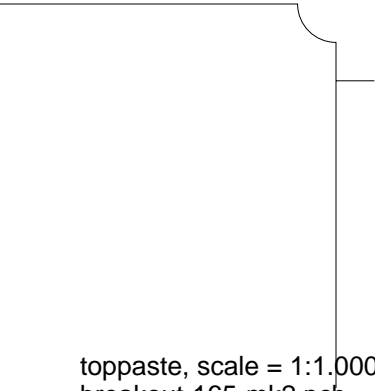
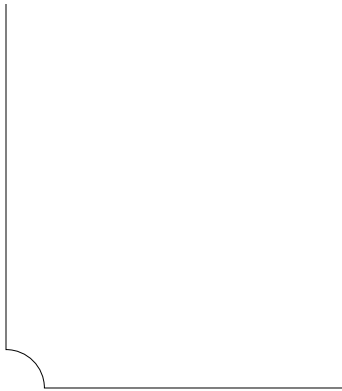
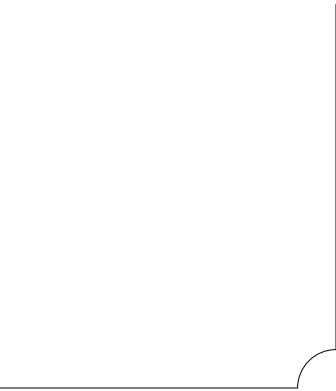
8-bit HC165
module mk 2

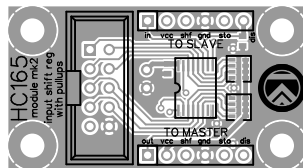
Inputs	
0	2
1	4
3	6
5	7
GND	Vcc

Ⓢ
Designed by
COCO universal
electronics.org
/zero/10

v1.0.0
2017-08-23

designed by jsmay
hw.hgkus



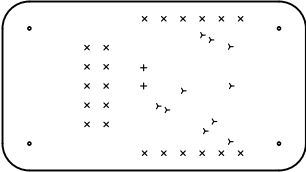




There are 4 different drill sizes used in this layout, 38 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.020	10	YES
+	0.035	2	YES
x	0.038	22	YES
•	0.125	4	YES

Title: (unknown) – Fabrication Drawing
Author: Peter S. May
Date: Wed 23 Aug 2017 04:31:24 PM GMT UTC



Board outline is the centerline of this path