

Bramha Swaroop Tripathi

Senior Project Associate
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CAREER VISION

Looking for a promising & challenging career which will enable me to provide best of my Technical, analytical & professional skills. A career which can sharpen my current skills and Knowledge and where I can have a good scope for learning and implementing new technologies.

RELEVANT EXPERIENCE

Currently working as a **Senior Project Associate** in Department of Computer Science, **Indra Gandhi Delhi Technical University Woman Delhi**, India

work on project mobile education kit (MEK2,MEK3) My smart phone Kit sponsored by Nokia University Relations, Finland and Microsoft Corporation

Key responsibilities handled:-

- Design and maintenance kit is designed to be used for engg. as a experimental environment for various courses such as Mobile Computing, Embedded System, Wireless Communications, Internet Of Things, Programming Languages like Python, C, Shell Script etc.
- Understand project specifications and client expectations related to different platform microcontroller, Arduino, raspberry pi etc.
- Organize the work shop related to 3D printing and CNC Router for PCB Design, Laser cutting Machine for staff member of different universities and students
- Assign tasks to the team, review the tasks and ensure that the deliverable are prepared to satisfy the project requirements, cost and schedule.

Role: Senior Projectionist & Electronics Technician Officer

Department of Wireless Comm., Sardar Vallabhbhai Patel National Police academy Hyderabad, India (1 year)

Key responsibilities handled:-

- Organized the Training class for IPS officer's related wireless technology and provided the training class of wireless equipment for police officers.
- Research Analysis related to cybercrime & Forensics.
- manage the Audiovisual control system programming, debugging, troubleshooting Maintenance and operation of LCD, Public address system and audio video equipment UHF & VHF, hardware of computer.

Role: Project Head & Assistant Professor

Department of Electronics & Communication Engineering, Bhopal Institute of technology Bhopal, India (4 year)

Key responsibilities handled:-

- Responsible for taking BE & M.Tech students Classes. Provided Technical Training Session to students for research project and seminar presentations.
- Working in capacity of in-charge of all software labs (Tk Gate simulator, Scilab, VHDL, Lab view, micro controller, PSpice, Orcad)
- Organize workshop of PCB designing, development, fabrication and assembling. Handling design & development of project related to VLSI design & PCB design.

TECHNICAL SKILLS

- Language : VHDL | C | Matlab | Python
- Tools : Xilinx ISE 14.1 | Solid work | LabVIEW | PSpice | K Sim | TK Gate simulator
- Hardware testing Tools: 3D printing, CNC Router, Laser Cutting machine Multi meter
- Proficient in software packages used for PCB designing like PCB Express, Eagle, OrCAD.

EDUCATIONAL QUALIFICATIONS

- **Post graduate certification in Cyber Law**
Indra Gandhi open university, Bhopal MP (IGNOU Bhopal)
- **Master of Technology in VLSI Design**
Laxmi Narayan College of Technology, Bhopal (RGPV Bhopal, Percentage – 80.40 %)
Sept, 2010 – June, 2013
- **Bachelor of Engineering in Electronics & Comm. Engineering**
Govt. Indra Gandhi Engineering College, Sagar (RGPV Bhopal, Percentage – 75.06 %)
Jul, 2006 – Jun, 2009
- **Diploma of Engineering in Electronics & Comm. Engineering**
Govt. Polytechnic College Nowgong, Chhatarpur (RGPV Bhopal, Percentage – 73.75 %)
Jul, 2003 – dec, 2005
- **Intermediate (MP Board, Percentage – 62.44 %)**
Govt. boys Higher Secondary School, Khajuraho Mar, 2003
- **High School (MP Board, Percentage – 69.20 %)**
Swami Prawnand Saraswati Shishu Mandir, Khajuraho Mar, 2001

ACADEMIC PROJECTS

VHDL IMPLEMENTATION OF MAC BASED DS SS CDMA FORWARD LINK FOR AD-HOC NETWORK

- **Details:** In this project, we focus on medium access problem, for this, we design multiple access interference (MAI) at the protocol level. The aim is that, use “VHDL implementation” for MAC Based DSSS CDMA design, which Consist transmitter & receiver with MAC protocol of a Ad-hoc network, which prevent fast degradation of Network throughput.
Platform – Xilinx 14.1 | Tools – ModelSim | Language – VHDL

OFFICE AUTO MOTION SYSTEM BASED ON TELEPHONE.

- **Details:** In this project we can control the different devices in one place by the help of Telephone System and use as security system.
Platform – PCB Design | Tools – Solder iron, Electronic Component

TROUBLE SHOOTING OF MONO CHROMES TELEVISION.

- **Details:** We can find the different loss & how to remove the loss and also know the Knowledge of various section of television kit and which type antenna are used in TV transmission & reception.
Platform – PCB Design | Tools – Solder iron, Electronic Component

DESIGN AND IMPLEMENTATION OF CRC USING VHDL.

- **Details:** This project is a demo that how the basic CRC 32 can be implemented for the detection of error in the data when it is transmitted from the transmitter to the receiver end. Therefore through the VHDL code CRC 4 is implemented successfully and it is justified by simulating the code in Model Sim Software.
Platform – Xilinx 14.1 | Tools – ModelSim | Language – VHDL

FM TRANSMITTER 100 METER RANGE.

- **Details:** This project transmitted the voice in 100 meter range with the help of frequency Modulation & knows the information how to better FM with respect to AM in voice Transmission.
Platform – PCB Design | Tools – Solder iron, Electronic Component

PUBLICATIONS

- VHDL Implementation of Mac Based DSSS-CDMA Protocol for Solving Near Far Effect In Ad-Hoc Network
- Review on DS SS-CDMA transmitter and receiver. For ad hoc network using VHDL implementation
- Review on STBC coded MISO and MIMO in frequency selective wireless fading channel
- A novel strategy for performance improvement of MISO wireless system using alamouti stbc with 4th order fir filter.
- A survey on performance analysis of different multiplexing scheme used in passive optical network system

- Mitigation of Rayleigh backscattering in wavelength division multiplexing passive optical network based on w-sask modulation
- Survey on Backhaul Rate Allocation in Uplink SC-FDMA Systems with Multicell Processing.
- High throughput in Uplink SC-FDMA Systems with Multicell Processing.

MEMBERSHIP & WORKSHOP

- Reviewer in Journal of Technological Advances and Scientific Research (JTASR)
- Reviewer in GRD Journals
- Reviewer International Journal of Scientific research in Science and Technology (IJSRST)
- International Association of Computer Science and Information Technology (IACSIT)
- Universal Association of Computer & Electronics Engineer (UACEE)
- International Association of Engineers (IAENG)

VOCATIONAL TRAINING

- **BHARAT SANCHAR NIGAM LIMITED DAMOH & CHHATARPUR** Dec,2007 –Jan,2008
Undergone summer training on field of GSM & Switching Section
- **ALL INDIA RADIO CHHATARPUR** Jul, 2007-Jul,2007
Undergone summer training on Radio transmission & reception
- **NAVIGATOR TECHNOLOGIES PVT LTD BHOPAL** Jun,2011 –Jul,2011
Undergone training on the field of VLSI Technology
- **HCL CDC BHOPAL MP** 50 Hrs, Jun,2011
Undergone training on Network terminogy and computer networking.
- **STEP IGNOU COMMUNITY COLLEGE BHOPAL** May,2011 –Jun,2011
Undergone training on the field of VLSI Technology

PERSONAL DETAILS

- Father's Name : Mr. Jai Narayan Tripathi
- Mother's Name : Smt. Rama tripathi
- Date of Birth : 03/07/1986
- Nationality : Indian
- Marital Status : Single
- Hobby : Listening to Music, Internet Surfing
- Languages Known : English, Hindi
- Present Address : H-6-114, Near to Irrigation Colony Rajnagar Distic-Chhatarpur MP -471625

DECLARATION

I hereby declare that the above given information are true to best of my knowledge.

Delhi

Date:

Bramha Swaroop Tripathi