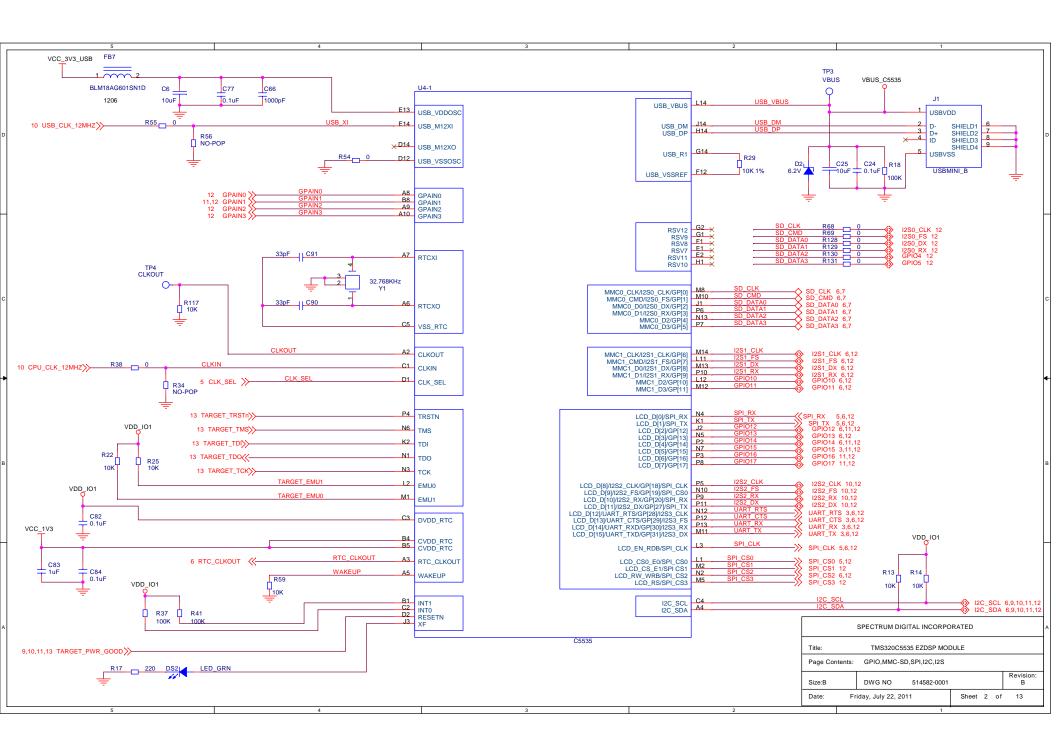
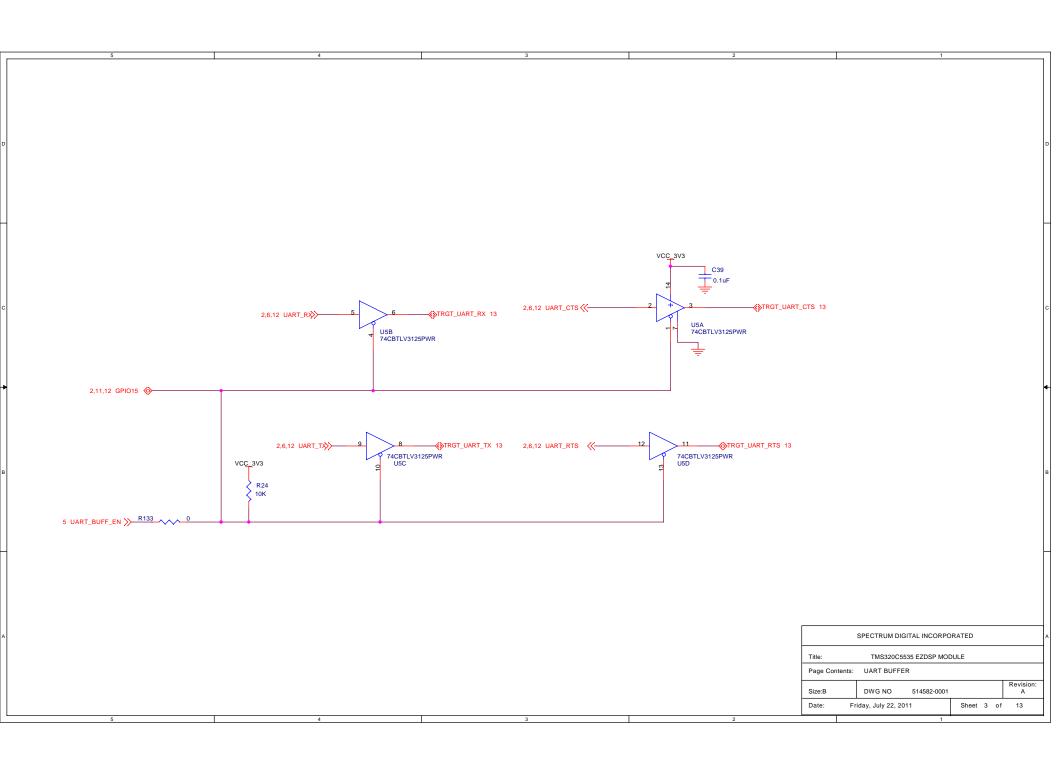
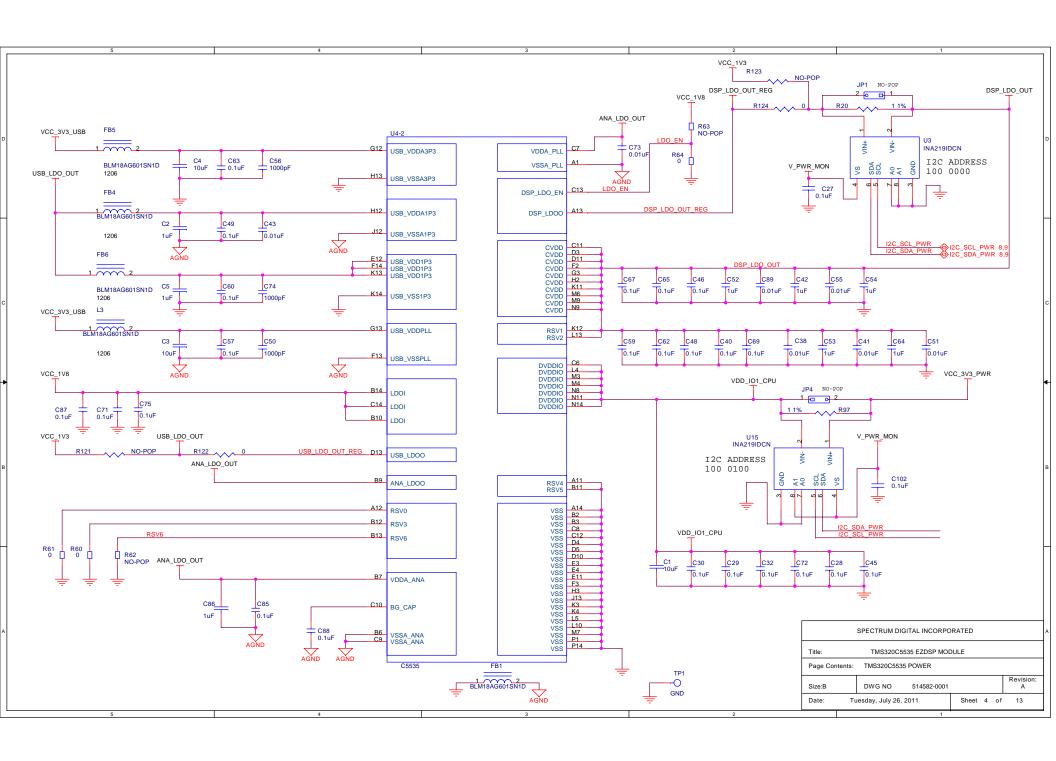
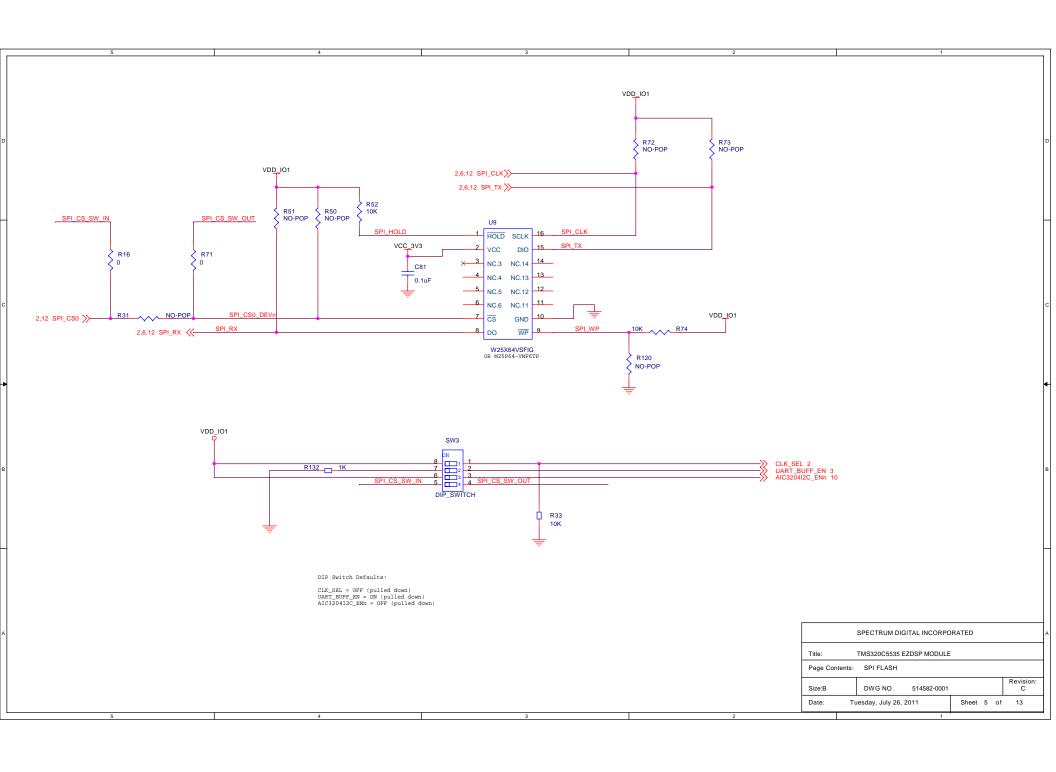
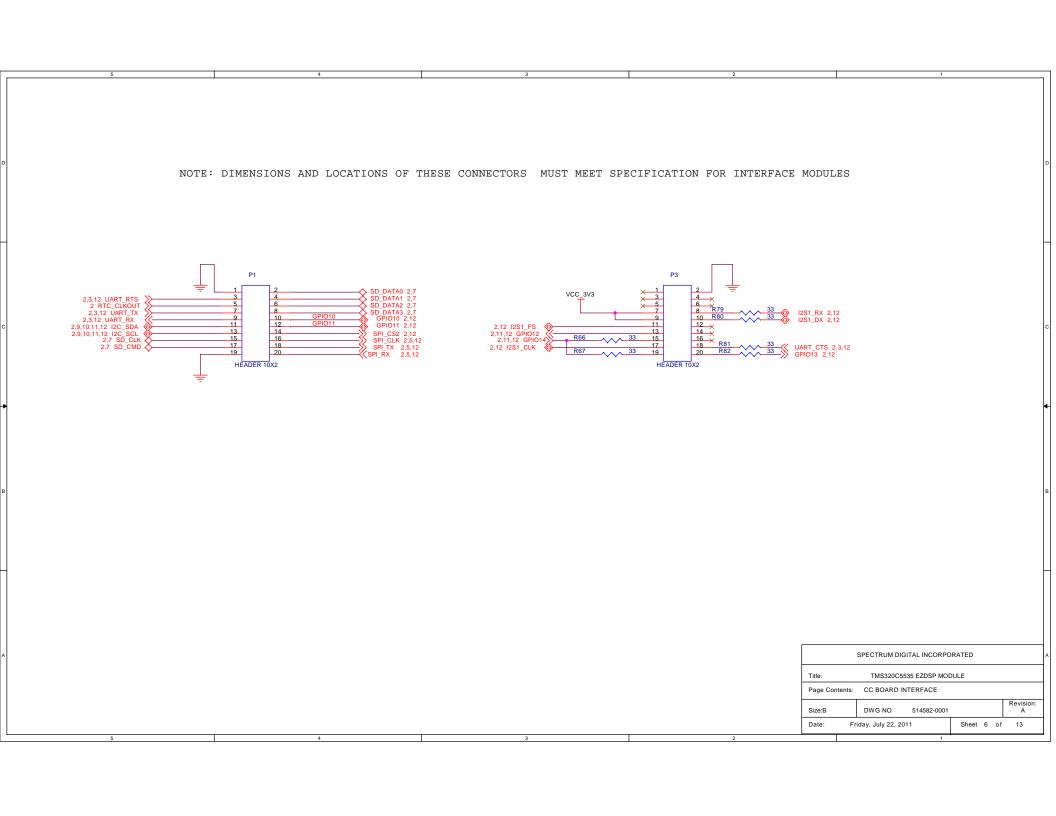
REV DESCRIPTION DATE APPROVED NOTES, UNLESS OTHERWISE SPECIFIED: 1/10/2011 1. RESISTANCE VALUES IN OHMS. A Initial schematic for layout RRP 2. CAPACTITANCE VALUES IN MICROFARADS. В BETA UPDATES 6/19/2011 RRP 3. REFERENCE DESIGNATORS USED: 4. ALL 0.1 uF AND 0.01uF CAPACITORS ARE DECOUPLING CAPS UNLESS C PRODUCTION UNITS 7/19/2011 RRP OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR. SCHEMATIC CONTENTS 01 - TITLE PAGE 02 - TMS320C5535 IO 03 - UART BUFFER 04 - TMS320C5535 POWER 05 - SPI FLASH 06 - CC BOARD INTERFACE 07 - MICRO SD INTERFACE 08 - POWER SUPPLIES 09 - AUDIO CODEC 10 - LCD/USER LEDS/SWITCHES 11 - EXPANSION CONNECTOR 12 - XDS100-v2 INTERFACE REVISION STATUS OF SHEETS REV SHEET DATE 10/15/2009 SPECTRUM DIGITAL INCORPORATED REV T.W.K. TMS320C5535 EZDSP MODULE R.R.P. 10/15/2009 Title: REV R.R.P. 10/15/2009 Page Contents: TITLE SHEET 10/15/2009 DATE 10/15/2009 SHEET C.M.D. Revision: C Size:B DWG NO 514582-0001 NEXT ASSY R.R.P. REV Date: Sheet 1 of 13 Tuesday, July 26, 2011 10/15/2009 APPLICATION

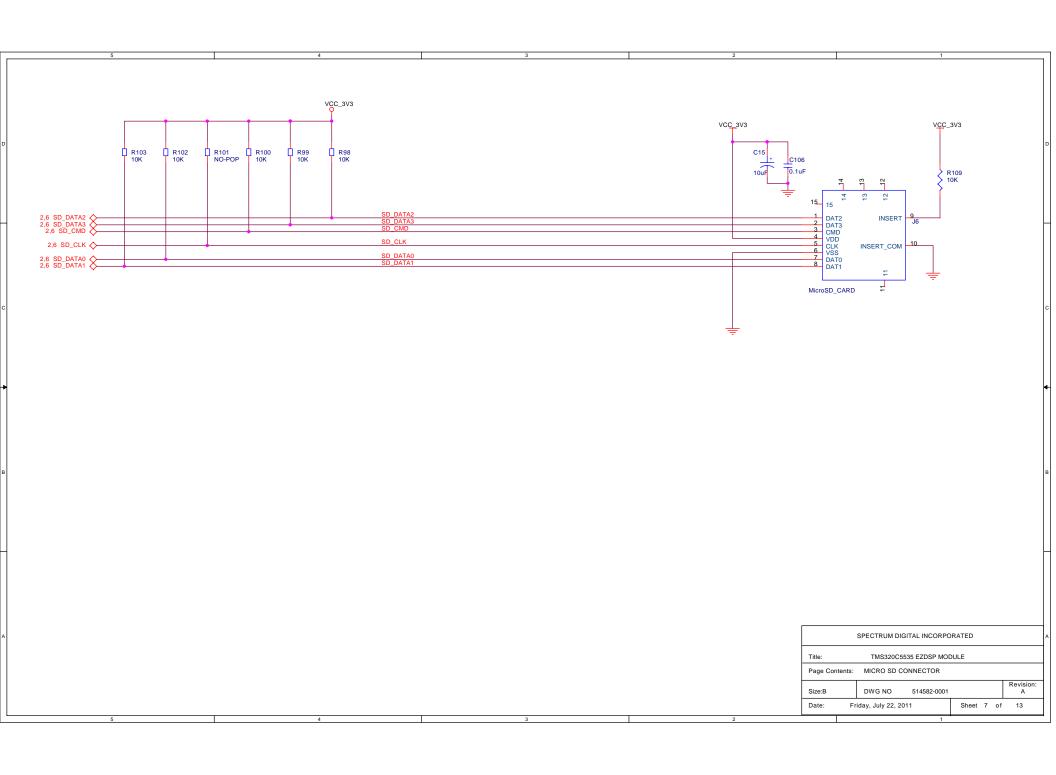


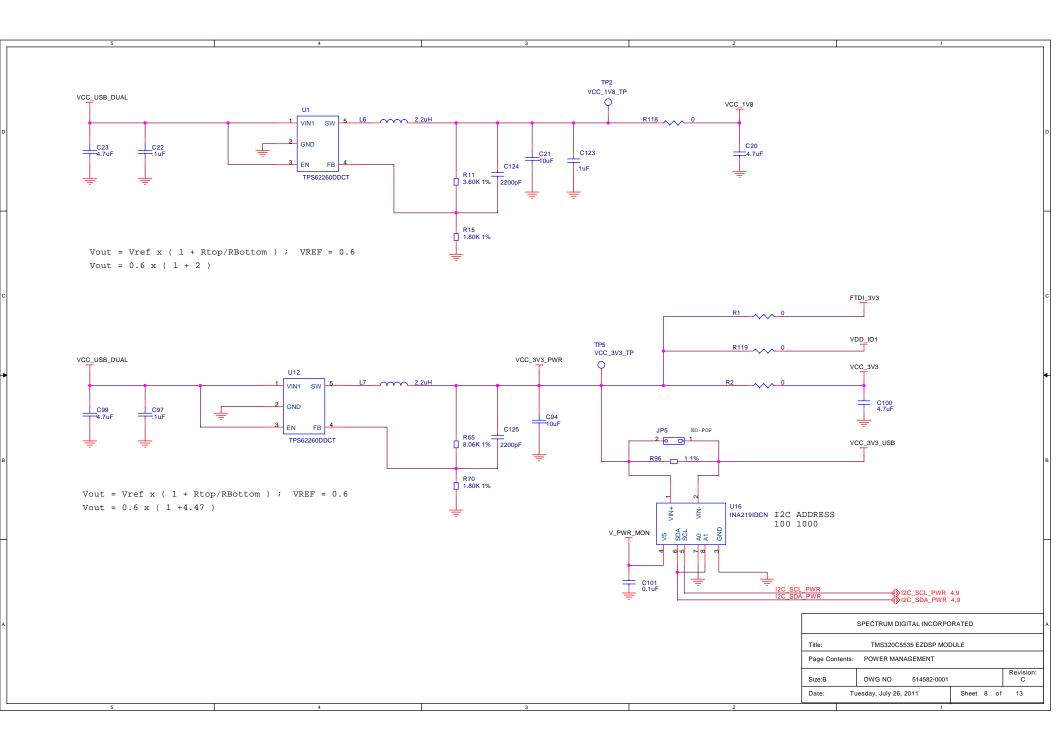


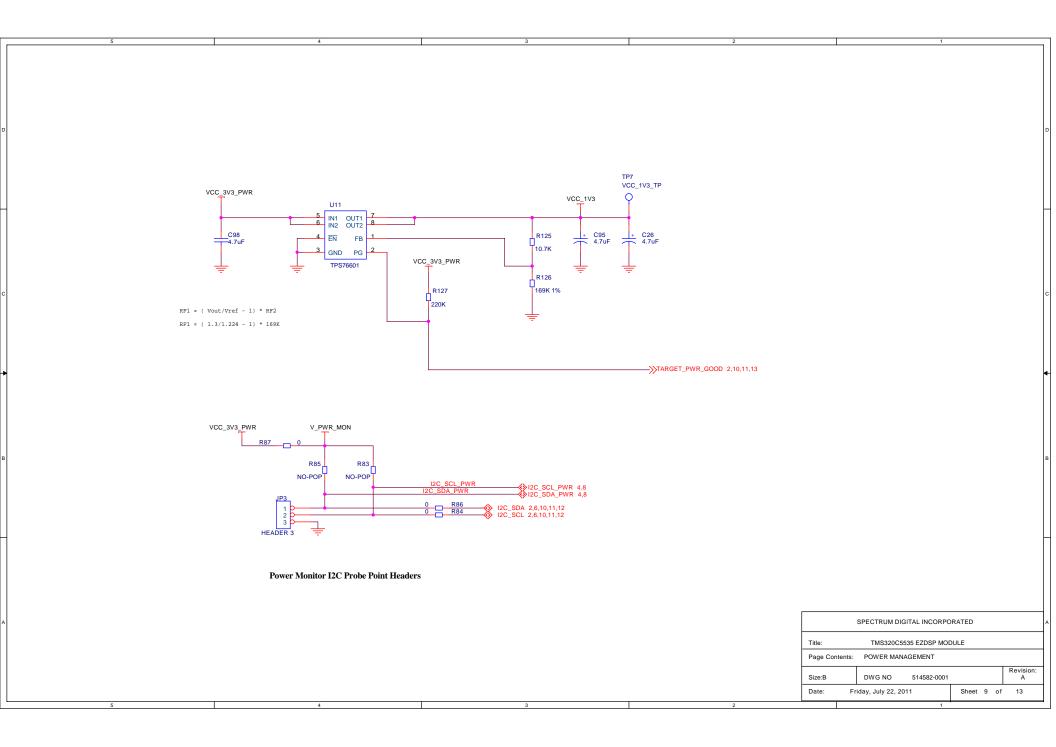


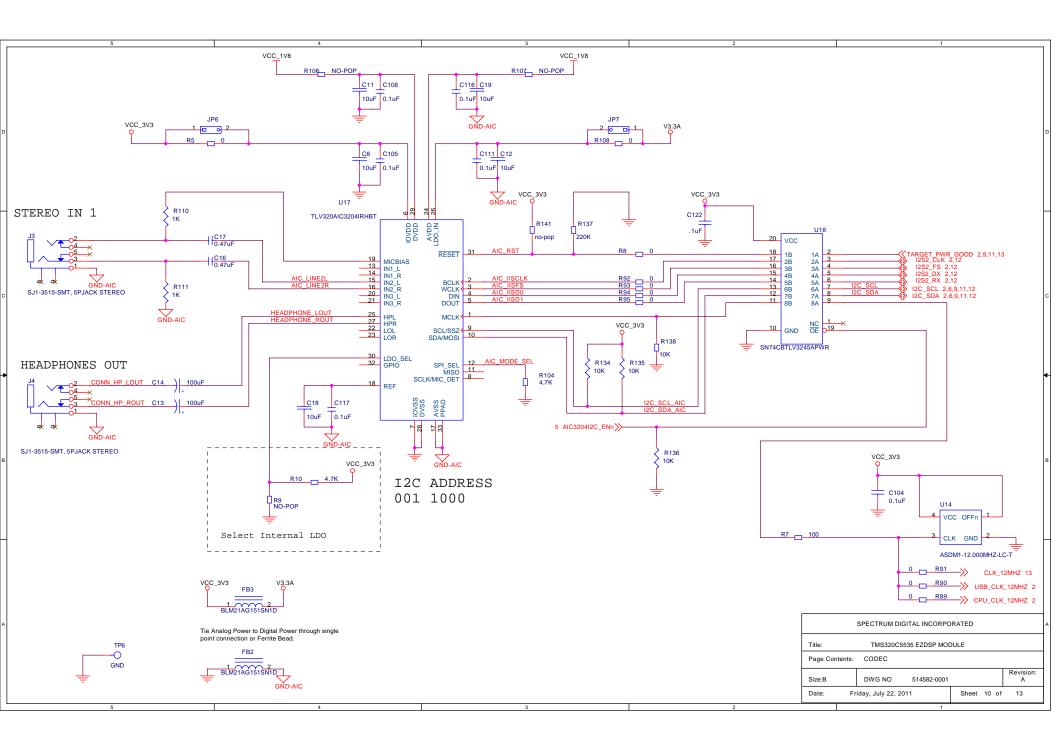


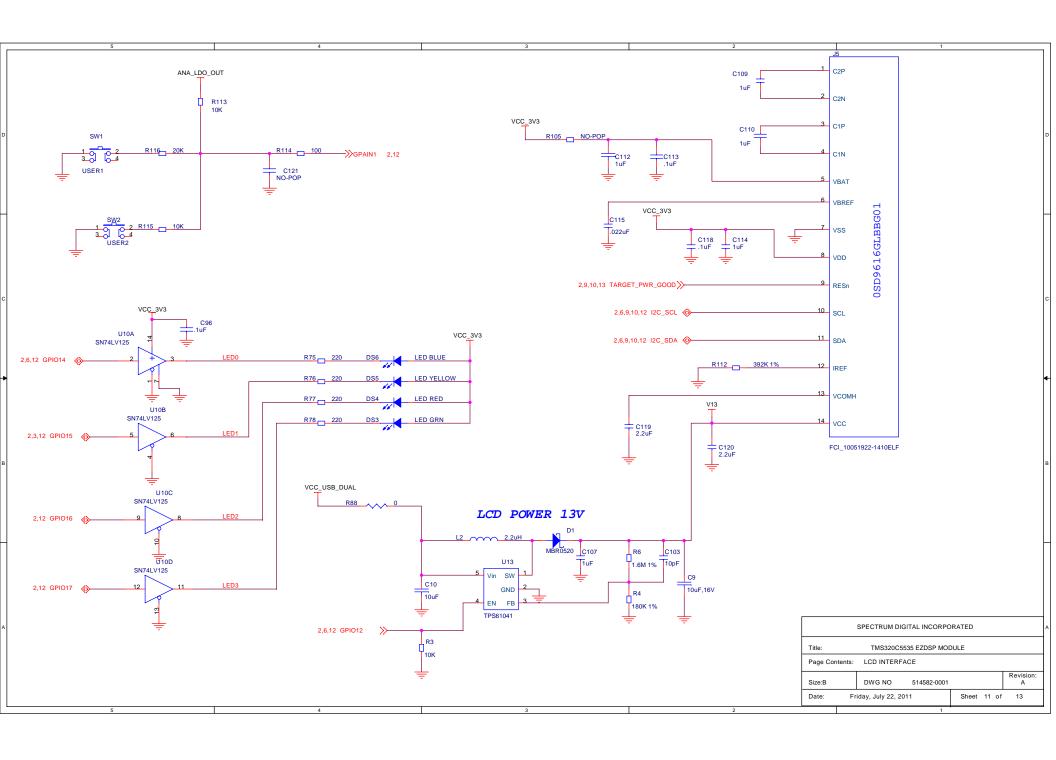


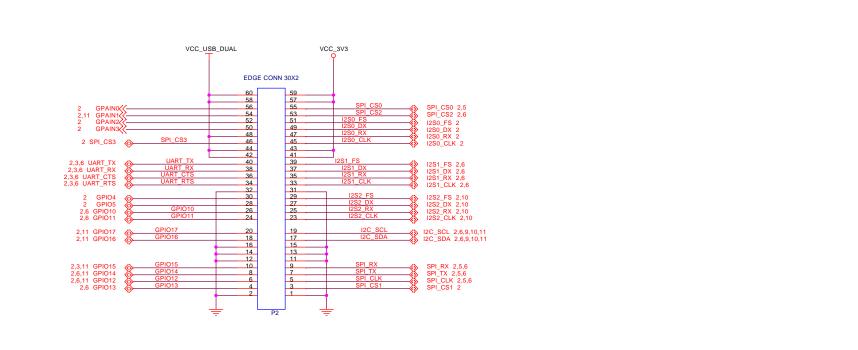












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Title: TMS320C5535 EZDSP MODULE			
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