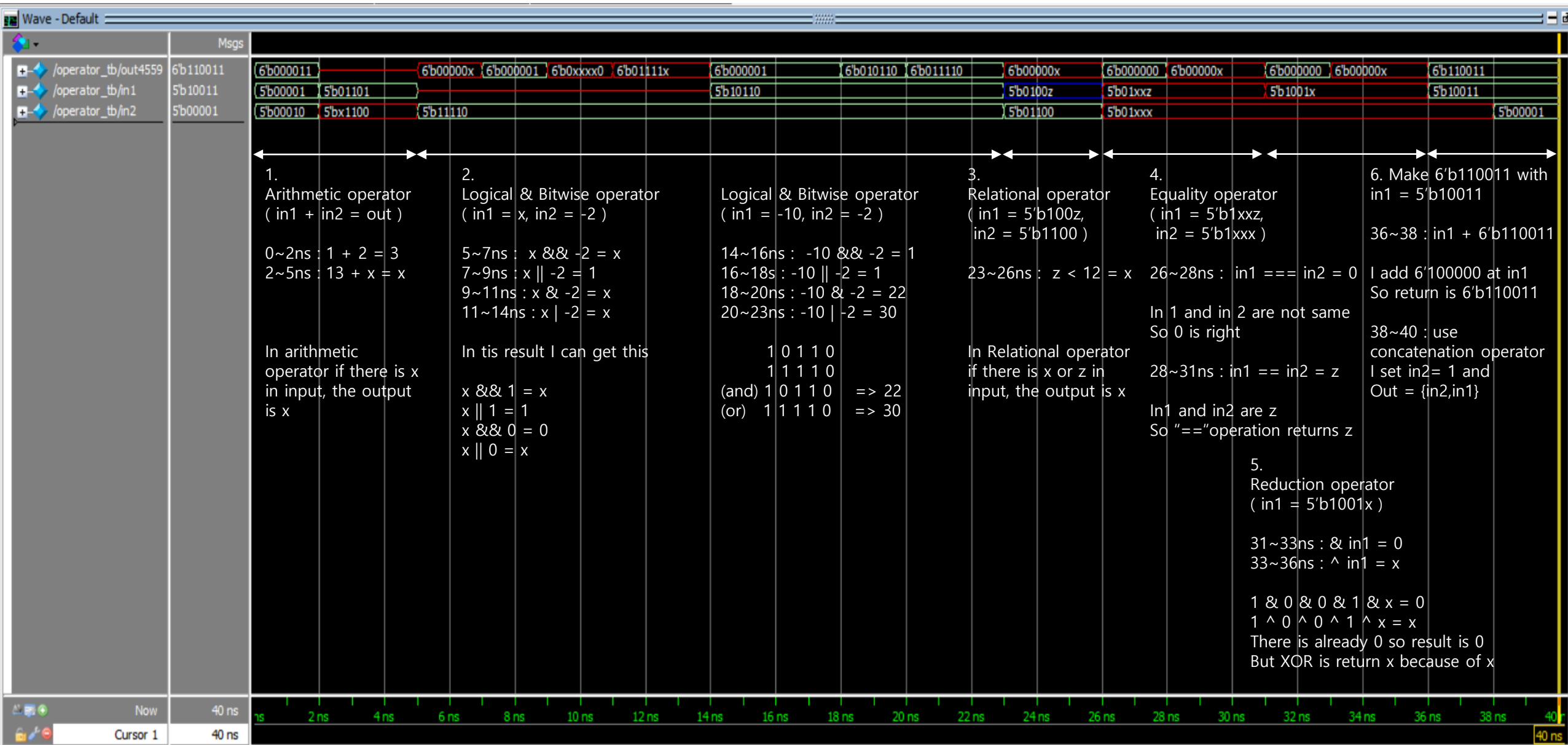


```
1  module operator_tb();
2      reg[5:0] out4559;
3      reg[4:0] in1,in2;
4
5      initial begin
6
7          //1
8          in1 = 1; in2 = 2; assign out4559=in1+in2;
9          #2 in1= 5'b1101; in2= 5'bx1100;
10
11         //2
12         #3 in1= 5'bx; in2= -2; assign out4559= in1 && in2;
13         #2 assign out4559= in1 || in2;
14         #2 assign out4559= in1 & in2;
15         #2 assign out4559= in1 | in2;
16
17         #3 in1= -10; in2= -2; assign out4559= in1 && in2;
18         #2 assign out4559= in1 || in2;
19         #2 assign out4559= in1 & in2;
20         #2 assign out4559= in1 | in2;
21
22         //3
23         #3 in1= 5'b100z; in2= 5'b1100; assign out4559= (in1<in2);
24
25         //4
26         #3 in1= 5'b1xxz; in2= 5'b1xxx; assign out4559= (in1 === in2);
27         #2 assign out4559= in1 == in2;
28
29         //5
30         #3 in1= 5'b100z; assign out4559= &in1;
31         #2 assign out4559= ^in1;
32
33         //6
34         #3 in1= 5'b10011; assign out4559 = in1+6'b100000;
35         #2 in2= 1; assign out4559= {in2,in1};
36     end
37
38
39
40 endmodule
```

Waveform results



Waveform results

