

UART_IReconfigureRxFIFO



```
graph LR; A[UART_IReconfigureRxFIFO] --> B[XMC_USIC_CH_RXFIFO_SetSizeTriggerLimit]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'UART_IReconfigureRxFIFO'. The right box is white with a black border and contains the text 'XMC_USIC_CH_RXFIFO_SetSizeTriggerLimit'. A blue arrow points from the right side of the left box to the left side of the right box.

XMC_USIC_CH_RXFIFO
_SetSizeTriggerLimit