

UART_SetRXFIFOTriggerLimit



```
graph LR; A[UART_SetRXFIFOTriggerLimit] --> B[XMC_USIC_CH_RXFIFO_SetSizeTriggerLimit]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'UART_SetRXFIFOTriggerLimit'. The right box is white and contains the text 'XMC_USIC_CH_RXFIFO_SetSizeTriggerLimit'. A blue arrow points from the right side of the gray box to the left side of the white box.

XMC_USIC_CH_RXFIFO
_SetSizeTriggerLimit