

Head Out Protection

Level 1

BUR Level

F OFF

R

User configuration option byte

- ☐ IWDG_STOP
 ☐ IWDG_STDBY
 ☐ nBoot0
 ☐ nBOOT0
- ☐ WWDG_SW
 ☐ IWDG_ULP
 ☐ nBoot1
 ☐ BOOT1
- ☐ nSRAM_Parity
 ☐ FZ_IWDG_STOP
 ☐ nDBOOT
 ☐ BFB2
- ☐ SRAM2_RST
 ☐ FZ_IWDG_STDBY
 ☐ nDBANK
 ☐ nBOOT_SE
- ☐ SRAM2_PE
 ☐ PCROP_RDP
 ☐ DB1M
 ☐ DUALBANK
- ☐ nRST_SHDW
 ☐ nBoot0_SW_Cfg
 ☐ IRHEN
 ☐ BOREN
- ☒ nRST_STOP
 ☐ nSWBOOT0
 ☒ WDG_SW
- ☒ nRST_STDBY
 ☐ VDDA_Monitor
 ☐ SDADC12_VDD_Monitor

NRST_MODE

Security option bytes

SEC_SIZE

0x00

SEC_SIZE2

0x00

☐ BOOT_LOCK

Boot address option bytes

BOOT_ADD0 (H)

Boot from (H)

BOOT_ADD1 (H)

Boot from (H)

User data storage option bytes

Data 0 (H)

Data 1 (H)

Flash sectors protection

Flash protection mode:

Read/Write protection

| Page | Start address | Size | Protection |
|---|---------------|---------|-----------------------|
| <input type="checkbox"/> Page 29 | 0x08000E80 | 0.12... | No Protection |
| <input type="checkbox"/> Page 30 | 0x08000F00 | 0.12... | No Protection |
| <input type="checkbox"/> Page 31 | 0x08000F80 | 0.12... | No Protection |
| <input checked="" type="checkbox"/> Page 32 | 0x08001000 | 0.12... | Read/Write Protection |
| <input checked="" type="checkbox"/> Page 33 | 0x08001080 | 0.12... | Read/Write Protection |
| <input checked="" type="checkbox"/> Page 34 | 0x08001100 | 0.12... | Read/Write Protection |
| <input checked="" type="checkbox"/> Page 35 | 0x08001180 | 0.12... | Read/Write Protection |
| <input checked="" type="checkbox"/> Page 36 | 0x08001200 | 0.12... | Read/Write Protection |
| <input checked="" type="checkbox"/> Page 37 | 0x08001280 | 0.12... | Read/Write Protection |

Unselect all

Select all