1 1	Table of Contents		Rev	Description		lons &
	Title		X1	Initial D		
	Block Diagram		A	Release t		action
	KL25Z MCU OpenSDA INTERFACE			11010000	o produ	
			В	Prototype	redefi	inition.
5 :	I/O Headers and Power Supply			KL25 XTAI	capaci	itors
				C16 & C19	change	ed
				from 10pF 22pF popu	DNP to)
				Defined A	.5 signa	al from
D				K20 MCU		
			С	Prototype	releas	se.
			"	Fixed J10	orient	cation
				according	to Arc	luino R3
				specifica		
			CX1	Prototype		
				Changed to DNP bo	wo pin	headers
				headers.		
				Added sup AT45DB161	port to	٥
				or AT45DB161	D-S 161E-SS	SHD
Н				SPI Flash	Memory	у.
				Added 2pi for on-bo	n heade	er (J11)
				programmi	ng isol	lation.
				Redefined	KL25 p	power
				netnames.	ISB conr	nectors
			1	Changed U	ers J5	and J7
			1	Changed F	L25 XTA	AL
			1	Changed		
			1	X-FRÉEDOM name to F	I-KL25Z	board
с			D	Pilot rel	ease.	
				Changed 2	pin hea	ader (J11)
			1	location SWD CLK i		-board MCI
				Changed t	o DNP	
				non-produ BT1, J1,	ction E	30M parts
				J9. J10.	TP6. U.5	5. J8
				Removed C leverage	penSDA	
		FREEDOM KL25Z		leverage legend	hint vo	oltage
		TREEDOTTREESE	DX1	Productio	n Re-sr	oin
			DAI			
•				Fixing V. changing	drop i	in ADC by
				R80, and	3.0V ze	enner (D9)
				R80, and also char rectifier	ged BAT	254C
				for MBR12	S (DI, OVLSET1	DZ, D5) 1G diodes
				(D6, D7,	D8, D10	O, D11,
				D12, D13 VF curve	to enha	ince
				capabilit	V.	
				Added J20	header	: to bypas
				D12 vdrop Changed J	4 and J	J3
				to no bot	tom-sho	orting
				headers a	nd adde iinstea	ed R/3 & ad, along
В				with R81	for cur	rent
				measuring Added USE		l.
			1	functiona	lity wi	ithout
			1	electrica by placin	I prote	ection and Re?
			1	options.		
			1	Updated F	L25 sym	abol
			1	TPM to F1	rı ıunct	.1011
			E	Productio		pin
			1	release.		
Ц				Adding 50	buck-h	oonst
			1	Adding 5V VR suppor	t by 1x	x3 pin
			1	header J2 10uF capa	2 and C	C26-29
			L-			
			F	Changing from luF	to 0.1:	ıF
			1	to fix yi	eld iss	sue
			1			
			1			
			L			
					T -	
^					Autor	motive, Indu: et Solutions
			free	scale	6501 Wil	et Solutions illiam Cannon Drive
					1 -30. 7811	
		Designer: RAFAELD	ri pr	Drawing Little:		tion: FCP:
1					FRDM-	-KL25Z
		Orawn by: RAFAEL D	EL REY	Page Title:	TITLE PA	AGE
Л		Approved. MICHAELI	JORMAN		ent Number	SCH-27556 PD
1		MICHAEL	-UT LIMITARY		y, June 18, 20	
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	5	4 3 2				

Revisions & Change Log

05/03/12

05/09/12

05/21/12

06/29/12

07/10/12

01/22/13

01/30/13

06/18/13

Automotive, Industrial & Multi-Market Solutions Group

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Sheet 1 of

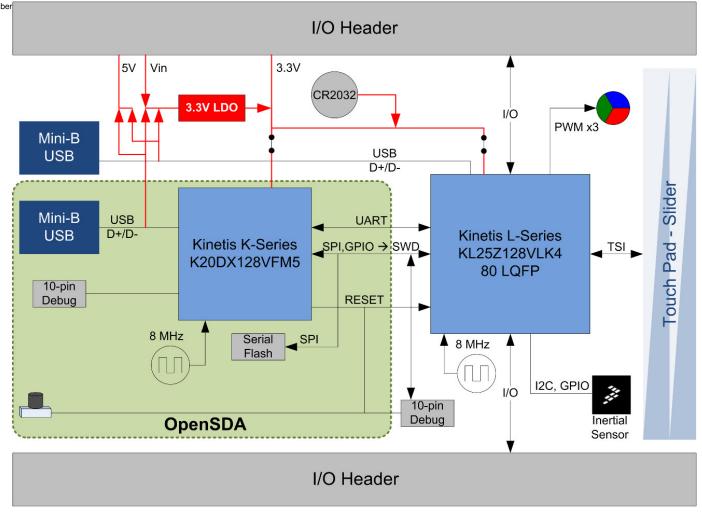
Approved

M. NORMAN

- 1. Unless Otherwise Specified:
 - All resistors are in ohms, 5%, 1/8 Watt All capacitors are in uF, 20%, 50V

All voltages are DC

- All polarized capacitors are aluminum electrolytic
- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



Indicates optional items that will not be populated by default

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	lassification:	FCP:	FIUO:	PUBI	X					
Drawing		DM-KL2	5Z							
Page Title: BLOCK DIAGRAM										
Size C							Rev F			
Date:	Tuesday, Jun	e 18, 2013	Sheet	2	of	5				

