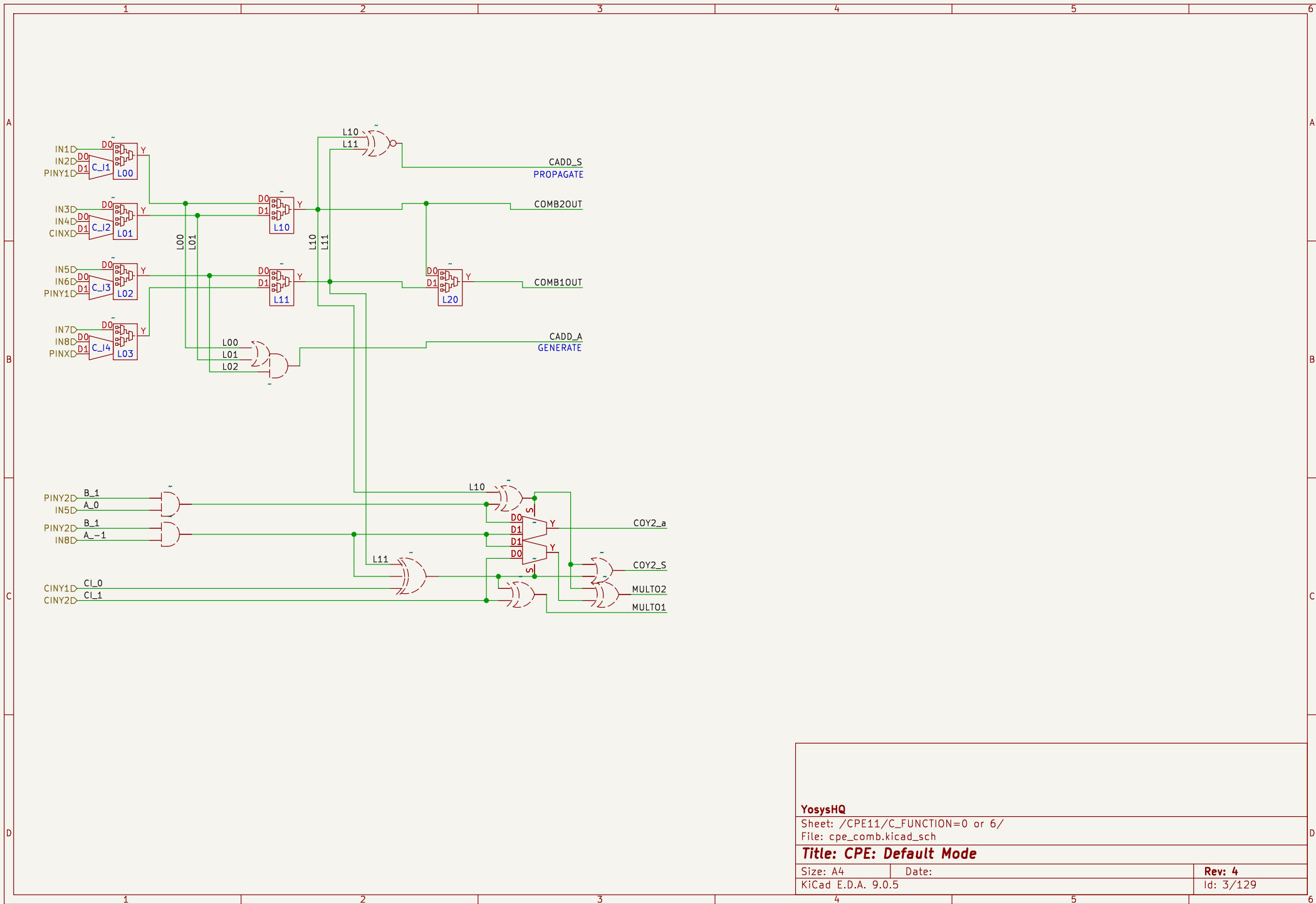
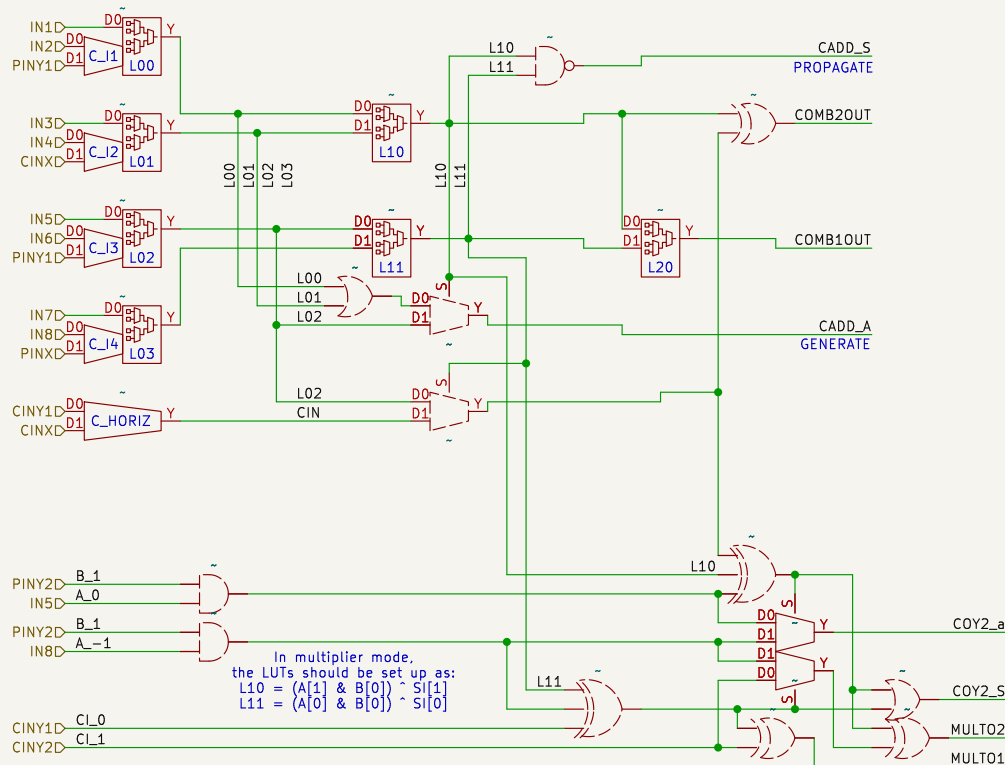


YosysHQ			
Sheet: /			
File: prjpeppercorn.kicad_sch			
Title:			
Size: A1		Date:	Rev: 4
KiCad E.D.A. 9.0.5			Id: 1/129



Id: 4/129



C_FUNCTION = 2: ADDF2

This provides a 2-bit full adder.

The addition is performed in propagate/generate style.

Setting C_HORIZ configures the adder to propagate carries horizontally, rather than vertically. This feature isn't implemented in p_r or nextpnr because horizontal carries have twice the propagation delay of vertical carries.

C_FUNCTION = 3: MULT

This provides a 2x2-bit Booth multiplier.

The dedicated logic performs multiplication by B[1], while multiplication by B[0] is performed within the soft logic.

(These functions differ by how they set up the downstream carry and propagation lines; from this part of the logic they function identically, thus have been grouped together)

YosysHQ

Sheet: /CPE11/C_FUNCTION=2 or 3/

File: cpe_comb_addf2.kicad_sch

Title: CPE: 2-bit Adder/Multiplier Mode

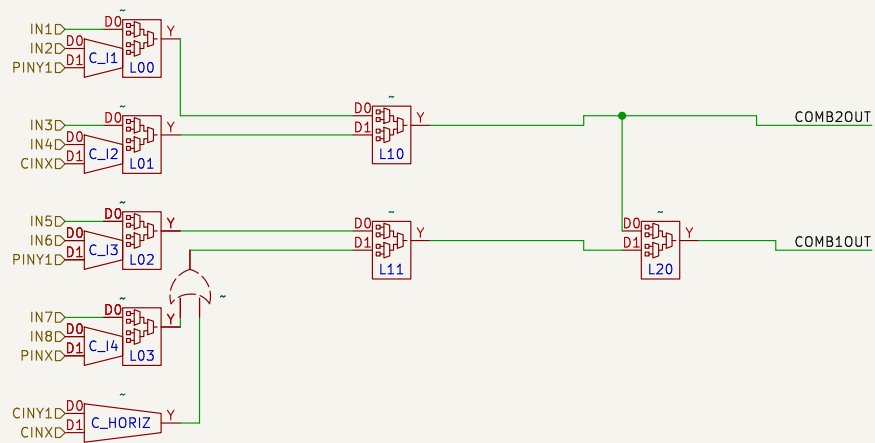
Size: A4

Date:

Rev: 4

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Id: 5/129



C_FUNCTION = 5: EN_CIN

This is one of the more obscure CPE configurations.

The intended usecase of this is to source a signal from the carry lines to reduce routing delay.

YosysHQ

Sheet: /CPE11/C_FUNCTION=5/
File: cpe_comb_en_cin.kicad_sch

Title: CPE: Sum Input With Carry

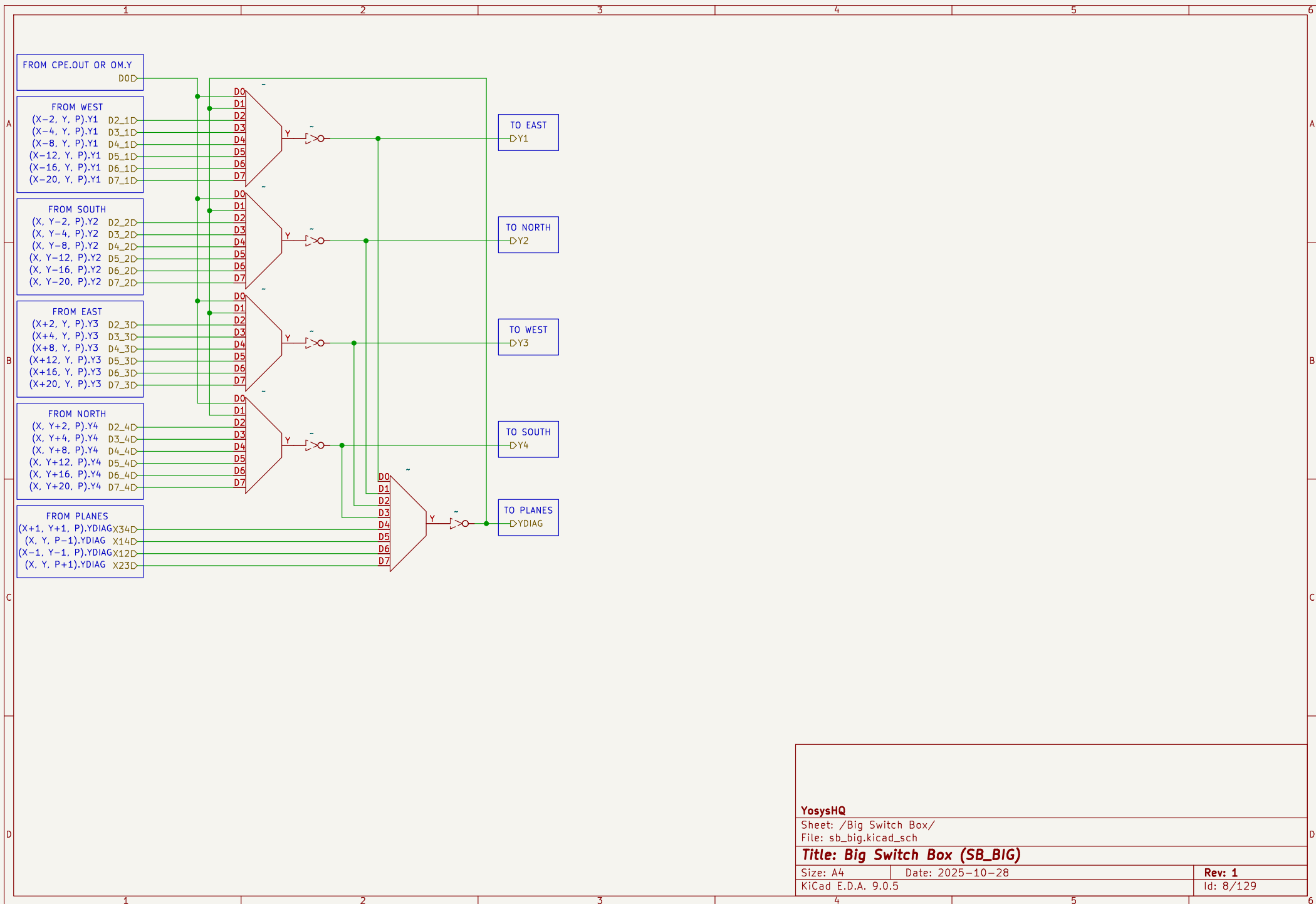
Size: A4

Date:

Rev: 4

KiCad E.D.A. 9.0.5

Id: 7/129



YosysHQ

Sheet: /Big Switch Box/

File: sb_big.kicad_sch

Title: Big Switch Box (SB_BIG)

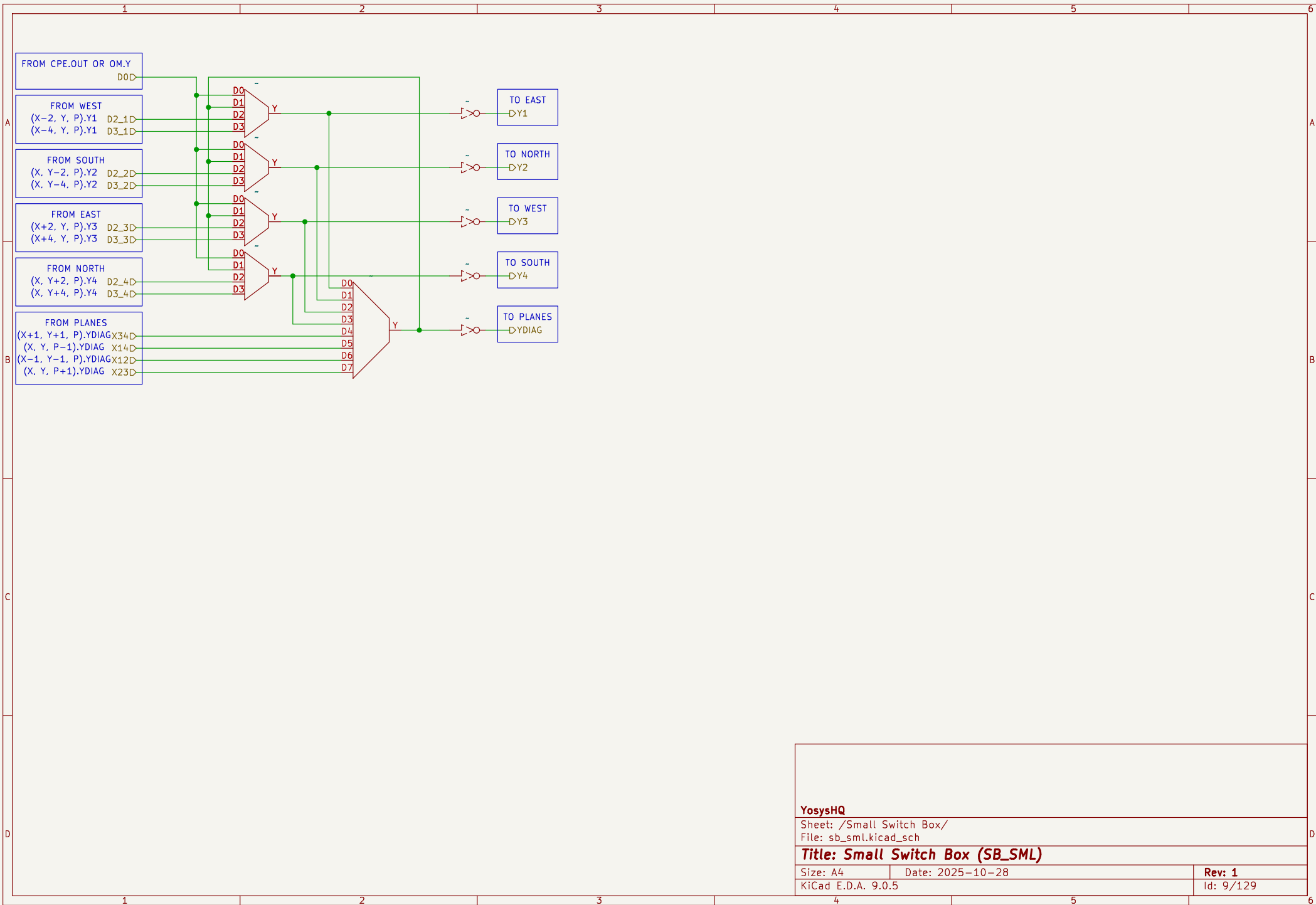
Size: A4

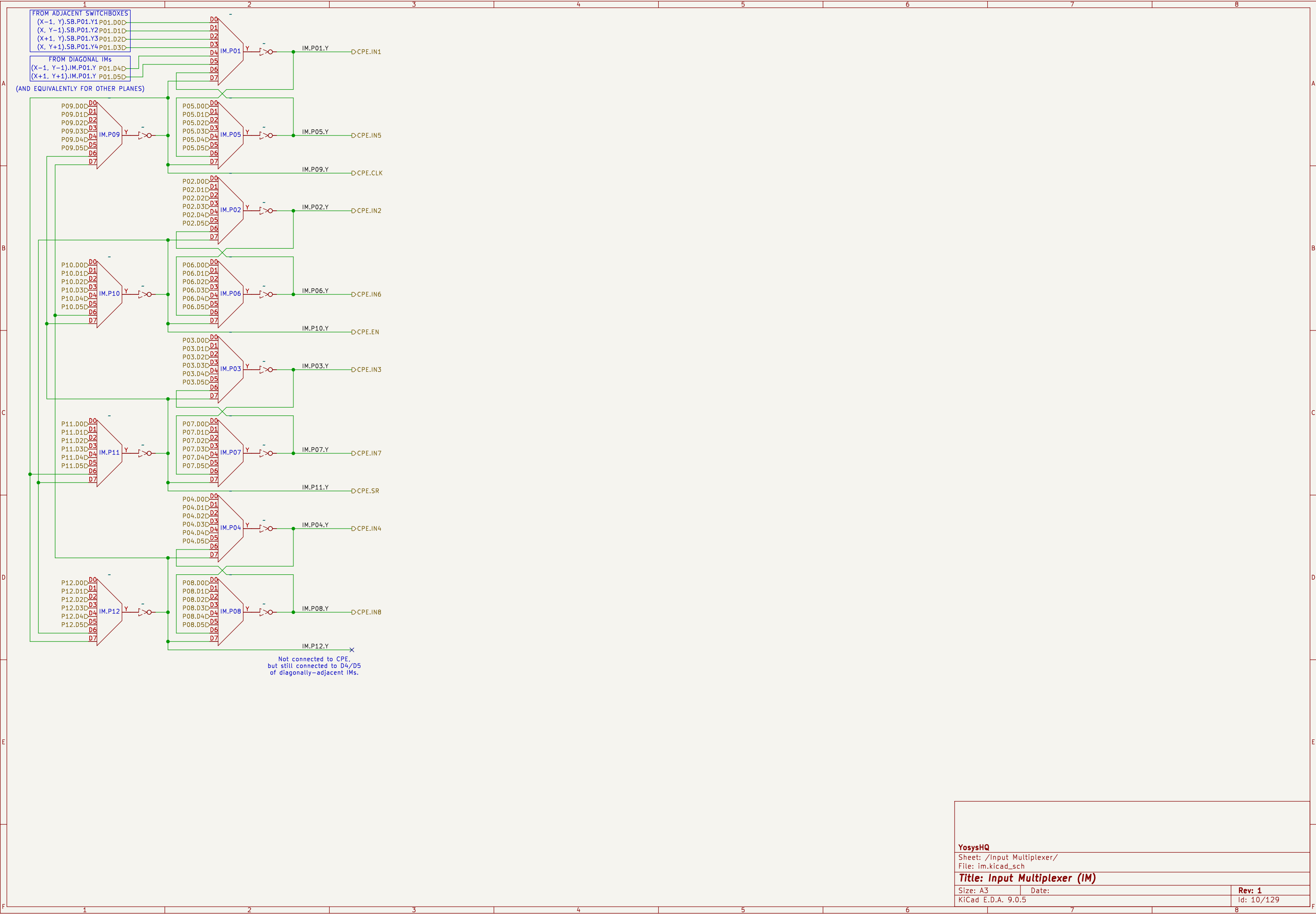
Date: 2025-10-28

Rev: 1

KiCad E.D.A. 9.0.5

Id: 8/129





YosysHQ

Sheet: /Input Multiplexer/
File: im.kicad_sch

Title: Input Multiplexer (IM)

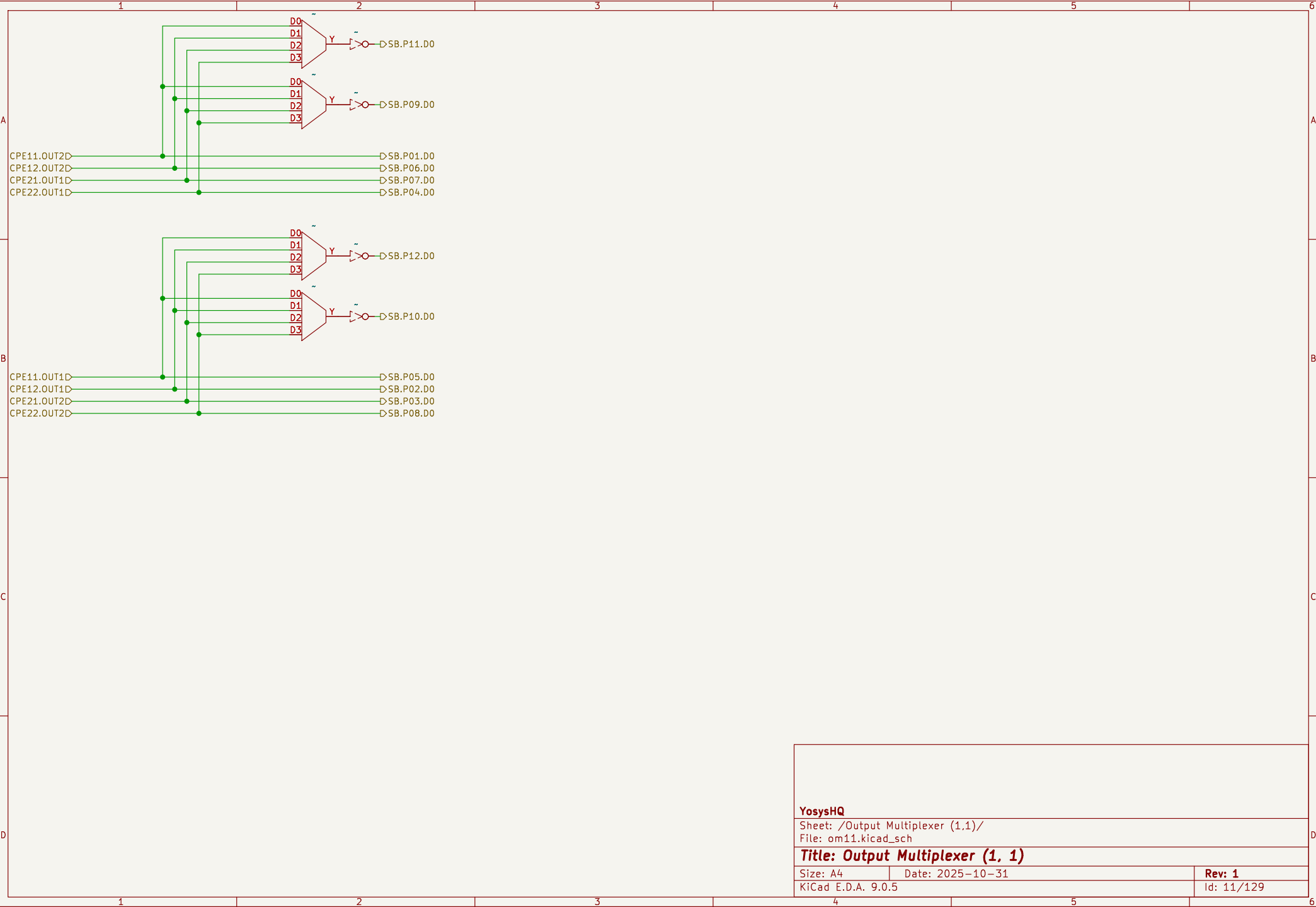
Size: A3

Date:

Rev: 1

KiCad E.D.A. 9.0.5

Id: 10/129



YosysHQ

Sheet: /Output Multiplexer (1,1)/
File: om11.kicad_sch

Title: Output Multiplexer (1, 1)

Size: A4
KiCad E.D.A. 9.0.5

Date: 2025-10-31

Rev: 1

Id: 11/129

