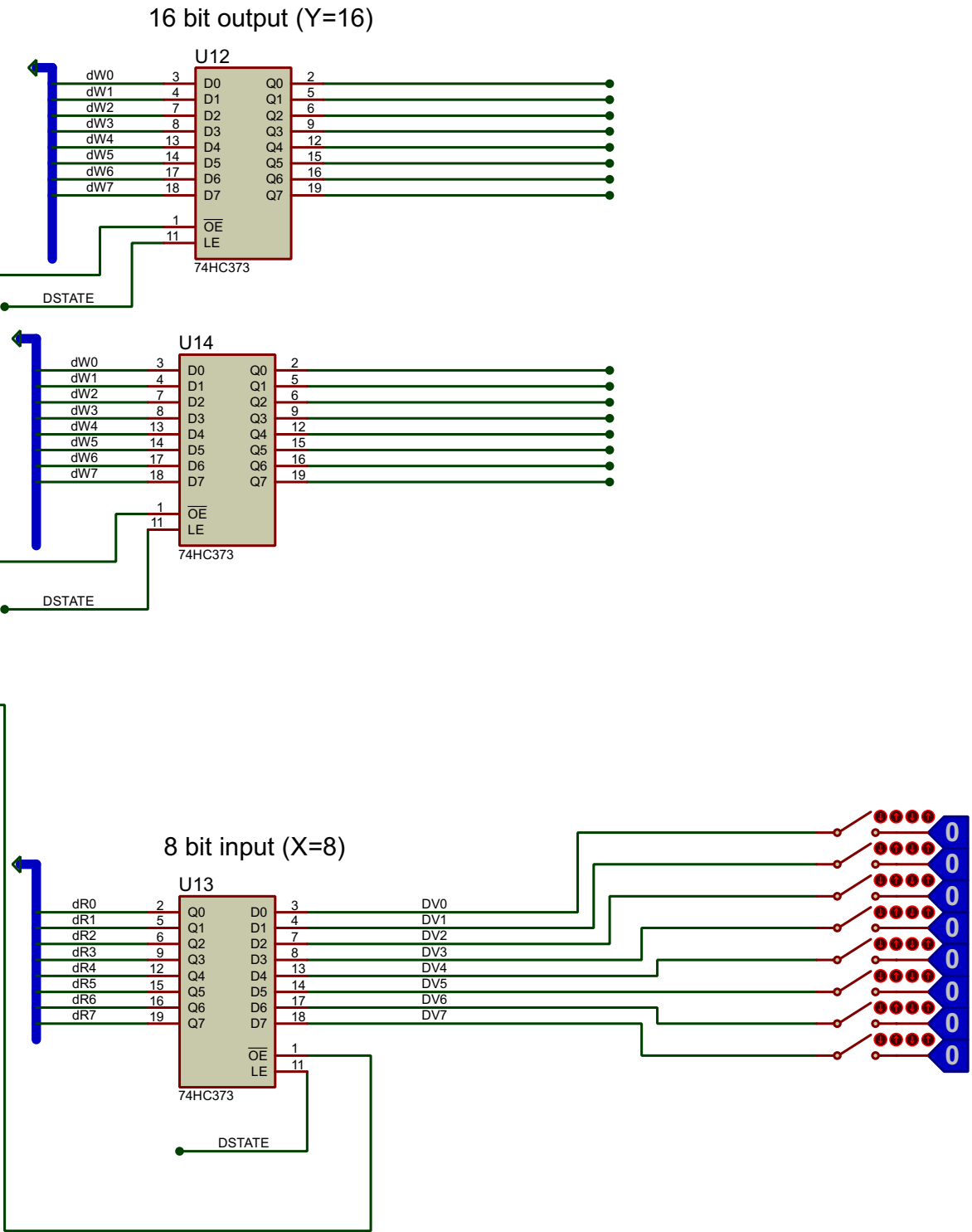
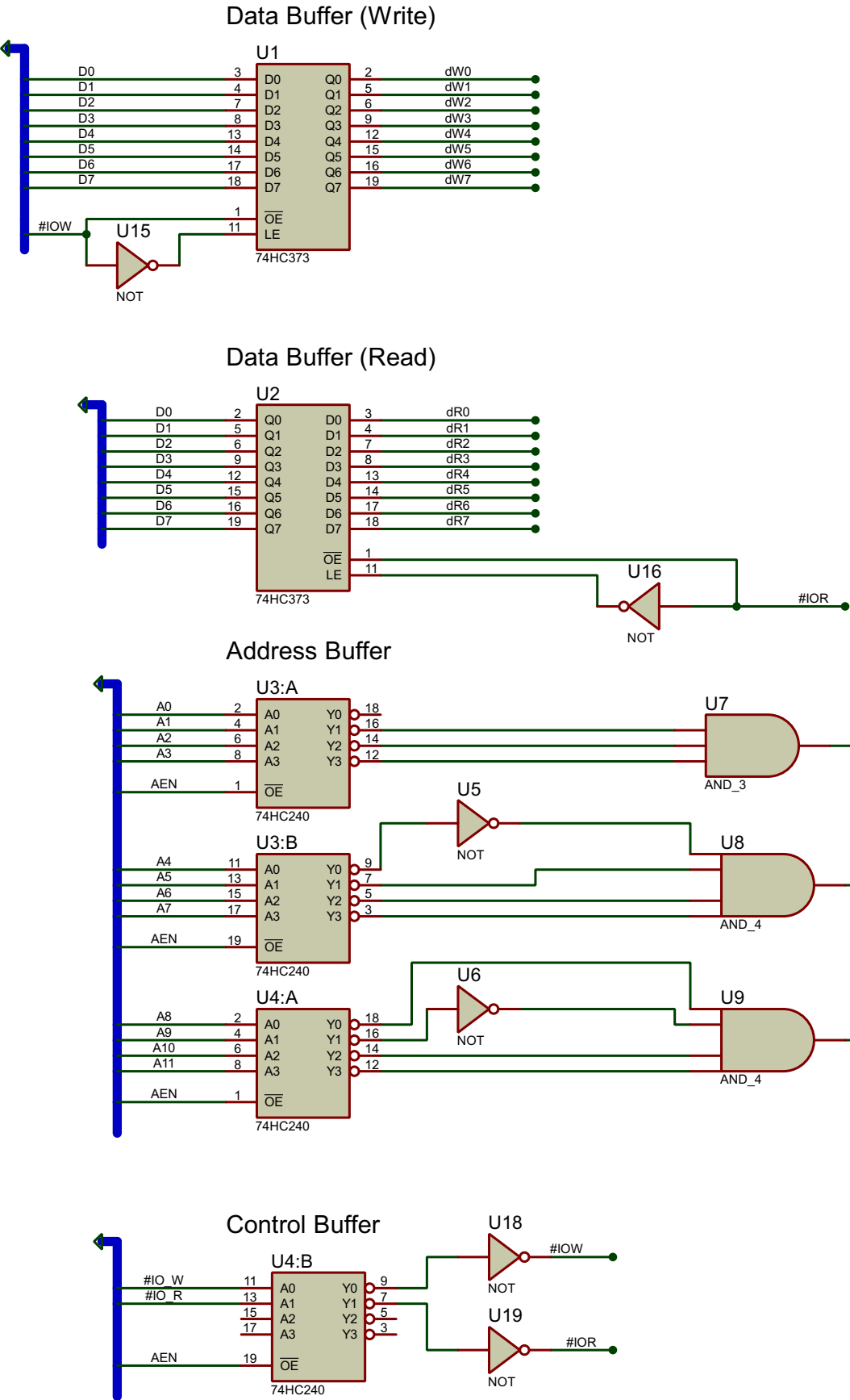
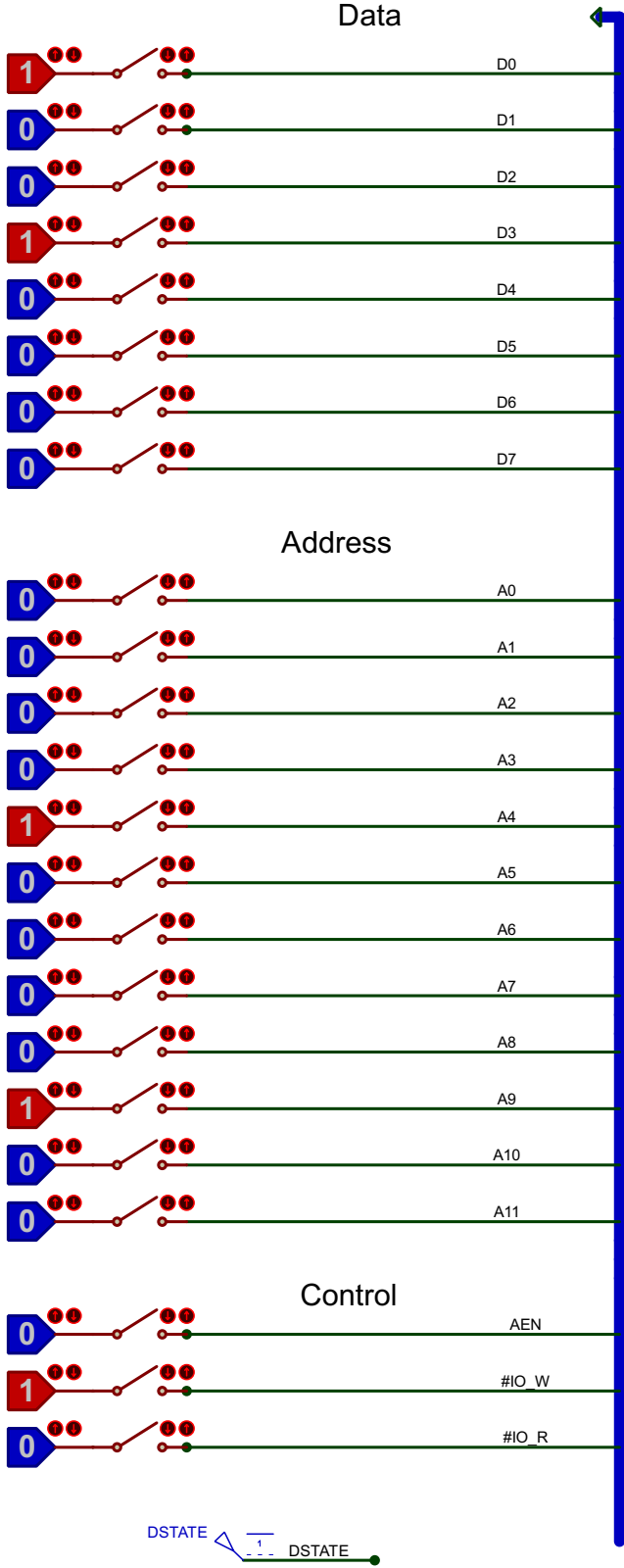
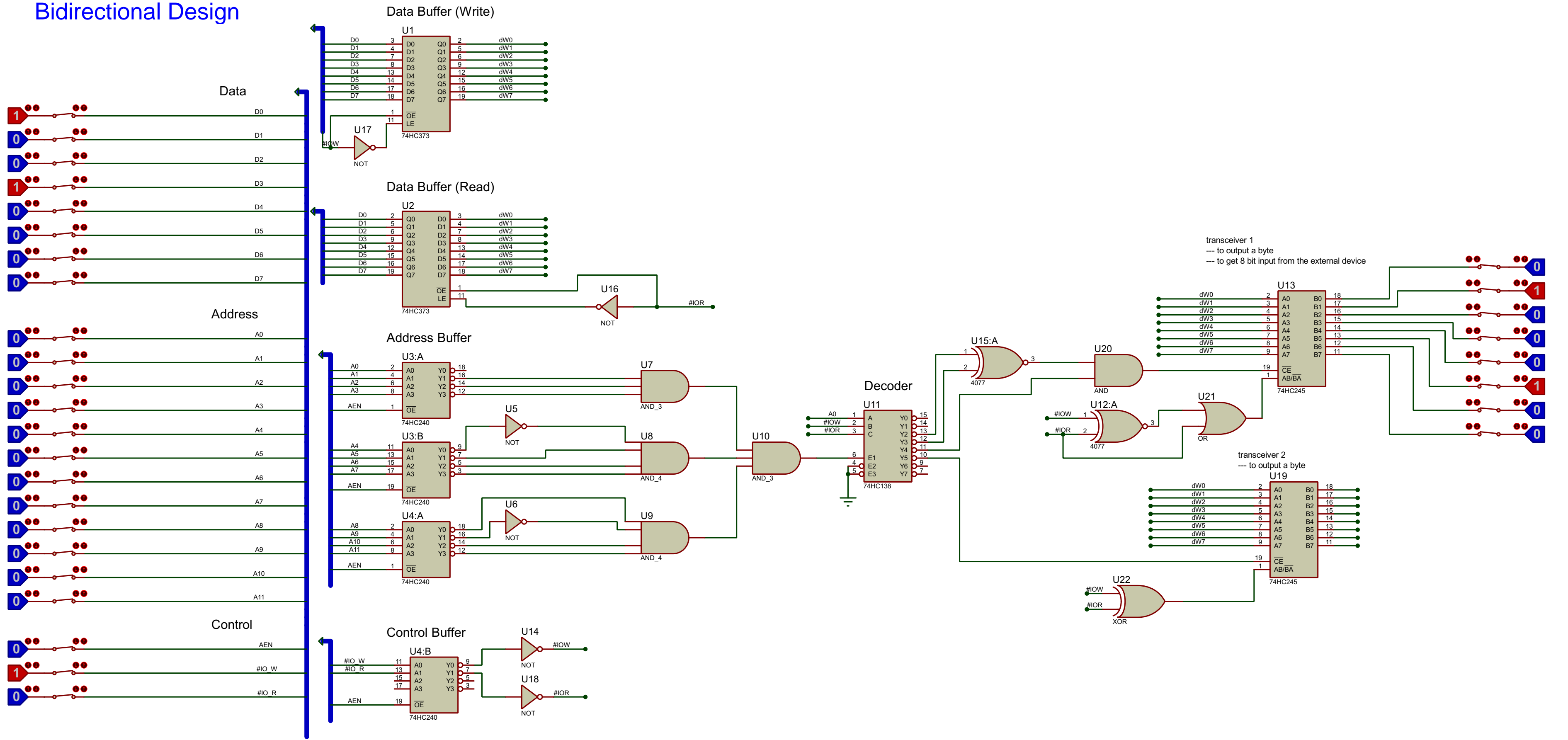


24-bit Design



Bidirectional Design



Logic states and switches are connected only for simulation purpose