**David Asare**

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**Professional Summary:**

Dedicated Senior Test Engineer adept at turning product development processes into documented test procedures, executing manual and automated tests, training and presenting test results. I do strive to improve the quality of products by precision testing to achieve better customer experiences.

**Areas of Expertise:**

* Architecting BIOS and firmware test plans to meet and exceed customer product requirements.
* Collaborating with engineering and marketing to help improve functional product designs.
* Intercepting and exposing product flaws through detailed, focused testing.
* Effective training of test personnel, presenting testing results to non-technical departments.
* Extensive understanding of test processes gained through permanent and contract experience.
* Defect management experience through collaboration with development and operations teams.
* Exposure to test methods including System Development Life Cycle, Agile and Waterfall Testing.
* Hands-on supervisory, presentation and accomplished team-training skills

***TECHNICAL BACKGROUND***

**Fibre Channel and Storage Area Networks:**

* Protocol experience: Fibre Channel, NVMe, PCIe, FCoE, SCSI, iSCSI and Bluetooth specifications.
* Knowledge of Fibre Channel: QLogic, Brocade and Cisco SANs, HBAs, switches, routers and hubs
* Work experience: FC, SANs, Finisar, LeCroy NVMe analyzers and Lauterbach Trace 32 debugger
* Fibre Channel experience: multi-boot BIOS, FC SANs, FC zoning, multi-pathing, fail-over testing
* Storage experience: HP, IBM, EMC, Clariion, NetApp, RAID and JBOD storage systems
* Enterprise Server Experience includes IBM, HP, Intel and Dell Blade servers

**Test Lab Skills:**

* Testing GSM signal interruption of Bluetooth and RF4CE devices using GSM signal generators
* Testing fibre channel BIOS, adapters, fibre channel storage, switches and FC Hubs
* OS experience with Microsoft Windows Server (2012/2008), Red Hat Linux, Ubuntu Linux
* Programming skills: Python, scripting, C/C++, Visual C#, BASIC, linux scripts, Powershell,
* Virtualization and hypervisors: Hyper-V, VirtualBox, VMWare, KVM, Proxmox
* Test applications: JIRA, GIT, Oakgate SVF, Edentree, Medusa, HP ALM, Perforce4,
* Embedded devices, Adaptec and QLogic ASICs, ARM7 SoC, Raspberry Pi, Arduino Mega
* Test set-up, execution and automation, FC SAN switch zoning and LUN masking
* Interests: Machine Leaning with TensorFlow, neural networks, cloud computing, Author and Art.

Education

Bachelors of Science – Physics 1985

Southern Illinois University Carbondale, Illinois

**Storage and Technology Experience**

**Contractor** 11/1/2017 – 5/22/2018

*Universal Electronics Inc.* Santa Ana, CA

Tested and did researched on how GSM signals interfere with Bluetooth and RF4CE device transmissions

Evaluated test fixtures and custom procedures for appropriateness specific to device test requirements.

Created standardized document system and structure to backup and record all GSM testing done

**Senior Test Engineer** 5/1/2015 – 11/1/2015

*SolidFire* Boulder, CO

Executed automated continuous integration (CI) tests in scrum team for 24/7 test coverage

Ran automated test scripts with Solidfire’s AT2 and their element named enterprise software

Implemented "Process to Procedure" initiative to document and gather product knowledge

**Test Engineer** (Contractor) 10/1/2014 – 4/1/2015

*HGST*  Santa Ana, CA

Executed NVME power shutdown test scripts on solid state drives using Oakgate SVF software

Identified power issues on NVME adapters and reprogrammed failed PCIE SSD test units

Reproduced and tracked firmware bugs in JIRA in Agile environment with HW/FW developers

Debugged power issues and did trace analysis of the NVMe bus with a LeCroy analyzer

**Senior Test Engineer** (Contractor) 11/1/2013 – 8/1/2014

*NetApp*  Morrisville, NC

Wrote test plans, executed Dell blade server/Netapp storage tests; Created and presented test reports

Tested pre-released Dell servers fibre channel interfaces, FC switches with Netapp enterprise storage

Managed rack mounted servers with HP ALM in High-Availability environment with Perforce 4

Interfaced with OEM vendor to resolve a severe fibre channel issue that I discovered during testing

Created wiki pages to address the test execution process and information sourcing.

**Lab Manager** 10/1/2012 – 8/1/2013

*QLogic*  Aliso Viejo, CA

Supervised the test lab facilities, scheduling tests and managing enterprise storage systems.

Developed test procedures for FibreCache HBAs with embedded Marvell 78460 ARM SoC

Managed enterprise storage units for a group of 16 engineers and mentored testers in test procedures.

Wrote C code for programming the QLogic FibreCache host adapter (SPG manufacturing software)

**BIOS Test Engineer** 11/1/1998 – 11/1/2008

*QLogic*  Aliso Viejo, CA

Prepared and promoted BIOS/FW test validation practices for training Device Verification Test engineers

Validated QLogic BIOS and FW code for 1G/2G/4G/8G FC host bus adapters on legacy BIOS/UEFI systems

Designed and executed functional and reliability test strategy for fibre channel HBA, BIOS and drivers

Read and captured fibre channel traces for troubleshooting with Finisar Xgig fibre channel analyzer

Created integrated single rack test environment for FC/BIOS, SAN and UEFI test validation

**Test Engineer** 8/1/1995 – 10/1/1998

*Adaptec*  Irvine CA

Managed low-cost SCSI host adapter lab, test fixtures, test case development and test automation

Collaborated with engineers and project managers regarding test report design for client projects

Generated testing plans for new products and components to reduced costs by catching errors early