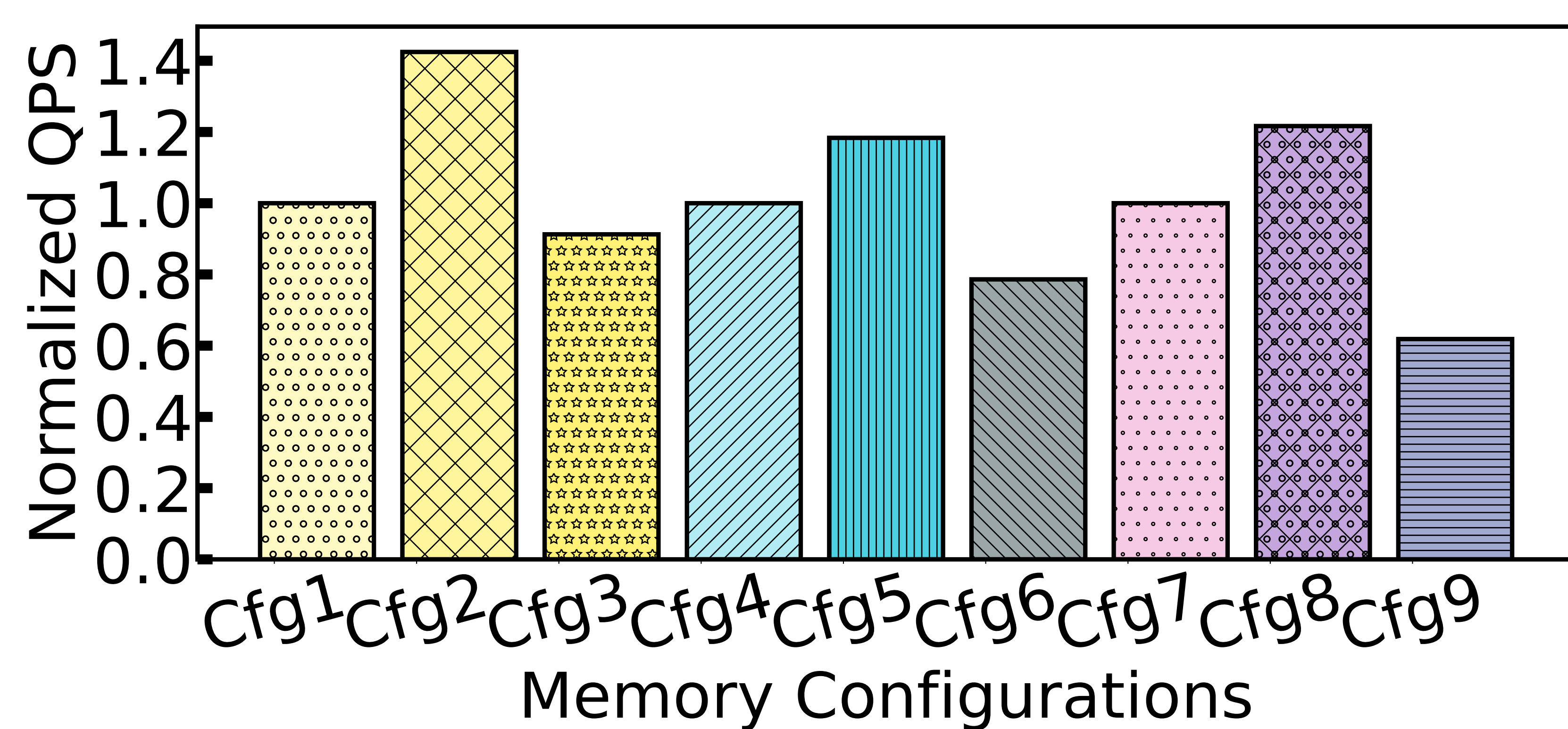
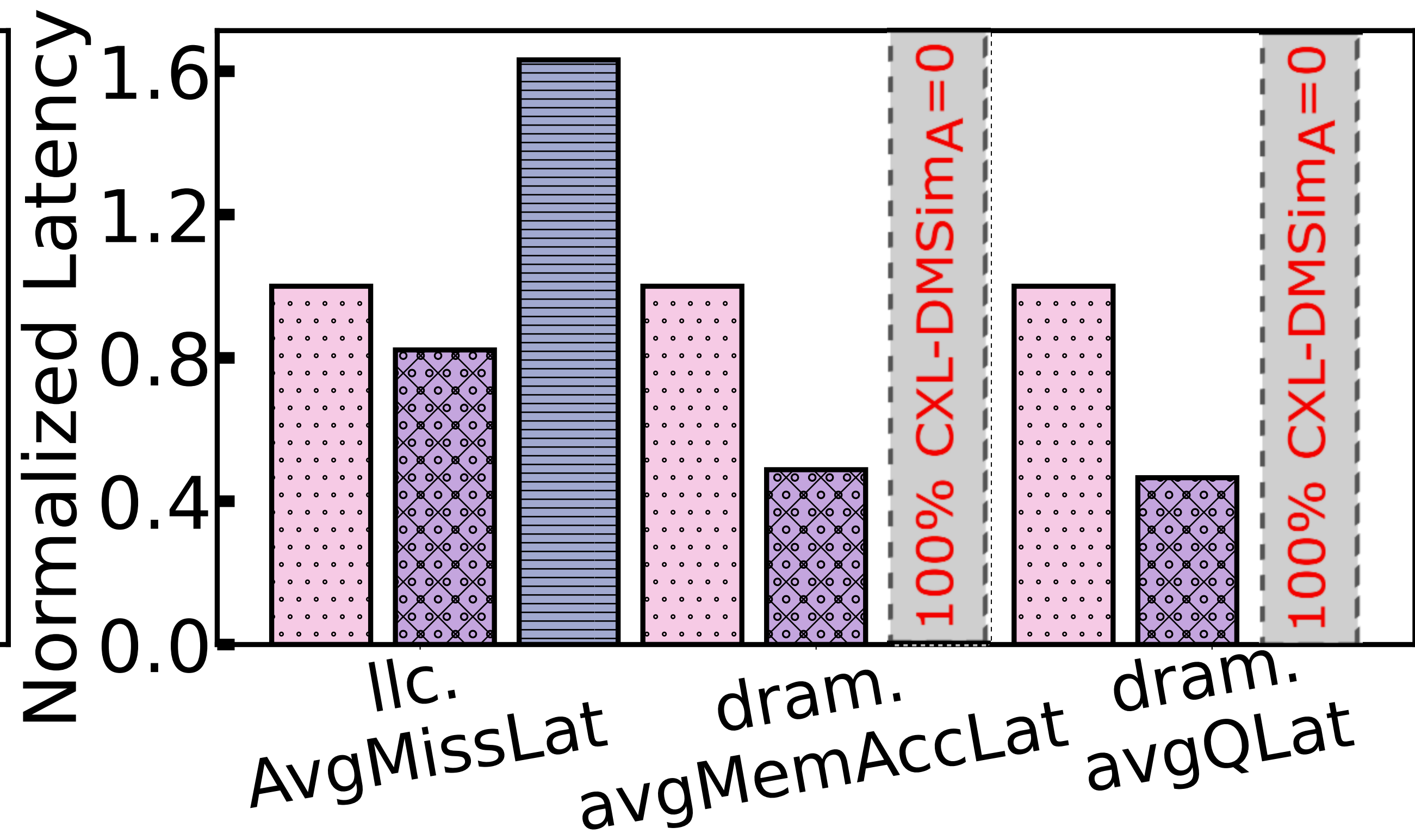


DDR-L:DDR-R= Cfg1. 100%:0% Cfg2. 50%:50% Cfg3. 0%:100%
 DDR-L:CXL-ASIC= Cfg4. 100%:0% Cfg5. 50%:50% Cfg6. 0%:100%
 CXL-DMSim_L:CXL-DMSim_A= Cfg7. 100%:0% Cfg8. 50%:50% Cfg9. 0%:100%



(a) Application execution QPS



(b) Latency metrics