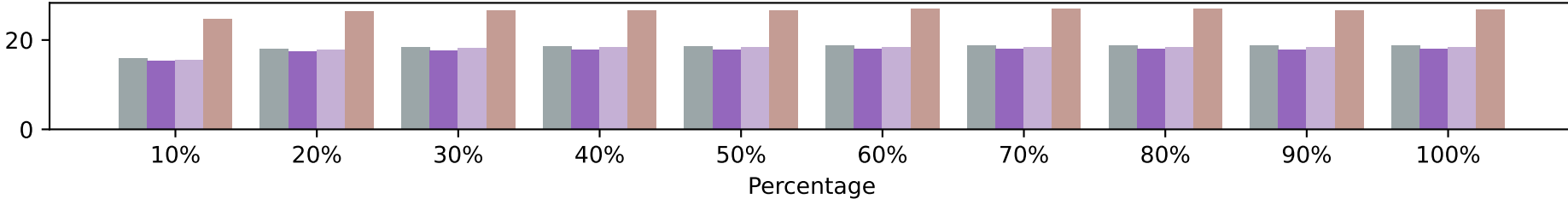


Edges/Sec for K0, K1, K2, K3 at Different Percentages in Intel MBA(CXL)

Edges/Sec



K0 Phase



K1 Phase



K2 Phase



K3 Phase