

DDR-L:DDR-R=100%:0%
DDR-L:DDR-R=50%:50%
DDR-L:DDR-R=0%:100%

DDR-L:CXL-ASIC=100%:0%
DDR-L:CXL-ASIC=50%:50%
DDR-L:CXL-ASIC=0%:100%

CXL-DMSim_L:CXL-DMSim_A=100%:0%
CXL-DMSim_L:CXL-DMSim_A=50%:50%
CXL-DMSim_L:CXL-DMSim_A=0%:100%

