

Google

Google

Software

Hardware Engineering Intern

Company overview:

The area: Intern - Technical

Google is and always will be an engineering company. We hire people with a broad set of technical skills who are ready for some of technology's greatest challenges and make an impact on millions, if not billions, of users. At Google, engineers not only revolutionize search, they routinely work on massive scalability and storage solutions, large-scale applications and entirely new platforms for developers around the world. From Chrome, Android to YouTube, Social to Local, Google engineers are changing the world one technological achievement after another.

Additional Role Description:

We have 3 months long Internships available which will start and end in the Summer 2021 timeframe between May and September.

As a Hardware Engineering intern, you will design, develop and deploy next generation consumer hardware. Google's Consumer Hardware Silicon division (gChips) builds chips (SoCs and ASICs) optimized for Google-branded consumer devices. Our product areas include imaging, machine learning, video, and security. We aim to build a team with great talent resonating Google's culture of innovation and fun and you have an opportunity to be part of it.

As a member of a fast-paced multi-disciplinary team, you will use your creativity and diverse range of engineering experience to explore solutions to a variety of engineering problems. Additionally, as an Electrical Engineer, you will participate in the design, analysis, and prototyping of new concepts.

Responsibilities:

- **Work with the team to develop power and performance optimized chips**
- **Contribute to the design, verification and silicon implementation of chips**
- **Work on design concepts around CPUs, image processing, machine learning, computer vision, security and video**
- **Collaborate with local and remote teams in automating design flows.**

Minimum qualifications:

- **BA/BS in Electrical Engineering or a related field.**
- **Relevant Internship work, work experience, or personal project experience in Hardware or Electrical Engineering.**
- **Basic design concepts and computer hardware architecture**

Preferred qualifications:

- Experience in one or more of the following areas: SoC/ASIC Design, Design Verification, Physical Design, Design for Testability.
- Experience with Verilog/HDL or System Verilog coding.
- Experience with one of the scripting languages - PERL, TCL or Python
- Experience with EDA tools and methodology like Lint, CDC, Synthesis, Formal Equivalence or low Power reduction techniques.

Program	AE	BSBE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	HSS
BT-BS	No	No	No	No	No	Yes	No	No	No	No	No	No	No	--	--	--

Eligibility :	MT	No	No	No	No	No	No	No	No	--	--	--	--	--	No	--
	DoubleMajor	No	No	No	No	No	Yes	No	No	No	No	No	No	No	--	--
	dual	No	No	No	No	No	Yes	No	No	No	No	No	No	No	--	--
	dualB	No	No	No	No	--	--	--	No	No	No	No	No	No	--	No
	dualC	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--
	Mdes	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--
	MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--
	Phd	No	No	No	No	No	No	No	No	No	No	No	No	--	--	No
	Msc	--	--	--	--	--	--	--	--	No	No	No	No	--	--	--
	MSR	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

Stipend per month : **90000**

Other Facilities Offered : **We will provide for commute to work, air travel for internship, accommodation and other onsite perks like meals, gym facility etc.**

Bond : **False**

CPI CutOff : **0.0**

Medical Requirments :

Resume Shortlist : **True**

Resume Shortlist Criteria: **N/A**

Aptitude Test: **False**

Group Discussion: **False**

Technical Test: **False**

Technical Interview: **True**

Technical Interview Duration: **N/A**

Number of Techincal Interview Rounds: **2**

HR Interview: **False**

Additional Information: