Company:
Company
Name:

Nature Of
Business:

ANALOG DEVICES

ANALOG DEVICES

Semiconductor

**Designation**:
Tentative Job

**Description:** 

Location:

Bengaluru

**Digital Design Intern** 

Analog Devices is a global leader in the design and manufacturing of integrated circuits to help solve the toughest engineering challenges. Analog Devices enables our customers to interpret the world around us by intelligently bridging the physical and digital with unmatched technologies that sense, measure and connect. We create innovative solutions to solve design challenges in instrumentation, automation, communications, healthcare, automotive and numerous other industries.

Analog Devices Inc (ADI) is looking for Digital IC Design and Verification Engineers for its Chip Design and development team in Bangalore, India.

The selected students will work on chip development based on ultra-deep submicron semiconductor process technologies. They will be guided and trained by ADI's experienced design and verification engineers.

This will include

- Development of key digital blocks and SoCs.
- Development of optimal micro-architecture and design by analyzing power, performance and area tradeoffs.
- Verification of key digital blocks and SoCs in High Speed signal converter /Communication products.
- Definition of testplan, tests and verification methodology for block and chip-level verification.
- They will use industry leading CAD tools.

## **Essential:**

1.	Knowledge of Verilog
2.	Knowledge of digital circuit design - combination and sequential
3.	Hands on any scripting languages such as perl/phyton
4.	Good Analytical skills
5.	Should be able to work independently and proactive

## Additional Skills being looked for:

1.	System Verilog
2.	Any projects on FPGA/matlab would benefit
3.	Understanding of signal processing/filtering background
4.	Good understanding of ADCs/DACs
<b>5.</b>	Basic understanding of pipe lining, 8085/any-other-assembly language

Program AE BSBE CE CHE CSE EE ES ME MSE PHY CHM MTH ECO DES IME HSS

Eligibility: Mtech / Dual Degree in Electronics Engineering with 8+ CGPA academic records and successfully completing Degree in June 2022

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	MT	No	No	No	No	No	No	No	No	No						No	
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	dual	No	No	No	No	No	Yes	No	No	No	No	No	No	No			
	dualB	No	No	No	No				No	No	No	No	No	No		No	
Eligibilty:	dualC															No	
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	MBA															No	
	Phd	No	No	No	No	No	No	No	No	No	No	No	No	No			No
	Msc										No	No	No	No			
	MSR																

Stipend per month:

50000/month

month :

Relocation :25000

Facilities Offered:

Bond : False CPI CutOff : 0.0

Medical

Requirments Resume True **Shortlist:** Resume **Shortlist** CGPA 8 Criteria: **Aptitude** True Test: **Aptitude 45 MINS** Test **Duration:** Group False **Discussion: Technical** True Test: **Technical 90 MINS** Test **Duration: Technical** True **Interview: Technical** Interview **60 MINS Duration:** Number of **Techincal** 2 Interview **Rounds:** HR False **Interview: Additional Information:**